

3GPP TS 38.212 V15.7.0 (2019-09)

Technical Specification

**3rd Generation Partnership Project;
Technical Specification Group Radio Access Network;
NR;
Multiplexing and channel coding
(Release 15)**



Keywords

3GPP, New Radio, Layer 1

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Foreword

This Technical Specification has been produced by the 3rd Generation Partnership Project (3GPP).

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1 Scope

The present document specifies the coding, multiplexing and mapping to physical channels for 5G NR.

2 References

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

- References are either specific (identified by date of publication, edition number, version number, etc.) or non-specific.
- For a specific reference, subsequent revisions do not apply.
- For a non-specific reference, the latest version applies. In the case of a reference to a 3GPP document (including a GSM document), a non-specific reference implicitly refers to the latest version of that document *in the same Release as the present document*.

- [1] 3GPP TR 21.905: "Vocabulary for 3GPP Specifications".
 - [2] 3GPP TS 38.201: "NR; Physical Layer – General Description"
 - [3] 3GPP TS 38.202: "NR; Services provided by the physical layer"
 - [4] 3GPP TS 38.211: "NR; Physical channels and modulation"
 - [5] 3GPP TS 38.213: "NR; Physical layer procedures for control"
 - [6] 3GPP TS 38.214: "NR; Physical layer procedures for data"
 - [7] 3GPP TS 38.215: "NR; Physical layer measurements"
 - [8] 3GPP TS 38.321: "NR; Medium Access Control (MAC) protocol specification"
 - [9] 3GPP TS 38.331: "NR; Radio Resource Control (RRC) protocol specification"
-

3 Definitions, symbols and abbreviations

3.1 Definitions

For the purposes of the present document, the terms and definitions given in 3GPP TR 21.905 [1] and the following apply. A term defined in the present document takes precedence over the definition of the same term, if any, in 3GPP TR 21.905 [1].

3.2 Symbols

For the purposes of the present document, the following symbols apply:

3.3 Abbreviations

For the purposes of the present document, the abbreviations given in 3GPP TR 21.905 [1] and the following apply. An abbreviation defined in the present document takes precedence over the definition of the same abbreviation, if any, in 3GPP TR 21.905 [1].

BCH	Broadcast channel
CBG	Code block group
CBGTI	Code block group transmission information
CORESET	Control resource set

CQI	Channel quality indicator
CRC	Cyclic redundancy check
CRI	CSI-RS resource indicator
CSI	Channel state information
CSI-RS	CSI reference signal
DAI	Downlink assignment index
DCI	Downlink control information
DL	Downlink
DL-SCH	Downlink shared channel
DMRS	Dedicated demodulation reference signal
HARQ	Hybrid automatic repeat request
HARQ-ACK	Hybrid automatic repeat request acknowledgement
LDPC	Low density parity check
LI	Layer indicator
MCS	Modulation and coding scheme
OFDM	Orthogonal frequency division multiplex
PBCH	Physical broadcast channel
PCH	Paging channel
PDCCH	Physical downlink control channel
PDSCH	Physical downlink shared channel
PMI	Precoding matrix indicator
PRB	Physical resource block
PRACH	Physical random access channel
PTRS	Phase-tracking reference signal
PUCCH	Physical uplink control channel
PUSCH	Physical uplink shared channel
RACH	Random access channel
RI	Rank indicator
RSRP	Reference signal received power
SFN	System frame number
SR	Scheduling request
SRS	Sounding reference signal
SS	Synchronisation signal
SUL	Supplementary uplink
TPC	Transmit power control
TrCH	Transport channel
UCI	Uplink control information
UE	User equipment
UL	Uplink
UL-SCH	Uplink shared channel
VRB	Virtual resource block
ZP CSI-RS	Zero power CSI-RS

4 Mapping to physical channels

4.1 Uplink

Table 4.1-1 specifies the mapping of the uplink transport channels to their corresponding physical channels. Table 4.1-2 specifies the mapping of the uplink control channel information to its corresponding physical channel.

Table 4.1-1

TrCH	Physical Channel
UL-SCH	PUSCH
RACH	PRACH

Table 4.1-2

Control information	Physical Channel
UCI	PUCCH, PUSCH

4.2 Downlink

Table 4.2-1 specifies the mapping of the downlink transport channels to their corresponding physical channels. Table 4.2-2 specifies the mapping of the downlink control channel information to its corresponding physical channel.

Table 4.2-1

TrCH	Physical Channel
DL-SCH	PDSCH
BCH	PBCH
PCH	PDSCH

Table 4.2-2

Control information	Physical Channel
DCI	PDCCH

5 General procedures

Data and control streams from/to MAC layer are encoded /decoded to offer transport and control services over the radio transmission link. Channel coding scheme is a combination of error detection, error correcting, rate matching, interleaving and transport channel or control information mapping onto/splitting from physical channels.

5.1 CRC calculation

Denote the input bits to the CRC computation by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, and the parity bits by $p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the size of the input sequence and L is the number of parity bits. The parity bits are generated by one of the following cyclic generator polynomials:

- $g_{\text{CRC24A}}(D) = [D^{24} + D^{23} + D^{18} + D^{17} + D^{14} + D^{11} + D^{10} + D^7 + D^6 + D^5 + D^4 + D^3 + D + 1]$ for a CRC length $L=24$;
- $g_{\text{CRC24B}}(D) = [D^{24} + D^{23} + D^6 + D^5 + D + 1]$ for a CRC length $L=24$;
- $g_{\text{CRC24C}}(D) = [D^{24} + D^{23} + D^{21} + D^{20} + D^{17} + D^{15} + D^{13} + D^{12} + D^8 + D^4 + D^2 + D + 1]$ for a CRC length $L=24$;
- $g_{\text{CRC16}}(D) = [D^{16} + D^{12} + D^5 + 1]$ for a CRC length $L=16$;
- $g_{\text{CRC11}}(D) = [D^{11} + D^{10} + D^9 + D^5 + 1]$ for a CRC length $L=11$;
- $g_{\text{CRC6}}(D) = [D^6 + D^5 + 1]$ for a CRC length $L=6$.

The encoding is performed in a systematic form, which means that in GF(2), the polynomial:

$$a_0 D^{A+L-1} + a_1 D^{A+L-2} + \dots + a_{A-1} D^L + p_0 D^{L-1} + p_1 D^{L-2} + \dots + p_{L-2} D^1 + p_{L-1}$$

yields a remainder equal to 0 when divided by the corresponding CRC generator polynomial.

The bits after CRC attachment are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B = A + L$. The relation between a_k and b_k is:

$$b_k = a_k \quad \text{for } k=0,1,2,\dots,A-1$$

$$b_k = p_{k-A} \quad \text{for } k=A, A+1, A+2, \dots, A+L-1.$$

5.2 Code block segmentation and code block CRC attachment

5.2.1 Polar coding

The input bit sequence to the code block segmentation is denoted by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, where $A > 0$.

if $I_{seg} = 1$

Number of code blocks: $C = 2$;

else

Number of code blocks: $C = 1$

end if

$$A' = \lceil A/C \rceil \cdot C ;$$

for $i=0$ to $A'-A-1$

$$a'_i = 0 ;$$

end for

for $i=A'-A$ to $A'-1$

$$a'_i = a_{i-(A'-A)} ;$$

end for

$$s = 0 ;$$

for $r=0$ to $C-1$

for $k=0$ to $A'/C-1$

$$c_{rk} = a'_s ;$$

$$s = s + 1 ;$$

end for

The sequence $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r(A'/C-1)}$ is used to calculate the CRC parity bits $p_{r0}, p_{r1}, p_{r2}, \dots, p_{r(L-1)}$ according to Subclause 5.1 with a generator polynomial of length L .

for $k=A'/C$ to $A'/C+L-1$

$$c_{rk} = p_{r(k-A'/C)} ;$$

end for

end for

The value of A is no larger than 1706.

5.2.2 Low density parity check coding

The input bit sequence to the code block segmentation is denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B > 0$. If B is larger than the maximum code block size K_{cb} , segmentation of the input bit sequence is performed and an additional CRC sequence of $L=24$ bits is attached to each code block.

For LDPC base graph 1, the maximum code block size is:

$$K_{cb} = 8448$$

For LDPC base graph 2, the maximum code block size is:

$$K_{cb} = 3840$$

Total number of code blocks C is determined by:

if $B \leq K_{cb}$

$$L = 0$$

Number of code blocks: $C = 1$

$$B' = B$$

else

$$L = 24$$

Number of code blocks: $C = \lceil B / (K_{cb} - L) \rceil$

$$B' = B + C \cdot L$$

end if

The bits output from code block segmentation are denoted by $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r(K_r-1)}$, where $0 \leq r < C$ is the code block number, and $K_r = K$ is the number of bits for the code block number r .

The number of bits K in each code block is calculated as:

$$K' = B' / C$$

For LDPC base graph 1,

$$K_b = 22$$

For LDPC base graph 2,

if $B > 640$

$$K_b = 10$$

elseif $B > 560$

$$K_b = 9$$

elseif $B > 192$

$K_b=8$;

else

$K_b=6$;

end if

find the minimum value of Z in all sets of lifting sizes in Table 5.3.2-1, denoted as Z_c , such that $K_b \cdot Z_c \geq K'$, and set $K=22 Z_c$ for LDPC base graph 1 and $K=10 Z_c$ for LDPC base graph 2;

The bit sequence c_{rk} is calculated as:

$s=0$;

for $r=0$ to $C-1$

for $k=0$ to $K'-L-1$

$c_{rk}=b_s$;

$s=s+1$;

end for

if $C>1$

The sequence $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r|K'-L-1|}$ is used to calculate the CRC parity bits $p_{r0}, p_{r1}, p_{r2}, \dots, p_{r|L-1|}$ according to Subclause 5.1 with the generator polynomial $g_{\text{CRC24B}}(D)$.

for $k=K'-L$ to $K'-1$

$c_{rk}=p_{r|k+L-K'|}$;

end for

end if

for $k=K'$ to $K-1$ -- Insertion of filler bits

$c_{rk}=\text{<NULL>}$;

end for

end for

5.3 Channel coding

Usage of coding scheme for the different types of TrCH is shown in table 5.3-1. Usage of coding scheme for the different control information types is shown in table 5.3-2.

Table 5.3-1: Usage of channel coding scheme for TrCHs

TrCH	Coding scheme
UL-SCH	LDPC
DL-SCH	
PCH	
BCH	Polar code

Table 5.3-2: Usage of channel coding scheme for control information

Control Information	Coding scheme
DCI	Polar code
UCI	Block code
	Polar code

5.3.1 Polar coding

The bit sequence input for a given code block to channel coding is denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits to encode. After encoding the bits are denoted by $d_0, d_1, d_2, \dots, d_{N-1}$, where $N=2^n$ and the value of n is determined by the following:

Denote by E the rate matching output sequence length as given in Subclause 5.4.1;

If $E \leq (9/8) \cdot 2^{\lceil \log_2 E \rceil - 1}$ and $K/E < 9/16$

$$n_1 = \lceil \log_2 E \rceil - 1;$$

else

$$n_1 = \lceil \log_2 E \rceil;$$

end if

$$R_{\min} = 1/8;$$

$$n_2 = \lceil \log_2 (K/R_{\min}) \rceil;$$

$$n = \max \left\{ \min \{n_1, n_2, n_{\max}\}, n_{\min} \right\}$$

where $n_{\min} = 5$.

UE is not expected to be configured with $K + n_{PC} > E$, where n_{PC} is the number of parity check bits defined in Subclause 5.3.1.2.

5.3.1.1 Interleaving

The bit sequence $c_0, c_1, c_2, c_3, \dots, c_{K-1}$ is interleaved into bit sequence $c'_0, c'_1, c'_2, c'_3, \dots, c'_{K-1}$ as follows:

$$c'_k = c_{\Pi(k)}, \quad k=0,1,\dots,K-1$$

where the interleaving pattern $\Pi(k)$ is given by the following:

if $I_{IL}=0$

$$\Pi(k) = k, \quad k=0,1,\dots,K-1$$

else

$k=0$;

for $m=0$ to $K_{IL}^{\max}-1$

if $\Pi_{IL}^{\max}(m) \geq K_{IL}^{\max} - K$

$\Pi(k) = \Pi_{IL}^{\max}(m) - (K_{IL}^{\max} - K)$;

$k=k+1$;

end if

end for

end if

where $\Pi_{IL}^{\max}(m)$ is given by Table 5.3.1.1-1 and $K_{IL}^{\max}=164$.

Table 5.3.1.1-1: Interleaving pattern $\Pi_{IL}^{\max}(m)$

m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$
0	0	28	67	56	122	84	68	$\begin{smallmatrix} 11 \\ 2 \end{smallmatrix}$	33	$\begin{smallmatrix} 14 \\ 0 \end{smallmatrix}$	38
1	2	29	69	57	123	85	73	$\begin{smallmatrix} 11 \\ 3 \end{smallmatrix}$	36	$\begin{smallmatrix} 14 \\ 1 \end{smallmatrix}$	144
2	4	30	70	58	126	86	78	$\begin{smallmatrix} 11 \\ 4 \end{smallmatrix}$	44	$\begin{smallmatrix} 14 \\ 2 \end{smallmatrix}$	39
3	7	31	71	59	127	87	84	$\begin{smallmatrix} 11 \\ 5 \end{smallmatrix}$	47	$\begin{smallmatrix} 14 \\ 3 \end{smallmatrix}$	145
4	9	32	72	60	129	88	90	$\begin{smallmatrix} 11 \\ 6 \end{smallmatrix}$	64	$\begin{smallmatrix} 14 \\ 4 \end{smallmatrix}$	40
5	14	33	76	61	132	89	92	$\begin{smallmatrix} 11 \\ 7 \end{smallmatrix}$	74	$\begin{smallmatrix} 14 \\ 5 \end{smallmatrix}$	146
6	19	34	77	62	134	90	94	$\begin{smallmatrix} 11 \\ 8 \end{smallmatrix}$	79	$\begin{smallmatrix} 14 \\ 6 \end{smallmatrix}$	41
7	20	35	81	63	138	91	96	$\begin{smallmatrix} 11 \\ 9 \end{smallmatrix}$	85	$\begin{smallmatrix} 14 \\ 7 \end{smallmatrix}$	147
8	24	36	82	64	139	92	99	$\begin{smallmatrix} 12 \\ 0 \end{smallmatrix}$	97	$\begin{smallmatrix} 14 \\ 8 \end{smallmatrix}$	148
9	25	37	83	65	140	93	102	$\begin{smallmatrix} 12 \\ 1 \end{smallmatrix}$	100	$\begin{smallmatrix} 14 \\ 9 \end{smallmatrix}$	149
10	26	38	87	66	1	94	105	$\begin{smallmatrix} 12 \\ 2 \end{smallmatrix}$	103	$\begin{smallmatrix} 15 \\ 0 \end{smallmatrix}$	150
11	28	39	88	67	3	95	107	$\begin{smallmatrix} 12 \\ 3 \end{smallmatrix}$	117	$\begin{smallmatrix} 15 \\ 1 \end{smallmatrix}$	151
12	31	40	89	68	5	96	109	$\begin{smallmatrix} 12 \\ 4 \end{smallmatrix}$	125	$\begin{smallmatrix} 15 \\ 2 \end{smallmatrix}$	152
13	34	41	91	69	8	97	112	$\begin{smallmatrix} 12 \\ 5 \end{smallmatrix}$	131	$\begin{smallmatrix} 15 \\ 3 \end{smallmatrix}$	153
14	42	42	93	70	10	98	114	$\begin{smallmatrix} 12 \\ 6 \end{smallmatrix}$	136	$\begin{smallmatrix} 15 \\ 4 \end{smallmatrix}$	154
15	45	43	95	71	15	99	116	$\begin{smallmatrix} 12 \\ 7 \end{smallmatrix}$	142	$\begin{smallmatrix} 15 \\ 5 \end{smallmatrix}$	155
16	49	44	98	72	21	$\begin{smallmatrix} 10 \\ 0 \end{smallmatrix}$	121	$\begin{smallmatrix} 12 \\ 8 \end{smallmatrix}$	12	$\begin{smallmatrix} 15 \\ 6 \end{smallmatrix}$	156
17	50	45	101	73	27	$\begin{smallmatrix} 10 \\ 1 \end{smallmatrix}$	124	$\begin{smallmatrix} 12 \\ 9 \end{smallmatrix}$	17	$\begin{smallmatrix} 15 \\ 7 \end{smallmatrix}$	157
18	51	46	104	74	29	$\begin{smallmatrix} 10 \\ 2 \end{smallmatrix}$	128	$\begin{smallmatrix} 13 \\ 0 \end{smallmatrix}$	23	$\begin{smallmatrix} 15 \\ 8 \end{smallmatrix}$	158
19	53	47	106	75	32	$\begin{smallmatrix} 10 \\ 3 \end{smallmatrix}$	130	$\begin{smallmatrix} 13 \\ 1 \end{smallmatrix}$	37	$\begin{smallmatrix} 15 \\ 9 \end{smallmatrix}$	159
20	54	48	108	76	35	$\begin{smallmatrix} 10 \\ 4 \end{smallmatrix}$	133	$\begin{smallmatrix} 13 \\ 2 \end{smallmatrix}$	48	$\begin{smallmatrix} 16 \\ 0 \end{smallmatrix}$	160
21	56	49	110	77	43	$\begin{smallmatrix} 10 \\ 5 \end{smallmatrix}$	135	$\begin{smallmatrix} 13 \\ 3 \end{smallmatrix}$	75	$\begin{smallmatrix} 16 \\ 1 \end{smallmatrix}$	161
22	58	50	111	78	46	$\begin{smallmatrix} 10 \\ 6 \end{smallmatrix}$	141	$\begin{smallmatrix} 13 \\ 4 \end{smallmatrix}$	80	$\begin{smallmatrix} 16 \\ 2 \end{smallmatrix}$	162
23	59	51	113	79	52	$\begin{smallmatrix} 10 \\ 7 \end{smallmatrix}$	6	$\begin{smallmatrix} 13 \\ 5 \end{smallmatrix}$	86	$\begin{smallmatrix} 16 \\ 3 \end{smallmatrix}$	163
24	61	52	115	80	55	$\begin{smallmatrix} 10 \\ 8 \end{smallmatrix}$	11	$\begin{smallmatrix} 13 \\ 6 \end{smallmatrix}$	137		
25	62	53	118	81	57	$\begin{smallmatrix} 10 \\ 9 \end{smallmatrix}$	16	$\begin{smallmatrix} 13 \\ 7 \end{smallmatrix}$	143		
26	65	54	119	82	60	$\begin{smallmatrix} 11 \\ 0 \end{smallmatrix}$	22	$\begin{smallmatrix} 13 \\ 8 \end{smallmatrix}$	13		
27	66	55	120	83	63	$\begin{smallmatrix} 11 \\ 1 \end{smallmatrix}$	30	$\begin{smallmatrix} 13 \\ 9 \end{smallmatrix}$	18		

5.3.1.2 Polar encoding

The Polar sequence $Q_0^{N_{\max}-1} = \{Q_0^{N_{\max}}, Q_1^{N_{\max}}, \dots, Q_{N_{\max}-1}^{N_{\max}}\}$ is given by Table 5.3.1.2-1, where $0 \leq Q_i^{N_{\max}} \leq N_{\max} - 1$ denotes a bit index before Polar encoding for $i=0,1,\dots,N_{\max}-1$ and $N_{\max}=1024$. The Polar sequence $Q_0^{N_{\max}-1}$ is in ascending order of reliability $w(Q_0^{N_{\max}}) < w(Q_1^{N_{\max}}) < \dots < w(Q_{N_{\max}-1}^{N_{\max}})$, where $w(Q_i^{N_{\max}})$ denotes the reliability of bit index $Q_i^{N_{\max}}$.

For any code block encoded to N bits, a same Polar sequence $Q_0^{N-1} = \{Q_0^N, Q_1^N, Q_2^N, \dots, Q_{N-1}^N\}$ is used. The Polar sequence Q_0^{N-1} is a subset of Polar sequence $Q_0^{N_{\max}-1}$ with all elements $Q_i^{N_{\max}}$ of values less than N , ordered in ascending order of reliability $w(Q_0^N) < w(Q_1^N) < w(Q_2^N) < \dots < w(Q_{N-1}^N)$.

Denote $\bar{Q}_I^N$ as a set of bit indices in Polar sequence Q_0^{N-1} , and $\bar{Q}_F^N$ as the set of other bit indices in Polar sequence Q_0^{N-1} , where $\bar{Q}_I^N$ and $\bar{Q}_F^N$ are given in Subclause 5.4.1.1, $|\bar{Q}_I^N| = K + n_{PC}$, $|\bar{Q}_F^N| = N - |\bar{Q}_I^N|$, and n_{PC} is the number of parity check bits.

Denote $G_N = (G_2)^{\otimes n}$ as the n -th Kronecker power of matrix G_2 , where $G_2 = \begin{bmatrix} 1 & 0 \\ 1 & 1 \end{bmatrix}$.

For a bit index j with $j=0,1,\dots,N-1$, denote g_j as the j -th row of G_N and $w(g_j)$ as the row weight of g_j , where $w(g_j)$ is the number of ones in g_j . Denote the set of bit indices for parity check bits as Q_{PC}^N , where $|Q_{PC}^N| = n_{PC}$. A number of $(n_{PC} - n_{PC}^{wm})$ parity check bits are placed in the $(n_{PC} - n_{PC}^{wm})$ least reliable bit indices in $\bar{Q}_I^N$. A number of n_{PC}^{wm} other parity check bits are placed in the bit indices of minimum row weight in $\bar{Q}_I^N$, where $\bar{Q}_I^N$ denotes the $(|\bar{Q}_I^N| - n_{PC})$ most reliable bit indices in $\bar{Q}_I^N$; if there are more than n_{PC}^{wm} bit indices of the same minimum row weight in $\bar{Q}_I^N$, the n_{PC}^{wm} other parity check bits are placed in the n_{PC}^{wm} bit indices of the highest reliability and the minimum row weight in $\bar{Q}_I^N$.

Generate $u = [u_0 u_1 u_2 \dots u_{N-1}]$ according to the following:

$k=0$;

if $n_{PC} > 0$

$y_0=0$; $y_1=0$; $y_2=0$; $y_3=0$; $y_4=0$;

for $n=0$ to $N-1$

$y_t=y_0$; $y_0=y_1$; $y_1=y_2$; $y_2=y_3$; $y_3=y_4$; $y_4=y_t$;

if $n \in \bar{Q}_I^N$

if $n \in Q_{PC}^N$

$u_n = y_0$;

else

$u_n = c_k'$;

```

         $k = k + 1$  ;

         $y_0 = y_0 \oplus u_n$  ;

    end if

else

     $u_n = 0$  ;

    end if

end for

else

    for  $n = 0$  to  $N - 1$ 

        if  $n \in \bar{Q}_I^N$ 

             $u_n = c'_k$  ;

             $k = k + 1$  ;

        else

             $u_n = 0$  ;

        end if

    end for

end if

```

The output after encoding $d = [d_0 d_1 d_2 \dots d_{N-1}]$ is obtained by $d = \mathbf{u} \mathbf{G}_N$. The encoding is performed in GF(2).

Table 5.3.1.2-1: Polar sequence $Q_0^{N_{\max}-1}$ and its corresponding reliability $w(Q_i^{N_{\max}})$

$w(Q_i^N)$	Q_i^N	$w(Q_i^N)$	Q_i^N	$w(Q_i^N)$	Q_i^N	$w(Q_i^N)$	Q_i^N	$w(Q_i^N)$	Q_i^N	$w(Q_i^N)$	Q_i^N	$w(Q_i^N)$	Q_i^N	$w(Q_i^N)$	Q_i^N	$w(Q_i^N)$	Q_i^N
0	0	128	518	256	94	384	214	512	364	640	414	768	819	896	966		
1	1	129	54	257	204	385	309	513	654	641	223	769	814	897	755		
2	2	130	83	258	298	386	188	514	659	642	663	770	439	898	859		
3	4	131	57	259	400	387	449	515	335	643	692	771	929	899	940		
4	8	132	521	260	608	388	217	516	480	644	835	772	490	900	830		
5	16	133	112	261	352	389	408	517	315	645	619	773	623	901	911		
6	32	134	135	262	325	390	609	518	221	646	472	774	671	902	871		
7	3	135	78	263	533	391	596	519	370	647	455	775	739	903	639		
8	5	136	289	264	155	392	551	520	613	648	796	776	916	904	888		
9	64	137	194	265	210	393	650	521	422	649	809	777	463	905	479		
10	9	138	85	266	305	394	229	522	425	650	714	778	843	906	946		
11	6	139	276	267	547	395	159	523	451	651	721	779	381	907	750		
12	17	140	522	268	300	396	420	524	614	652	837	780	497	908	969		
13	10	141	58	269	109	397	310	525	543	653	716	781	930	909	508		
14	18	142	168	270	184	398	541	526	235	654	864	782	821	910	861		
15	128	143	139	271	534	399	773	527	412	655	810	783	726	911	757		
16	12	144	99	272	537	400	610	528	343	656	606	784	961	912	970		
17	33	145	86	273	115	401	657	529	372	657	912	785	872	913	919		
18	65	146	60	274	167	402	333	530	775	658	722	786	492	914	875		
19	20	147	280	275	225	403	119	531	317	659	696	787	631	915	862		
20	256	148	89	276	326	404	600	532	222	660	377	788	729	916	758		
21	34	149	290	277	306	405	339	533	426	661	435	789	700	917	948		
22	24	150	529	278	772	406	218	534	453	662	817	790	443	918	977		
23	36	151	524	279	157	407	368	535	237	663	319	791	741	919	923		
24	7	152	196	280	656	408	652	536	559	664	621	792	845	920	972		
25	129	153	141	281	329	409	230	537	833	665	812	793	920	921	761		
26	66	154	101	282	110	410	391	538	804	666	484	794	382	922	877		
27	512	155	147	283	117	411	313	539	712	667	430	795	822	923	952		
28	11	156	176	284	212	412	450	540	834	668	838	796	851	924	495		
29	40	157	142	285	171	413	542	541	661	669	667	797	730	925	703		
30	68	158	530	286	776	414	334	542	808	670	488	798	498	926	935		
31	130	159	321	287	330	415	233	543	779	671	239	799	880	927	978		
32	19	160	31	288	226	416	555	544	617	672	378	800	742	928	883		
33	13	161	200	289	549	417	774	545	604	673	459	801	445	929	762		
34	48	162	90	290	538	418	175	546	433	674	622	802	471	930	503		
35	14	163	545	291	387	419	123	547	720	675	627	803	635	931	925		
36	72	164	292	292	308	420	658	548	816	676	437	804	932	932	878		
37	257	165	322	293	216	421	612	549	836	677	380	805	687	933	735		
38	21	166	532	294	416	422	341	550	347	678	818	806	903	934	993		
39	132	167	263	295	271	423	777	551	897	679	461	807	825	935	885		
40	35	168	149	296	279	424	220	552	243	680	496	808	500	936	939		
41	258	169	102	297	158	425	314	553	662	681	669	809	846	937	994		
42	26	170	105	298	337	426	424	554	454	682	679	810	745	938	980		
43	513	171	304	299	550	427	395	555	318	683	724	811	826	939	926		
44	80	172	296	300	672	428	673	556	675	684	841	812	732	940	764		
45	37	173	163	301	118	429	583	557	618	685	629	813	446	941	941		
46	25	174	92	302	332	430	355	558	898	686	351	814	962	942	967		
47	22	175	47	303	579	431	287	559	781	687	467	815	936	943	886		
48	136	176	267	304	540	432	183	560	376	688	438	816	475	944	831		
49	260	177	385	305	389	433	234	561	428	689	737	817	853	945	947		
50	264	178	546	306	173	434	125	562	665	690	251	818	867	946	507		
51	38	179	324	307	121	435	557	563	736	691	462	819	637	947	889		
52	514	180	208	308	553	436	660	564	567	692	442	820	907	948	984		
53	96	181	386	309	199	437	616	565	840	693	441	821	487	949	751		
54	67	182	150	310	784	438	342	566	625	694	469	822	695	950	942		
55	41	183	153	311	179	439	316	567	238	695	247	823	746	951	996		
56	144	184	165	312	228	440	241	568	359	696	683	824	828	952	971		
57	28	185	106	313	338	441	778	569	457	697	842	825	753	953	890		
58	69	186	55	314	312	442	563	570	399	698	738	826	854	954	509		
59	42	187	328	315	704	443	345	571	787	699	899	827	857	955	949		
60	516	188	536	316	390	444	452	572	591	700	670	828	504	956	973		
61	49	189	577	317	174	445	397	573	678	701	783	829	799	957	1000		
62	74	190	548	318	554	446	403	574	434	702	849	830	255	958	892		
63	272	191	113	319	581	447	207	575	677	703	820	831	964	959	950		
64	160	192	154	320	393	448	674	576	349	704	728	832	909	960	863		
65	520	193	79	321	283	449	558	577	245	705	928	833	719	961	759		
66	288	194	269	322	122	450	785	578	458	706	791	834	477	962	1008		
67	528	195	108	323	448	451	432	579	666	707	367	835	915	963	510		
68	192	196	578	324	353	452	357	580	620	708	901	836	638	964	979		
69	544	197	224	325	561	453	187	581	363	709	630	837	748	965	953		
70	70	198	166	326	203	454	236	582	127	710	685	838	944	966	763		
71	44	199	519	327	63	455	664	583	191	711	844	839	869	967	974		
72	131	200	552	328	340	456	624	584	782	712	633	840	491	968	954		
73	81	201	195	329	394	457	587	585	407	713	711	841	699	969	879		
74	50	202	270	330	527	458	780	586	436	714	253	842	754	970	981		
75	73	203	641	331	582	459	705	587	626	715	691	843	858	971	982		
76	15	204	523	332	556	460	126	588	571	716	824	844	478	972	927		
77	320	205	275	333	181	461	242	589	465	717	902	845	968	973	995		
78	133	206	580	334	295	462	565	590	681	718	686	846	383	974	765		
79	52	207	291	335	285	463	398	591	246	719	740	847	910	975	956		
80	23	208	59	336	232	464	346	592	707	720	850	848	815	976	887		
81	134	209	169	337	124	465	456	593	350	721	375	849	976	977	985		
82	384	210	560	338	205	466	358	594	599	722	444	850	870	978	997		

83	76	211	114	339	182	467	405	595	668	723	470	851	917	979	986
84	137	212	277	340	643	468	303	596	790	724	483	852	727	980	943
85	82	213	156	341	562	469	569	597	460	725	415	853	493	981	891
86	56	214	87	342	286	470	244	598	249	726	485	854	873	982	998
87	27	215	197	343	585	471	595	599	682	727	905	855	701	983	766
88	97	216	116	344	299	472	189	600	573	728	795	856	931	984	511
89	39	217	170	345	354	473	566	601	411	729	473	857	756	985	988
90	259	218	61	346	211	474	676	602	803	730	634	858	860	986	1001
91	84	219	531	347	401	475	361	603	789	731	744	859	499	987	951
92	138	220	525	348	185	476	706	604	709	732	852	860	731	988	1002
93	145	221	642	349	396	477	589	605	365	733	960	861	823	989	893
94	261	222	281	350	344	478	215	606	440	734	865	862	922	990	975
95	29	223	278	351	586	479	786	607	628	735	693	863	874	991	894
96	43	224	526	352	645	480	647	608	689	736	797	864	918	992	1009
97	98	225	177	353	593	481	348	609	374	737	906	865	502	993	955
98	515	226	293	354	535	482	419	610	423	738	715	866	933	994	1004
99	88	227	388	355	240	483	406	611	466	739	807	867	743	995	1010
100	140	228	91	356	206	484	464	612	793	740	474	868	760	996	957
101	30	229	584	357	95	485	680	613	250	741	636	869	881	997	983
102	146	230	769	358	327	486	801	614	371	742	694	870	494	998	958
103	71	231	198	359	564	487	362	615	481	743	254	871	702	999	987
104	262	232	172	360	800	488	590	616	574	744	717	872	921	1000	1012
105	265	233	120	361	402	489	409	617	413	745	575	873	501	1001	999
106	161	234	201	362	356	490	570	618	603	746	913	874	876	1002	1016
107	576	235	336	363	307	491	788	619	366	747	798	875	847	1003	767
108	45	236	62	364	301	492	597	620	468	748	811	876	992	1004	989
109	100	237	282	365	417	493	572	621	655	749	379	877	447	1005	1003
110	640	238	143	366	213	494	219	622	900	750	697	878	733	1006	990
111	51	239	103	367	568	495	311	623	805	751	431	879	827	1007	1005
112	148	240	178	368	832	496	708	624	615	752	607	880	934	1008	959
113	46	241	294	369	588	497	598	625	684	753	489	881	882	1009	1011
114	75	242	93	370	186	498	601	626	710	754	866	882	937	1010	1013
115	266	243	644	371	646	499	651	627	429	755	723	883	963	1011	895
116	273	244	202	372	404	500	421	628	794	756	486	884	747	1012	1006
117	517	245	592	373	227	501	792	629	252	757	908	885	505	1013	1014
118	104	246	323	374	896	502	802	630	373	758	718	886	855	1014	1017
119	162	247	392	375	594	503	611	631	605	759	813	887	924	1015	1018
120	53	248	297	376	418	504	602	632	848	760	476	888	734	1016	991
121	193	249	770	377	302	505	410	633	690	761	856	889	829	1017	1020
122	152	250	107	378	649	506	231	634	713	762	839	890	965	1018	1007
123	77	251	180	379	771	507	688	635	632	763	725	891	938	1019	1015
124	164	252	151	380	360	508	653	636	482	764	698	892	884	1020	1019
125	768	253	209	381	539	509	248	637	806	765	914	893	506	1021	1021
126	268	254	284	382	111	510	369	638	427	766	752	894	749	1022	1022
127	274	255	648	383	331	511	190	639	904	767	868	895	945	1023	1023

5.3.2 Low density parity check coding

The bit sequence input for a given code block to channel coding is denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits to encode as defined in Subclause 5.2.2. After encoding the bits are denoted by $d_0, d_1, d_2, \dots, d_{N-1}$, where $N=66Z_c$ for LDPC base graph 1 and $N=50Z_c$ for LDPC base graph 2, and the value of Z_c is given in Subclause 5.2.2.

For a code block encoded by LDPC, the following encoding procedure applies:

- 1) Find the set with index i_{LS} in Table 5.3.2-1 which contains Z_c .
- 2) for $k=2Z_c$ to $K-1$

if $c_k \neq \text{NULL}$;

$$d_{k-2Z_c} = c_k ;$$

else

$$c_k = 0 ;$$

$$d_{k-2Z_c} = \text{NULL} ;$$

end if

end for

- 3) Generate $N+2Z_c-K$ parity bits $w = [w_0, w_1, w_2, \dots, w_{N+2Z_c-K-1}]^T$ such that $H \times \begin{bmatrix} c \\ w \end{bmatrix} = 0$, where $c = [c_0, c_1, c_2, \dots, c_{K-1}]^T$; 0 is a column vector of all elements equal to 0. The encoding is performed in GF(2).

For LDPC base graph 1, a matrix of H_{BG} has 46 rows with row indices $i=0,1,2,\dots,45$ and 68 columns with column indices $j=0,1,2,\dots,67$. For LDPC base graph 2, a matrix of H_{BG} has 42 rows with row indices $i=0,1,2,\dots,41$ and 52 columns with column indices $j=0,1,2,\dots,51$. The elements in H_{BG} with row and column indices given in Table 5.3.2-2 (for LDPC base graph 1) and Table 5.3.2-3 (for LDPC base graph 2) are of value 1, and all other elements in H_{BG} are of value 0.

The matrix H is obtained by replacing each element of H_{BG} with a $Z_c \times Z_c$ matrix, according to the following:

- Each element of value 0 in H_{BG} is replaced by an all zero matrix 0 of size $Z_c \times Z_c$;
- Each element of value 1 in H_{BG} is replaced by a circular permutation matrix $I^{(P_{i,j})}$ of size $Z_c \times Z_c$, where i and j are the row and column indices of the element, and $I^{(P_{i,j})}$ is obtained by circularly shifting the identity matrix I of size $Z_c \times Z_c$ to the right $P_{i,j}$ times. The value of $P_{i,j}$ is given by $P_{i,j} = \text{mod}(V_{i,j}, Z_c)$. The value of $V_{i,j}$ is given by Tables 5.3.2-2 and 5.3.2-3 according to the set index i_{LS} and LDPC base graph.

- 4) for $k=K$ to $N+2Z_c-1$

$$d_{k-2Z_c} = w_{k-K} ;$$

end for

Table 5.3.2-1: Sets of LDPC lifting size Z

Set index (i_{LS})	Set of lifting sizes (Z)
0	{2, 4, 8, 16, 32, 64, 128, 256}
1	{3, 6, 12, 24, 48, 96, 192, 384}
2	{5, 10, 20, 40, 80, 160, 320}
3	{7, 14, 28, 56, 112, 224}
4	{9, 18, 36, 72, 144, 288}
5	{11, 22, 44, 88, 176, 352}
6	{13, 26, 52, 104, 208}
7	{15, 30, 60, 120, 240}

Table 5.3.2-2: LDPC base graph 1 (H_{BG}) and its parity check matrices ($V_{i,j}$)

H_{BG}		$V_{i,j}$								H_{BG}		$V_{i,j}$							
Row index i	Column index j	Set index i_{LS}								Row index i	Column index j	Set index i_{LS}							
		0	1	2	3	4	5	6	7			0	1	2	3	4	5	6	7
0	0	25 0	30 7	73	22 3	21 1	29 4	0	13 5	15	1	96	2	29 0	12 0	0	34 8	6	13 8
	1	69	19	15	16	19 8	11 8	0	22 7		10	65	21 0	60	13 1	18 3	15	81	22 0
	2	22 6	50	10 3	94	18 8	16 7	0	12 6		13	63	31 8	13 0	20 9	10 8	81	18 2	17 3
	3	15 9	36 9	49	91	18 6	33 0	0	13 4		18	75	55	18 4	20 9	68	17 6	53	14 2
	5	10 0	18 1	24 0	74	21 9	20 7	0	84		25	17 9	26 9	51	81	64	11 3	46	49
	6	10	21 6	39	10	4	16 5	0	83		37	0	0	0	0	0	0	0	0
	9	59	31 7	15	0	29	24 3	0	53	16	1	64	13	69	15 4	27 0	19 0	88	78
	10	22 9	28 8	16 2	20 5	14 4	25 0	0	22 5		3	49	33 8	14 0	16 4	13	29 3	19 8	15 2
	11	11 0	10 9	21 5	21 6	11 6	1	0	20 5		11	49	57	45	43	99	33 2	16 0	84
	12	19 1	17	16 4	21	21 6	33 9	0	12 8		20	51	28 9	11 5	18 9	54	33 1	12 2	5
	13	9	35 7	13 3	21 5	11 5	20 1	0	75		22	15 4	57	30 0	10 1	0	11 4	18 2	20 5
	15	19 5	21 5	29 8	14	23 3	53	0	13 5		38	0	0	0	0	0	0	0	0
	16	23	10 6	11 0	70	14 4	34 7	0	21 7	17	0	7	26 0	25 7	56	15 3	11 0	91	18 3
	18	19 0	24 2	11 3	14 1	95	30 4	0	22 0		14	16 4	30 3	14 7	11 0	13 7	22 8	18 4	11 2
	19	35	18 0	16	19 8	21 6	16 7	0	90		16	59	81	12 8	20 0	0	24 7	30	10 6
	20	23 9	33 0	18 9	10 4	73	47	0	10 5		17	1	35 8	51	63	0	11 6	3	21 9
	21	31	34 6	32	81	26 1	18 8	0	13 7		21	14 4	37 5	22 8	4	16 2	19 0	15 5	12 9
	22	1	1	1	1	1	1	0	1		39	0	0	0	0	0	0	0	0
	23	0	0	0	0	0	0	0	0	18	1	42	13 0	26 9	19 1	16 1	47	1	18 3
1	0	2	76	30 3	14 1	17 9	77	22	96		12	23 3	16 3	29 4	11 0	15 1	28 6	41	21 5
	2	23 9	76	29 4	45	16 2	22 5	11	23 6		13	8	28 0	29 1	20 0	0	24 6	16 7	18 0
	3	11 7	73	27	15 1	22 3	96	12 4	13 6		18	15 5	13 2	14 1	14 3	24 1	18 1	68	14 3
	4	12 4	28 8	26 1	46	25 6	33 8	0	22 1		19	14 7	4	29 5	18 6	14 4	73	14 8	14
	5	71	14 4	16 1	11 9	16 0	26 8	10	12 8		40	0	0	0	0	0	0	0	0
	7	22 2	33 1	13 3	15 7	76	11 2	0	92		0	60	14 5	64	8	0	87	12	17 9
	8	10 4	33 1	4	13 3	20 2	30 2	0	17 2	19	1	73	21 3	18 1	6	0	11 0	6	10 8
	9	17 3	17 8	80	87	11 7	50	2	56		7	72	34 4	10 1	10 3	11 8	14 7	16 6	15 9
	11	22 0	29 5	12 9	20 6	10 9	16 7	16	11		8	12 7	24 2	27 0	19 8	14 4	25 8	18 4	13 8
	12	10 2	34 2	30 0	93	15	25 3	60	18 9		10	22 4	19 7	41	8	0	20 4	19 1	19 6
	14	10 9	21 7	76	79	72	33 4	0	95		41	0	0	0	0	0	0	0	0
	15	13 2	99	26 6	9	15 2	24 2	6	85	20	0	15 1	18 7	30 1	10 5	26 5	89	6	77
	16	14 2	35 4	72	11 8	15 8	25 7	30	15 3		3	18 6	20 6	16 2	0	81	65	12	18 7
	17	15 5	11 4	83	19 4	14 7	13 3	0	87		9	21 7	26 4	40	12 1	90	15 5	15	20 3
	19	25 5	33 1	26 0	31	15 6	9	16 8	16 3		11	47	34 1	13 0	21 4	14 4	24 4	5	16 7
	21	28	11 2	30 1	18 7	11 9	30 2	31	21 6		22	16 0	59	10	18 3	22 8	30	30	13 0
	22	0	0	0	0	0	0	10 5	0		42	0	0	0	0	0	0	0	0
	23	0	0	0	0	0	0	0	0	21	1	24 9	20 5	79	19 2	64	16 2	6	19 7
	24	0	0	0	0	0	0	0	0		5	12 1	10 2	17 5	13 1	46	26 4	86	12 2
2	0	10 6	20 5	68	20 7	25 8	22 6	13 2	18 9		16	10 9	32 8	13 2	22 0	26 6	34 6	96	21 5
	1	11 1	25 0	7	20 3	16 7	35	37	4		20	13 1	21 3	28 3	50	9	14 3	42	65
	2	18 5	32 8	80	31	22 0	21 3	21	22 5		21	17 1	97	10 3	10 6	18	10 9	19 9	21 6

	4	63	33 2	28 0	17 6	13 3	30 2	18 0	15 1	22	43	0	0	0	0	0	0	0		
	5	11 7	25 6	38 0	18 0	24 3	11 1	4	23 6		0	64	30	17 7	53	72	28 0	44	25	
	6	93	16 1	22 7	18 6	20 2	26 5	14 9	11 7		12	14 2	11	20	0	18 9	15 7	58	47	
	7	22 9	26 7	20 2	95	21 8	12 8	48	17 9		13	18 8	23 3	55	3	72	23 6	13 0	12 6	
	8	17 7	16 0	20 0	15 3	63	23 7	38	92		17	15 8	22	31 6	14 8	25 7	11 3	13 1	17 8	
	9	95	63	71	17 7	0	29 4	12 2	24		44	0	0	0	0	0	0	0	0	
	10	39	12 9	10 6	70	3	12 7	19 5	68	23	1	15 6	24	24 9	88	18 0	18	45	18 5	
	13	14 2	20 0	29 5	77	74	11 0	15 5	6		2	14 7	89	50	20 3	0	6	18	12 7	
	14	22 5	88	28 3	21 4	22 9	28 6	28	10 1		10	17 0	61	13 3	16 8	0	18 1	13 2	11 7	
	15	22 5	53	30 1	77	0	12 5	85	33		18	15 2	27	10 5	12 2	16 5	30 4	10 0	19 9	
	17	24 5	13 1	18 4	19 8	21 6	13 1	47	96		45	0	0	0	0	0	0	0	0	
	18	20 5	24 0	24 6	11 7	26 9	16 3	17 9	12 5		24	0	11 2	29 8	28 9	49	23 6	38	9	32
	19	25 1	20 5	23 0	22 3	20 0	21 0	42	67	3		86	15 8	28 0	15 7	19 9	17 0	12 5	17 8	
	20	11 7	13	27 6	90	23 4	7	66	23 0	4		23 6	23 5	11 0	64	0	24 9	19 1	2	
	24	0	0	0	0	0	0	0	0	11		11 6	33 9	18 7	19 3	26 6	28 8	28	15 6	
	25	0	0	0	0	0	0	0	0	22		22 2	23 4	28 1	12 4	0	19 4	6	58	
	46	0	0	0	0	0	0	0	0	0										
	3	0	12 1	27 6	22 0	20 1	18 7	97	4	12 8	25	1	23	72	17 2	1	20 5	27 9	4	27
1		89	87	20 8	18	14 5	94	6	23	6		13 6	17	29 5	16 6	0	25 5	74	14 1	
3		84	0	30	16 5	16 6	49	33	16 2	7		11 6	38 3	96	65	0	11 1	16	11	
4		20	27 5	19 7	5	10 8	27 9	11 3	22 0	14		18 2	31 2	46	81	18 3	54	28	18 1	
6		15 0	19 9	61	45	82	13 9	49	43	47		0	0	0	0	0	0	0	0	
7		13 1	15 3	17 5	14 2	13 2	16 6	21	18 6	0		19 5	71	27 0	10 7	0	32 5	21	16 3	
10		13 6	13 2	28 1	34	41	10 6	15 1	1	26	2	24 3	81	11 0	17 6	0	32 6	14 2	13 1	
11		86	30 5	30 3	15 5	16 2	24 6	83	21 6		4	21 5	76	31 8	21 2	0	22 6	19 2	16 9	
12		24 6	23 1	25 3	21 3	57	34 5	15 4	22		15	61	13 6	67	12 7	27 7	99	19 7	98	
13		21 9	34 1	16 4	14 7	36	26 9	87	24		48	0	0	0	0	0	0	0	0	
14		21 1	21 2	53	69	11 5	18 5	5	16 7		27	1	25	19 4	21 0	20 8	45	91	98	16 5
16		24 0	30 4	44	96	24 2	24 9	92	20 0			6	10 4	19 4	29	14 1	36	32 6	14 0	23 2
17		76	30 0	28	74	16 5	21 5	17 3	32	8		19 4	10 1	30 4	17 4	72	26 8	22	9	
18		24 4	27 1	77	99	0	14 3	12 0	23 5	49		0	0	0	0	0	0	0	0	
20		14 4	39	31 9	30	11 3	12 1	2	17 2	28	0	12 8	22 2	11	14 6	27 5	10 2	4	32	
21		12	35 7	68	15 8	10 8	12 1	14 2	21 9		4	16 5	19	29 3	15 3	0	1	1	43	
22		1	1	1	1	1	1	0	1		19	18 1	24 4	50	21 7	15 5	40	40	20 0	
25		0	0	0	0	0	0	0	0		21	63	27 4	23 4	11 4	62	16 7	93	20 5	
50	0	0	0	0	0	0	0	0	0											
4	0	15 7	33 2	23 3	17 0	24 6	42	24	64	29	1	86	25 2	27	15 0	0	27 3	92	23 2	
	1	10 2	18 1	20 5	10	23 5	25 6	20 4	21 1		14	23 6	5	30 8	11	18 0	10 4	13 6	32	
	26	0	0	0	0	0	0	0	0		18	84	14 7	11 7	53	0	24 3	10 6	11 8	
5	0	20 5	19 5	83	16 4	26 1	21 9	18 5	2	30	25	6	78	29	68	42	10 7	6	10 3	
	1	23 6	14	29 2	59	18 1	13 0	10 0	17 1		51	0	0	0	0	0	0	0	0	
	3	19 4	11 5	50	86	72	25 1	24	47		0	21 6	15 9	91	34	0	17 1	2	17 0	
	12	23 1	16 6	31 8	80	28 3	32 2	65	14 3		10	73	22 9	23	13 0	90	16	88	19 9	
	16	28	24 1	20 1	18 2	25 4	29 5	20 7	21 0	13	12 0	26 0	10 5	21 0	25 2	95	11 2	26		
	21	12 3	51	26 7	13 0	79	25 8	16 1	18 0	24	9	90	13 5	12 3	17 3	21 2	20	10 5		
	22	11 5	15 7	27 9	15 3	14 4	28 3	72	18 0	52	0	0	0	0	0	0	0	0		
27	0	0	0	0	0	0	0	0	0											

6	0	18 3	27 8	28 9	15 8	80	29 4	6	19 9	31	1	95	10 0	22 2	17 5	14 4	10 1	4	73
	6	22	25 7	21	11 9	14 4	73	27	22		7	17 7	21 5	30 8	49	14 4	29 7	49	14 9
	10	28	1	29 3	11 3	16 9	33 0	16 3	23		22	17 2	25 8	66	17 7	16 6	27 9	12 5	17 5
	11	67	35 1	13	21	90	99	50	10 0		25	61	25 6	16 2	12 8	19	22 2	19 4	10 8
	13	24 4	92	23 2	63	59	17 2	48	92		53	0	0	0	0	0	0	0	0
	17	11	25 3	30 2	51	17 7	15 0	24	20 7		0	22 1	10 2	21 0	19 2	0	35 1	6	10 3
	18	15 7	18	13 8	13 6	15 1	28 4	38	52		12	11 2	20 1	22	20 9	21 1	26 5	12 6	11 0
	20	21 1	22 5	23 5	11 6	10 8	30 5	91	13		14	19 9	17 5	27 1	58	36	33 8	63	15 1
	28	0	0	0	0	0	0	0	0		24	12 1	28 7	21 7	30	16 2	83	20	21 1
7	0	22 0	9	12	17	16 9	3	14 5	77	32	54	0	0	0	0	0	0	0	0
	1	44	62	88	76	18 9	10 3	88	14 6		1	2	32 3	17 0	11 4	0	56	10	19 9
	4	15 9	31 6	20 7	10 4	15 4	22 1	11 2	20 9		2	18 7	8	20	49	0	30 4	30	13 2
	7	31	33 3	50	10 0	18 4	29 7	15 3	32		11	41	36 1	14 0	16 1	76	14 1	6	17 2
	8	16 7	29 0	25	15 0	10 4	21 5	15 9	16 6		21	21 1	10 5	33	13 7	18	10 1	92	65
	14	10 4	11 4	76	15 8	16 4	39	76	18		55	0	0	0	0	0	0	0	0
	29	0	0	0	0	0	0	0	0		0	12 7	23 0	18 7	82	19 7	60	4	16 1
8	0	11 2	30 7	29 5	33	54	34 8	17 2	18 1	33	7	16 7	14 8	29 6	18 6	0	32 0	15 3	23 7
	1	4	17 9	13 3	95	0	75	2	10 5		15	16 4	20 2	5	68	10 8	11 2	19 7	14 2
	3	7	16 5	13 0	4	25 2	22	13 1	14 1		17	15 9	31 2	44	15 0	0	54	15 5	18 0
	12	21 1	18	23 1	21 7	41	31 2	14 1	22 3		56	0	0	0	0	0	0	0	0
	16	10 2	39	29 6	20 4	98	22 4	96	17 7		1	16 1	32 0	20 7	19 2	19 9	10 0	4	23 1
	19	16 4	22 4	11 0	39	46	17	99	14 5		6	19 7	33 5	15 8	17 3	27 8	21 0	45	17 4
	21	10 9	36 8	26 9	58	15	59	10 1	19 9		12	20 7	2	55	26	0	19 5	16 8	14 5
	22	24 1	67	24 5	44	23 0	31 4	35	15 3		22	10 3	26 6	28 5	18 7	20 5	26 8	18 5	10 0
	24	90	17 0	15 4	20 1	54	24 4	11 6	38		57	0	0	0	0	0	0	0	0
9	0	10 3	36 6	18 9	9	16 2	15 6	6	16 9	34	0	37	21 0	25 9	22 2	21 6	13 5	6	11
	1	18 2	23 2	24 4	37	15 9	88	10	12		14	10 5	31 3	17 9	15 7	16	15	20 0	20 7
	10	10 9	32 1	36	21 3	93	29 3	14 5	20 6		15	51	29 7	17 8	0	0	35	17 7	42
	11	21	13 3	28 6	10 5	13 4	11 1	53	22 1		18	12 0	21	16 0	6	0	18 8	43	10 0
	13	14 2	57	15 1	89	45	92	20 1	17		58	0	0	0	0	0	0	0	0
	17	14	30 3	26 7	18 5	13 2	15 2	4	21 2		1	19 8	26 9	29 8	81	72	31 9	82	59
	18	61	63	13 5	10 9	76	23	16 4	92		13	22 0	82	15	19 5	14 4	23 6	2	20 4
	20	21 6	82	20 9	21 8	20 9	33 7	17 3	20 5		23	12 2	11 5	11 5	13 8	0	85	13 5	16 1
	31	0	0	0	0	0	0	0	0		59	0	0	0	0	0	0	0	0
10	0	10 3	36 6	18 9	9	16 2	15 6	6	16 9	35	0	16 7	18 5	15 1	12 3	19 0	16 4	91	12 1
	1	18 2	23 2	24 4	37	15 9	88	10	12		9	15 1	17 7	17 9	90	0	19 6	64	90
	10	10 9	32 1	36	21 3	93	29 3	14 5	20 6		10	15 7	28 9	64	73	0	20 9	19 8	26
	11	21	13 3	28 6	10 5	13 4	11 1	53	22 1		12	16 3	21 4	18 1	10	0	24 6	10 0	14 0
	13	14 2	57	15 1	89	45	92	20 1	17		60	0	0	0	0	0	0	0	0
	17	14	30 3	26 7	18 5	13 2	15 2	4	21 2		1	17 3	25 8	10 2	12	15 3	23 6	4	11 5
	18	61	63	13 5	10 9	76	23	16 4	92		3	13 9	93	77	77	0	26 4	28	18 8
11	0	77	48	16	52	55	25	18 4	45	36	7	14 9	34 6	19 2	49	16 5	37	10 9	16 8
	1	41	10 2	14 7	11	23	32 2	19 4	11 5		19	0	29 7	20 8	11 4	11 7	27 2	18 8	52
	12	83	8	29 0	2	27 4	20 0	12 3	13 4		61	0	0	0	0	0	0	0	0
	16	18 2	47	28 9	35	18 1	35 1	16	1		0	15 7	17 5	32	67	21 6	30 4	10	4
											8	13 7	37	80	45	14 4	23 7	84	10 3

	21	78	18 8	17 7	32	27 3	16 6	10 4	15 2		17	14 9	31 2	19 7	96	2	13 5	12	30
	22	25 2	33 4	43	84	39	33 8	10 9	16 5		62	0	0	0	0	0	0	0	0
	23	22	11 5	28 0	20 1	26	19 2	12 4	10 7		1	16 7	52	15 4	23	0	12 3	2	53
	33	0	0	0	0	0	0	0	0		3	17 3	31 4	47	21 5	0	77	75	18 9
12	0	16 0	77	22 9	14 2	22 5	12 3	6	18 6	41	9	13 9	13 9	12 4	60	0	25	14 2	21 5
	1	42	18 6	23 5	17 5	16 2	21 7	20	21 5		18	15 1	28 8	20 7	16 7	18 3	27 2	12 8	24
	10	21	17 4	16 9	13 6	24 4	14 2	20 3	12 4		63	0	0	0	0	0	0	0	0
	11	32	23 2	48	3	15 1	11 0	15 3	18 0		0	14 9	11 3	22 6	11 4	27	28 8	16 3	22 2
	13	23 4	50	10 5	28	23 8	17 6	10 4	98	42	4	15 7	14	65	91	0	83	10	17 0
	18	7	74	52	18 2	24 3	76	20 7	80		24	13 7	21 8	12 6	78	35	17	16 2	71
	34	0	0	0	0	0	0	0	0		64	0	0	0	0	0	0	0	0
13	0	17 7	31 3	39	81	23 1	31 1	52	22 0	43	1	15 1	11 3	22 8	20 6	52	21 0	1	22
	3	24 8	17 7	30 2	56	0	25 1	14 7	18 5		16	16 3	13 2	69	22	24 3	3	16 3	12 7
	7	15 1	26 6	30 3	72	21 6	26 5	1	15 4		18	17 3	11 4	17 6	13 4	0	53	99	49
	20	18 5	11 5	16 0	21 7	47	94	16	17 8		25	13 9	16 8	10 2	16 1	27 0	16 7	98	12 5
	23	62	37 0	37	78	36	81	46	15 0		65	0	0	0	0	0	0	0	0
	35	0	0	0	0	0	0	0	0	44	0	13 9	80	23 4	84	18	79	4	19 1
14	0	20 6	14 2	78	14	0	22	1	12 4		7	15 7	78	22 7	4	0	24 4	6	21 1
	12	55	24 8	29 9	17 5	18 6	32 2	20 2	14 4		9	16 3	16 3	25 9	9	0	29 3	14 2	18 7
	15	20 6	13 7	54	21 1	25 3	27 7	11 8	18 2		22	17 3	27 4	26 0	12	57	27 2	3	14 8
	16	12 7	89	61	19 1	16	15 6	13 0	95		66	0	0	0	0	0	0	0	0
	17	16	34 7	17 9	51	0	66	1	72	45	1	14 9	13 5	10 1	18 4	16 8	82	18 1	17 7
	21	22 9	12	25 8	43	79	78	2	76		6	15 1	14 9	22 8	12 1	0	67	45	11 4
	36	0	0	0	0	0	0	0	0		10	16 7	15	12 6	29	14 4	23 5	15 3	93
	15	0	40	24 1	22 9	90	17 0	17 6	39		67	0	0	0	0	0	0	0	0

Table 5.3.2-3: LDPC base graph 2 (H_{BG}) and its parity check matrices ($V_{i,j}$)

H_{BG}		$V_{i,j}$								H_{BG}		$V_{i,j}$							
Row index i	Column index j	Set index i_{LS}								Row index i	Column index j	Set index i_{LS}							
		0	1	2	3	4	5	6	7			0	1	2	3	4	5	6	7
0	0	9	17 4	0	72	3	15 6	14 3	14 5	16	26	0	0	0	0	0	0	0	0
	1	11 7	97	0	11 0	26	14 3	19	13 1		1	25 4	15 8	0	48	12 0	13 4	57	19 6
	2	20 4	16 6	0	23	53	14	17 6	71		5	12 4	23	24	13 2	43	23	20 1	17 3
	3	26	66	0	18 1	35	3	16 5	21		11	11 4	9	10 9	20 6	65	62	14 2	19 5
	6	18 9	71	0	95	11 5	40	19 6	23	17	12	64	6	18	2	42	16 3	35	21 8
	9	20 5	17 2	0	8	12 7	12 3	13	11 2		27	0	0	0	0	0	0	0	0
	10	0	0	0	1	0	0	0	1		0	22 0	18 6	0	68	17	17 3	12 9	12 8
	11	0	0	0	0	0	0	0	0		6	19 4	6	18	16	10 6	31	20 3	21 1
1	0	16 7	27	13 7	53	19	17	18	14 2	18	7	50	46	86	15 6	14 2	22	14 0	21 0
	3	16 6	36	12 4	15 6	94	65	27	17 4		28	0	0	0	0	0	0	0	0
	4	25 3	48	0	11 5	10 4	63	3	18 3		0	87	58	0	35	79	13	11 0	39
	5	12 5	92	0	15 6	66	1	10 2	27		1	20	42	15 8	13 8	28	13 5	12 4	84
	6	22 6	31	88	11 5	84	55	18 5	96	19	10	18 5	15 6	15 4	86	41	14 5	52	88
	7	15 6	18 7	0	20 0	98	37	17	23		29	0	0	0	0	0	0	0	0
	8	22 4	18 5	0	29	69	17 1	14	9		1	26	76	0	6	2	12 8	19 6	11 7
	9	25 2	3	55	31	50	13 3	18 0	16 7		4	10 5	61	14 8	20	10 3	52	35	22 7
	11	0	0	0	0	0	0	0	0	20	11	29	15 3	10 4	14 1	78	17 3	11 4	6
	12	0	0	0	0	0	0	0	0		30	0	0	0	0	0	0	0	0
	0	81	25	20	15 2	95	98	12 6	74		0	76	15 7	0	80	91	15 6	10	23 8
	1	11 4	11 4	94	13 1	10 6	16 8	16 3	31		8	42	17 5	17	43	75	16 6	12 2	13
2	3	44	11 7	99	46	92	10 7	47	3	21	13	21 0	67	33	81	81	40	23	11
	4	52	11 0	9	19 1	11 0	82	18 3	53		31	0	0	0	0	0	0	0	0
	8	24 0	11 4	10 8	91	11 1	14 2	13 2	15 5		1	22 2	20	0	49	54	18	20 2	19 5
	10	1	1	1	0	1	1	1	0		2	63	52	4	1	13 2	16 3	12 6	44
	12	0	0	0	0	0	0	0	0	22	32	0	0	0	0	0	0	0	0
	13	0	0	0	0	0	0	0	0		0	23	10 6	0	15 6	68	11 0	52	5
	1	8	13 6	38	18 5	12 0	53	36	23 9		3	23 5	86	75	54	11 5	13 2	17 0	94
	2	58	17 5	15	6	12 1	17 4	48	17 1		5	23 8	95	15 8	13 4	56	15 0	13	11 1
3	4	15 8	11 3	10 2	36	22	17 4	18	95	23	33	0	0	0	0	0	0	0	0
	5	10 4	72	14 6	12 4	4	12 7	11 1	11 0		1	46	18 2	0	15 3	30	11 3	11 3	81
	6	20 9	12 3	12	12 4	73	17	20 3	15 9		2	13 9	15 3	69	88	42	10 8	16 1	19
	7	54	11 8	57	11 0	49	89	3	19 9		9	8	64	87	63	10 1	61	88	13 0
	8	18	28	53	15 6	12 8	17	19 1	43	24	34	0	0	0	0	0	0	0	0
	9	12 8	18 6	46	13 3	79	10 5	16 0	75		0	22 8	45	0	21 1	12 8	72	19 7	66
	10	0	0	0	1	0	0	0	1		5	15 6	21	65	94	63	13 6	19 4	95
	13	0	0	0	0	0	0	0	0		35	0	0	0	0	0	0	0	0
	0	17 9	72	0	20 0	42	86	43	29	25	2	29	67	0	90	14 2	36	16 4	14 6
	1	21 4	74	13 6	16	24	67	27	14 0		7	14 3	13 7	10 0	6	28	38	17 2	66
4	11	71	29	15 7	10 1	51	83	11 7	18 0		12	16 0	55	13	22 1	10 0	53	49	19 0
	14	0	0	0	0	0	0	0	0		13	12 2	85	7	6	13 3	14 5	16 1	86
	0	23 1	10	0	18 5	40	79	13 6	12 1	26	36	0	0	0	0	0	0	0	0
	1	41	44	13 1	13 8	14 0	84	49	41		0	8	10 3	0	27	13	42	16 8	64
5	0	23 1	10	0	18 5	40	79	13 6	12 1	27	0	8	10 3	0	27	13	42	16 8	64
	1	41	44	13 1	13 8	14 0	84	49	41		0	8	10 3	0	27	13	42	16 8	64

	5	19 4	12 1	14 2	17 0	84	35	36	16 9		6	15 1	50	32	11 8	10	10 4	19 3	18 1
	7	15 9	80	14 1	21 9	13 7	10 3	13 2	88		37	0	0	0	0	0	0	0	0
	11	10 3	48	64	19 3	71	60	62	20 7		1	98	70	0	21 6	10 6	64	14	7
	15	0	0	0	0	0	0	0	0		2	10 1	11 1	12 6	21 2	77	24	18 6	14 4
6	0	15 5	12 9	0	12 3	10 9	47	7	13 7	28	5	13 5	16 8	11 0	19 3	43	14 9	46	16
	5	22 8	92	12 4	55	87	15 4	34	72		38	0	0	0	0	0	0	0	0
	7	45	10 0	99	31	10 7	10	19 8	17 2		0	18	11 0	0	10 8	13 3	13 9	50	25
	9	28	49	45	22 2	13 3	15 5	16 8	12 4		4	28	17	15 4	61	25	16 1	27	57
	11	15 8	18 4	14 8	20 9	13 9	29	12	56		39	0	0	0	0	0	0	0	0
	16	0	0	0	0	0	0	0	0		2	71	12 0	0	10 6	87	84	70	37
7	1	12 9	80	0	10 3	97	48	16 3	86	29	5	24 0	15 4	35	44	56	17 3	17	13 9
	5	14 7	18 6	45	13	13 5	12 5	78	18 6		7	9	52	51	18 5	10 4	93	50	22 1
	7	14 0	16	14 8	10 5	35	24	14 3	87		9	84	56	13 4	17 6	70	29	6	17
	11	3	10 2	96	15 0	10 8	47	10 7	17 2		40	0	0	0	0	0	0	0	0
	13	11 6	14 3	78	18 1	65	55	58	15 4		1	10 6	3	0	14 7	80	11 7	11 5	20 1
	17	0	0	0	0	0	0	0	0		13	1	17 0	20	18 2	13 9	14 8	18 9	46
8	0	14 2	11 8	0	14 7	70	53	10 1	17 6	30	41	0	0	0	0	0	0	0	0
	1	94	70	65	43	69	31	17 7	16 9		0	24 2	84	0	10 8	32	11 6	11 0	17 9
	12	23 0	15 2	87	15 2	88	16 1	22	22 5		5	44	8	20	21	89	73	0	14
	18	0	0	0	0	0	0	0	0		12	16 6	17	12 2	11 0	71	14 2	16 3	11 6
9	1	20 3	28	0	2	97	10 4	18 6	16 7	31	42	0	0	0	0	0	0	0	0
	8	20 5	13 2	97	30	40	14 2	27	23 8		2	13 2	16 5	0	71	13 5	10 5	16 3	46
	10	61	18 5	51	18 4	24	99	20 5	48		7	16 4	17 9	88	12	6	13 7	17 3	2
	11	24 7	17 8	85	83	49	64	81	68		10	23 5	12 4	13	10 9	2	29	17 9	10 6
	19	0	0	0	0	0	0	0	0		43	0	0	0	0	0	0	0	0
10	0	11	59	0	17 4	46	11 1	12 5	38	32	0	14 7	17 3	0	29	37	11	19 7	18 4
	1	18 5	10 4	17	15 0	41	25	60	21 7		12	85	17 7	19	20 1	25	41	19 1	13 5
	6	0	22	15 6	8	10 1	17 4	17 7	20 8		13	36	12	78	69	11 4	16 2	19 3	14 1
	7	11 7	52	20	56	96	23	51	23 2		44	0	0	0	0	0	0	0	0
	20	0	0	0	0	0	0	0	0		1	57	77	0	91	60	12 6	15 7	85
11	0	11	32	0	99	28	91	39	17 8	33	5	40	18 4	15 7	16 5	13 7	15 2	16 7	22 5
	7	23 6	92	7	13 8	30	17 5	29	21 4		11	63	18	6	55	93	17 2	18 1	17 5
	9	21 0	17 4	4	11 0	11 6	24	35	16 8		45	0	0	0	0	0	0	0	0
	13	56	15 4	2	99	64	14 1	8	51		0	14 0	25	0	1	12 1	73	19 7	17 8
	21	0	0	0	0	0	0	0	0		2	38	15 1	63	17 5	12 9	15 4	16 7	11 2
12	1	63	39	0	46	33	12 2	18	12 4	34	7	15 4	17 0	82	83	26	12 9	17 9	10 6
	3	11 1	93	11 3	21 7	12 2	11	15 5	12 2		46	0	0	0	0	0	0	0	0
	11	14	11	48	10 9	13 1	4	49	72		10	21 9	37	0	40	97	16 7	18 1	15 4
	22	0	0	0	0	0	0	0	0		13	15 1	31	14 4	12	56	38	19 3	11 4
13	0	83	49	0	37	76	29	32	48	35	47	0	0	0	0	0	0	0	0
	1	2	12 5	11 2	11 3	37	91	53	57		1	31	84	0	37	1	11 2	15 7	42
	8	38	35	10 2	14 3	62	27	95	16 7		5	66	15 1	93	97	70	7	17 3	41
	13	22 2	16 6	26	14 0	47	12 7	18 6	21 9		11	38	19 0	19	46	1	19	19 1	10 5
	23	0	0	0	0	0	0	0	0		48	0	0	0	0	0	0	0	0
14	1	11 5	19	0	36	14 3	11	91	82	36	0	23 9	93	0	10 6	11 9	10 9	18 1	16 7
	6	14 5	11 8	13 8	95	51	14 5	20	23 2		7	17 2	13 2	24	18 1	32	6	15 7	45
	11	3	21	57	40	13 0	8	52	20 4		12	34	57	13 8	15 4	14 2	10 5	17 3	18 9

	13	23 2	16 3	27	11 6	97	16 6	10 9	16 2	40	49	0	0	0	0	0	0	0		
	24	0	0	0	0	0	0	0	0		2	0	10 3	0	98	6	16 0	19 3	78	
15	0	51	68	0	11 6	13 9	13 7	17 4	38		10	75	10 7	36	35	73	15 6	16 3	67	
	10	17 5	63	73	20 0	96	10 3	10 8	21 7		13	12 0	16 3	14 3	36	10 2	82	17 9	18 0	
	11	21 3	81	99	11 0	12 8	40	10 2	15 7		50	0	0	0	0	0	0	0	0	
	25	0	0	0	0	0	0	0	0		41	1	12 9	14 7	0	12 0	48	13 2	19 1	53
16	1	20 3	87	0	75	48	78	12 5	17 0			5	22 9	7	2	10 1	47	6	19 7	21 5
	9	14 2	17 7	79	15 8	9	15 8	31	23			11	11 8	60	55	81	19	8	16 7	23 0
	11	8	13 5	11 1	13 4	28	17	54	17 5			51	0	0	0	0	0	0	0	0
	12	24 2	64	14 3	97	8	16 5	17 6	20 2											

5.3.3 Channel coding of small block lengths

The bit sequence input for a given code block to channel coding is denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits to encode. After encoding the bits are denoted by $d_0, d_1, d_2, \dots, d_{N-1}$.

5.3.3.1 Encoding of 1-bit information

For $K=1$, the code block is encoded according to Table 5.3.3.1-1, where $N=Q_m$ and Q_m is the modulation order for the code block.

Table 5.3.3.1-1: Encoding of 1-bit information

Q_m	Encoded bits $d_0, d_1, d_2, \dots, d_{N-1}$
1	$[c_0]$
2	$[c_0 \ y]$
4	$[c_0 \ y \ x \ x]$
6	$[c_0 \ y \ x \ x \ x \ x]$
8	$[c_0 \ y \ x \ x \ x \ x \ x \ x]$

The "x" and "y" in Table 5.3.3.1-1 are placeholders for Subclause 6.3.1.1 of [4, TS 38.211] to scramble the information bits in a way that maximizes the Euclidean distance of the modulation symbols carrying the information bits.

5.3.3.2 Encoding of 2-bit information

For $K=2$, the code block is encoded according to Table 5.3.3.2-1, where $c_2=(c_0+c_1) \bmod 2$, $N=3Q_m$, and Q_m is the modulation order for the code block.

Table 5.3.3.2-1: Encoding of 2-bit information

Q_m	Encoded bits $d_0, d_1, d_2, \dots, d_{N-1}$
1	$[c_0 \ c_1 \ c_2]$
2	$[c_0 \ c_1 \ c_2 \ c_0 \ c_1 \ c_2]$
4	$[c_0 \ c_1 \ x \ x \ c_2 \ c_0 \ x \ x \ c_1 \ c_2 \ x \ x]$
6	$[c_0 \ c_1 \ x \ x \ x \ x \ c_2 \ c_0 \ x \ x \ x \ x \ c_1 \ c_2 \ x \ x \ x \ x]$
8	$[c_0 \ c_1 \ x \ x \ x \ x \ x \ x \ c_2 \ c_0 \ x \ x \ x \ x \ x \ x \ c_1 \ c_2 \ x \ x \ x \ x \ x \ x]$

The "x" in Table 5.3.3.2-1 are placeholders for Subclause 6.3.1.1 of [4, TS 38.211] to scramble the information bits in a way that maximizes the Euclidean distance of the modulation symbols carrying the information bits.

5.3.3.3 Encoding of other small block lengths

For $3 \leq K \leq 11$, the code block is encoded by $d_i = \left(\sum_{k=0}^{K-1} c_k \cdot M_{i,k} \right) \bmod 2$, where $i=0,1,\dots,N-1$, $N=32$, and $M_{i,k}$ represents the basis sequences as defined in Table 5.3.3.3-1.

Table 5.3.3.3-1: Basis sequences for (32, K) code

i	$M_{i,0}$	$M_{i,1}$	$M_{i,2}$	$M_{i,3}$	$M_{i,4}$	$M_{i,5}$	$M_{i,6}$	$M_{i,7}$	$M_{i,8}$	$M_{i,9}$	$M_{i,10}$
0	1	1	0	0	0	0	0	0	0	0	1
1	1	1	1	0	0	0	0	0	0	1	1
2	1	0	0	1	0	0	1	0	1	1	1
3	1	0	1	1	0	0	0	0	1	0	1
4	1	1	1	1	0	0	0	1	0	0	1
5	1	1	0	0	1	0	1	1	1	0	1
6	1	0	1	0	1	0	1	0	1	1	1
7	1	0	0	1	1	0	0	1	1	0	1
8	1	1	0	1	1	0	0	1	0	1	1
9	1	0	1	1	1	0	1	0	0	1	1
10	1	0	1	0	0	1	1	1	0	1	1
11	1	1	1	0	0	1	1	0	1	0	1
12	1	0	0	1	0	1	0	1	1	1	1
13	1	1	0	1	0	1	0	1	0	1	1
14	1	0	0	0	1	1	0	1	0	0	1
15	1	1	0	0	1	1	1	1	0	1	1
16	1	1	1	0	1	1	1	0	0	1	0
17	1	0	0	1	1	1	0	0	1	0	0
18	1	1	0	1	1	1	1	1	0	0	0
19	1	0	0	0	0	1	1	0	0	0	0
20	1	0	1	0	0	0	1	0	0	0	1
21	1	1	0	1	0	0	0	0	0	1	1
22	1	0	0	0	1	0	0	1	1	0	1
23	1	1	1	0	1	0	0	0	1	1	1
24	1	1	1	1	1	0	1	1	1	1	0
25	1	1	0	0	0	1	1	1	0	0	1
26	1	0	1	1	0	1	0	0	1	1	0
27	1	1	1	1	0	1	0	1	1	1	0
28	1	0	1	0	1	1	1	0	1	0	0
29	1	0	1	1	1	1	1	1	1	0	0
30	1	1	1	1	1	1	1	1	1	1	1
31	1	0	0	0	0	0	0	0	0	0	0

5.4 Rate matching

5.4.1 Rate matching for Polar code

The rate matching for Polar code is defined per coded block and consists of sub-block interleaving, bit collection, and bit interleaving. The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$. The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

5.4.1.1 Sub-block interleaving

The bits input to the sub-block interleaver are the coded bits $d_0, d_1, d_2, \dots, d_{N-1}$. The coded bits $d_0, d_1, d_2, \dots, d_{N-1}$ are divided into 32 sub-blocks. The bits output from the sub-block interleaver are denoted as $y_0, y_1, y_2, \dots, y_{N-1}$, generated as follows:

for $n=0$ to $N-1$

$$i = \lfloor 32n/N \rfloor;$$

$$J(n) = P(i) \times (N/32) + \text{mod}(n, N/32);$$

$$y_n = d_{J(n)};$$

end for

where the sub-block interleaver pattern $P(i)$ is given by Table 5.4.1.1-1.

Table 5.4.1.1-1: Sub-block interleaver pattern $P(i)$

i	$P(i)$	i	$P(i)$	i	$P(i)$	i	$P(i)$	i	$P(i)$	i	$P(i)$	i	$P(i)$	i	$P(i)$
0	0	4	3	8	8	12	10	16	12	20	14	24	24	28	27
1	1	5	5	9	16	13	18	17	20	21	22	25	25	29	29
2	2	6	6	10	9	14	11	18	13	22	15	26	26	30	30
3	4	7	7	11	17	15	19	19	21	23	23	27	28	31	31

The sets of bit indices $\bar{Q}_I^N$ and $\bar{Q}_F^N$ are determined as follows, where K , n_{PC} , and Q_0^{N-1} are defined in Subclause 5.3.1

$$\bar{Q}_{F,tmp}^N = \emptyset$$

if $E < N$

if $K/E \leq 7/16$ -- puncturing

for $n=0$ to $N-E-1$

$$\bar{Q}_{F,tmp}^N = \bar{Q}_{F,tmp}^N \cup \{J(n)\};$$

end for

if $E \geq 3N/4$

$$\bar{Q}_{F,tmp}^N = \bar{Q}_{F,tmp}^N \cup \{0, 1, \dots, \lceil 3N/4 - E/2 \rceil - 1\};$$

else

$$\bar{Q}_{F,tmp}^N = \bar{Q}_{F,tmp}^N \cup \{0, 1, \dots, \lceil 9N/16 - E/4 \rceil - 1\};$$

end if

```

else -- shortening
    for  $n=E$  to  $N-1$ 
         $\bar{Q}_{F,tmp}^N = \bar{Q}_{F,tmp}^N \cup \{J(n)\}$  ;
    end for
end if
end if

 $\bar{Q}_{I,tmp}^N = Q_0^{N-1}(\bar{Q}_{F,tmp}^N)$  ;

 $\bar{Q}_I^N$  comprises  $(K+n_{PC})$  most reliable bit indices in  $\bar{Q}_{I,tmp}^N$  ;

 $\bar{Q}_F^N = Q_0^{N-1}(\bar{Q}_I^N)$  ;

```

5.4.1.2 Bit selection

The bit sequence after the sub-block interleaver $y_0, y_1, y_2, \dots, y_{N-1}$ from Subclause 5.4.1.1 is written into a circular buffer of length N .

Denoting by E the rate matching output sequence length, the bit selection output bit sequence e_k , $k=0,1,2,\dots,E-1$, is generated as follows:

```

if  $E \geq N$  -- repetition
    for  $k=0$  to  $E-1$ 
         $e_k = y_{\text{mod}(k,N)}$  ;
    end for
else
    if  $K/E \leq 7/16$  -- puncturing
        for  $k=0$  to  $E-1$ 
             $e_k = y_{k+N-E}$  ;
        end for
    else -- shortening
        for  $k=0$  to  $E-1$ 
             $e_k = y_k$  ;
        end for
    end if
end if

```

5.4.1.3 Interleaving of coded bits

The bit sequence $e_0, e_1, e_2, \dots, e_{E-1}$ is interleaved into bit sequence $f_0, f_1, f_2, \dots, f_{E-1}$, as follows:

If $I_{BL}=1$

Denote T as the smallest integer such that $T(T+1)/2 \geq E$;

$k=0$;

for $i=0$ to $T-1$

for $j=0$ to $T-1-i$

if $k < E$

$v_{i,j} = e_k$;

else

$v_{i,j} = \text{<NULL>}$;

end if

$k=k+1$;

end for

end for

$k=0$;

for $j=0$ to $T-1$

for $i=0$ to $T-1-j$

if $v_{i,j} \neq \text{<NULL>}$

$f_k = v_{i,j}$;

$k=k+1$

end if

end for

end for

else

for $i=0$ to $E-1$

$f_i = e_i$;

end for

end if

The value of E is no larger than 8192.

5.4.2 Rate matching for LDPC code

The rate matching for LDPC code is defined per coded block and consists of bit selection and bit interleaving. The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$. The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

5.4.2.1 Bit selection

The bit sequence after encoding $d_0, d_1, d_2, \dots, d_{N-1}$ from Subclause 5.3.2 is written into a circular buffer of length N_{cb} for the r -th coded block, where N is defined in Subclause 5.3.2.

For the r -th code block, let $N_{cb} = N$ if $I_{LBRM} = 0$ and $N_{cb} = \min(N, N_{ref})$ otherwise, where $N_{ref} = \left\lfloor \frac{TBS_{LBRM}}{C \cdot R_{LBRM}} \right\rfloor$, $R_{LBRM} = 2/3$, TBS_{LBRM} is determined according to Subclause 6.1.4.2 in [6, TS 38.214] for UL-SCH and Subclause 5.1.3.2 in [6, TS 38.214] for DL-SCH/PCH, assuming the following:

- maximum number of layers for one TB for UL-SCH is given by X, where
 - if the higher layer parameter *maxMIMO-Layers* of *PUSCH-ServingCellConfig* of the serving cell is configured, X is given by that parameter
 - elseif the higher layer parameter *maxRank* of *pusch-Config* of the serving cell is configured, X is given by the maximum value of *maxRank* across all BWPs of the serving cell
 - otherwise, X is given by the maximum number of layers for PUSCH supported by the UE for the serving cell
- maximum number of layers for one TB for DL-SCH/PCH is given by the minimum of X and 4, where
 - if the higher layer parameter *maxMIMO-Layers* of *PDSCH-ServingCellConfig* of the serving cell is configured, X is given by that parameter
 - otherwise, X is given by the maximum number of layers for PDSCH supported by the UE for the serving cell
- if the higher layer parameter *mcs-Table* given by a *pdsch-Config* for at least one DL BWP of the serving cell is set to 'qam256', maximum modulation order $Q_m = 8$ is assumed for DL-SCH; otherwise a maximum modulation order $Q_m = 6$ is assumed for DL-SCH;
- if the higher layer parameter *mcs-Table* or *mcs-TableTransformPrecoder* given by a *pusch-Config* or *configuredGrantConfig* for at least one UL BWP of the serving cell is set to 'qam256', maximum modulation order $Q_m = 8$ is assumed for UL-SCH; otherwise a maximum modulation order $Q_m = 6$ is assumed for UL-SCH
- maximum coding rate of 948/1024;
- $n_{PRB} = n_{PRB, LBRM}$ is given by Table 5.4.2.1-1, where the value of $n_{PRB, LBRM}$ for DL-SCH is determined according to the initial downlink bandwidth part if there is no other downlink bandwidth part configured to the UE;
- $N_{RE} = 156 \cdot n_{PRB}$;
- C is the number of code blocks of the transport block determined according to Subclause 5.2.2.

Table 5.4.2.1-1: Value of $n_{PRB,LBRM}$

Maximum number of PRBs across all configured DL BWPs and UL BWPs of a carrier for DL-SCH and UL-SCH, respectively	$n_{PRB,LBRM}$
Less than 33	32
33 to 66	66
67 to 107	107
108 to 135	135
136 to 162	162
163 to 217	217
Larger than 217	273

Denoting by E_r the rate matching output sequence length for the r -th coded block, where the value of E_r is determined as follows:

Set $j=0$

for $r=0$ to $C-1$

if the r -th coded block is not scheduled for transmission as indicated by CBGTI according to Subclause 5.1.7.2 for DL-SCH and 6.1.5.2 for UL-SCH in [6, TS 38.214]

$E_r=0$;

else

if $j \leq C' - \text{mod}(G/(N_L \cdot Q_m), C') - 1$

$$E_r = N_L \cdot Q_m \cdot \left\lfloor \frac{G}{N_L \cdot Q_m \cdot C'} \right\rfloor ;$$

else

$$E_r = N_L \cdot Q_m \cdot \left\lceil \frac{G}{N_L \cdot Q_m \cdot C'} \right\rceil ;$$

end if

$j=j+1$;

end if

end for

where

- N_L is the number of transmission layers that the transport block is mapped onto;
- Q_m is the modulation order;
- G is the total number of coded bits available for transmission of the transport block;
- $C'=C$ if CBGTI is not present in the DCI scheduling the transport block and C' is the number of scheduled code blocks of the transport block if CBGTI is present in the DCI scheduling the transport block.

Denote by rv_{id} the redundancy version number for this transmission ($rv_{id} = 0, 1, 2$ or 3), the rate matching output bit sequence e_k , $k=0,1,2,\dots,E-1$, is generated as follows, where k_0 is given by Table 5.4.2.1-2 according to the value of rv_{id} and LDPC base graph:

```

 $k=0$  ;
 $j=0$  ;
while  $k < E$ 
  if  $d_{(k_0+j) \bmod N_{cb}} \neq \text{NULL} > i$ 
     $e_k = d_{(k_0+j) \bmod N_{cb}}$  ;
     $k=k+1$  ;
  end if
   $j=j+1$  ;
end while

```

Table 5.4.2.1-2: Starting position of different redundancy versions, k_0

rv_{id}	k_0	
	LDPC base graph 1	LDPC base graph 2
0	0	0
1	$\left\lfloor \frac{17 N_{cb}}{66 Z_c} \right\rfloor Z_c$	$\left\lfloor \frac{13 N_{cb}}{50 Z_c} \right\rfloor Z_c$
2	$\left\lfloor \frac{33 N_{cb}}{66 Z_c} \right\rfloor Z_c$	$\left\lfloor \frac{25 N_{cb}}{50 Z_c} \right\rfloor Z_c$
3	$\left\lfloor \frac{56 N_{cb}}{66 Z_c} \right\rfloor Z_c$	$\left\lfloor \frac{43 N_{cb}}{50 Z_c} \right\rfloor Z_c$

5.4.2.2 Bit interleaving

The bit sequence $e_0, e_1, e_2, \dots, e_{E-1}$ is interleaved to bit sequence $f_0, f_1, f_2, \dots, f_{E-1}$, according to the following, where the value of Q_m is the modulation order.

```

for  $j=0$  to  $E/Q_m-1$ 
  for  $i=0$  to  $Q_m-1$ 
     $f_{i+j \cdot Q_m} = e_{i \cdot E/Q_m + j}$  ;
  end for
end for

```

5.4.3 Rate matching for channel coding of small block lengths

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$. The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$, where E is the rate matching output sequence length. The bit sequence $f_0, f_1, f_2, \dots, f_{E-1}$ is obtained by the following:

```

for  $k=0$  to  $E-1$ 

```

$$f_k = d_{k \bmod N} ;$$

end for

5.5 Code block concatenation

The input bit sequence for the code block concatenation block are the sequences f_{rk} , for $r=0, \dots, C-1$ and $k=0, \dots, E_r-1$, where E_r is the number of rate matched bits for the r -th code block. The output bit sequence from the code block concatenation block is the sequence g_k for $k=0, \dots, G-1$.

The code block concatenation consists of sequentially concatenating the rate matching outputs for the different code blocks. Therefore,

Set $k=0$ and $r=0$

while $r < C$

Set $j=0$

while $j < E_r$

$$g_k = f_{rj}$$

$$k = k + 1$$

$$j = j + 1$$

end while

$$r = r + 1$$

end while

6 Uplink transport channels and control information

6.1 Random access channel

The sequence index for the random access channel is received from higher layers and is processed according to [4, TS 38.211].

6.2 Uplink shared channel

6.2.1 Transport block CRC attachment

Error detection is provided on each UL-SCH transport block through a Cyclic Redundancy Check (CRC).

The entire transport block is used to calculate the CRC parity bits. Denote the bits in a transport block delivered to layer 1 by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, and the parity bits by $p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the payload size and

L is the number of parity bits. The lowest order information bit a_0 is mapped to the most significant bit of the transport block as defined in Subclause 6.1.1 of [TS38.321].

The parity bits are computed and attached to the UL-SCH transport block according to Subclause 5.1, by setting L to 24 bits and using the generator polynomial $g_{\text{CRC24A}}^{(D)}$ if $A > 3824$; and by setting L to 16 bits and using the generator polynomial $g_{\text{CRC16}}^{(D)}$ otherwise.

The bits after CRC attachment are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B = A + L$.

6.2.2 LDPC base graph selection

For initial transmission of a transport block with coding rate R indicated by the MCS index according to Subclause 6.1.4.1 in [6, TS 38.214] and subsequent re-transmission of the same transport block, each code block of the transport block is encoded with either LDPC base graph 1 or 2 according to the following:

- if $A \leq 292$, or if $A \leq 3824$ and $R \leq 0.67$, or if $R \leq 0.25$, LDPC base graph 2 is used;
- otherwise, LDPC base graph 1 is used,

where A is the payload size as described in Subclause 6.2.1.

6.2.3 Code block segmentation and code block CRC attachment

The bits input to the code block segmentation are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$ where B is the number of bits in the transport block (including CRC).

Code block segmentation and code block CRC attachment are performed according to Subclause 5.2.2.

The bits after code block segmentation are denoted by $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r(K_r-1)}$, where r is the code block number and K_r is the number of bits for code block number r according to Subclause 5.2.2.

6.2.4 Channel coding of UL-SCH

Code blocks are delivered to the channel coding block. The bits in a code block are denoted by

$c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r(K_r-1)}$, where r is the code block number, and K_r is the number of bits in code block number r . The total number of code blocks is denoted by C and each code block is individually LDPC encoded according to Subclause 5.3.2.

After encoding the bits are denoted by $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$, where the values of N_r is given in Subclause 5.3.2.

6.2.5 Rate matching

Coded bits for each code block, denoted as $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$, are delivered to the rate match block, where r is the code block number, and N_r is the number of encoded bits in code block number r . The total number of code blocks is denoted by C and each code block is individually rate matched according to Subclause 5.4.2 by setting $I_{\text{LBRM}} = 1$ if higher layer parameter *rateMatching* is set to *limitedBufferRM* and by setting $I_{\text{LBRM}} = 0$ otherwise.

After rate matching, the bits are denoted by $f_{r0}, f_{r1}, f_{r2}, f_{r3}, \dots, f_{r(E_r-1)}$, where E_r is the number of rate matched bits for code block number r .

6.2.6 Code block concatenation

The input bit sequence for the code block concatenation block are the sequences $f_{r0}, f_{r1}, f_{r2}, f_{r3}, \dots, f_{r(E_r-1)}$, for $r = 0, \dots, C-1$ and where E_r is the number of rate matched bits for the r -th code block.

Code block concatenation is performed according to Subclause 5.5.

The bits after code block concatenation are denoted by $g_0, g_1, g_2, g_3, \dots, g_{G-1}$, where G is the total number of coded bits for transmission.

6.2.7 Data and control multiplexing

Denote the coded bits for UL-SCH as $g_0^{\text{UL-SCH}}, g_1^{\text{UL-SCH}}, g_2^{\text{UL-SCH}}, g_3^{\text{UL-SCH}}, \dots, g_{G^{\text{UL-SCH}}-1}^{\text{UL-SCH}}$.

Denote the coded bits for HARQ-ACK, if any, as $g_0^{\text{ACK}}, g_1^{\text{ACK}}, g_2^{\text{ACK}}, g_3^{\text{ACK}}, \dots, g_{G^{\text{ACK}}-1}^{\text{ACK}}$.

Denote the coded bits for CSI part 1, if any, as $g_0^{\text{CSI-part1}}, g_1^{\text{CSI-part1}}, g_2^{\text{CSI-part1}}, g_3^{\text{CSI-part1}}, \dots, g_{G^{\text{CSI-part1}}-1}^{\text{CSI-part1}}$.

Denote the coded bits for CSI part 2, if any, as $g_0^{\text{CSI-part2}}, g_1^{\text{CSI-part2}}, g_2^{\text{CSI-part2}}, g_3^{\text{CSI-part2}}, \dots, g_{G^{\text{CSI-part2}}-1}^{\text{CSI-part2}}$.

Denote the multiplexed data and control coded bit sequence as $g_0, g_1, g_2, g_3, \dots, g_{G-1}$.

Denote l as the OFDM symbol index of the scheduled PUSCH, starting from 0 to $N_{\text{sym},\text{all}}^{\text{PUSCH}}-1$, where $N_{\text{sym},\text{all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS.

Denote k as the subcarrier index of the scheduled PUSCH, starting from 0 to $M_{\text{sc}}^{\text{PUSCH}}-1$, where $M_{\text{sc}}^{\text{PUSCH}}$ is expressed as a number of subcarriers.

Denote $\Phi_l^{\text{UL-SCH}}$ as the set of resource elements, in ascending order of indices k , available for transmission of data in OFDM symbol l , for $l=0, 1, 2, \dots, N_{\text{sym},\text{all}}^{\text{PUSCH}}-1$.

Denote $M_{\text{sc}}^{\text{UL-SCH}}(l) = |\Phi_l^{\text{UL-SCH}}|$ as the number of elements in set $\Phi_l^{\text{UL-SCH}}$. Denote $\Phi_l^{\text{UL-SCH}}(j)$ as the j -th element in $\Phi_l^{\text{UL-SCH}}$.

Denote Φ_l^{UCI} as the set of resource elements, in ascending order of indices k , available for transmission of UCI in OFDM symbol l , for $l=0, 1, 2, \dots, N_{\text{sym},\text{all}}^{\text{PUSCH}}-1$. Denote $M_{\text{sc}}^{\text{UCI}}(l) = |\Phi_l^{\text{UCI}}|$ as the number of elements in set Φ_l^{UCI} .

Denote $\Phi_l^{\text{UCI}}(j)$ as the j -th element in Φ_l^{UCI} . For any OFDM symbol that carries DMRS of the PUSCH, $\Phi_l^{\text{UCI}} = \Phi_l^{\text{UL-SCH}}$. For any OFDM symbol that does not carry DMRS of the PUSCH, $\Phi_l^{\text{UCI}} = \Phi_l^{\text{UL-SCH}}$.

If frequency hopping is configured for the PUSCH,

- denote $l^{(1)}$ as the OFDM symbol index of the first OFDM symbol after the first set of consecutive OFDM symbol(s) carrying DMRS in the first hop;
- denote $l^{(2)}$ as the OFDM symbol index of the first OFDM symbol after the first set of consecutive OFDM symbol(s) carrying DMRS in the second hop.
- denote $l_{\text{CSI}}^{(1)}$ as the OFDM symbol index of the first OFDM symbol that does not carry DMRS in the first hop;
- denote $l_{\text{CSI}}^{(2)}$ as the OFDM symbol index of the first OFDM symbol that does not carry DMRS in the second hop;
- if HARQ-ACK is present for transmission on the PUSCH with UL-SCH, let

$$G^{\text{ACK}}(1) = N_L \cdot Q_m \cdot \lceil G^{\text{ACK}} / (2 \cdot N_L \cdot Q_m) \rceil \quad \text{and} \quad G^{\text{ACK}}(2) = N_L \cdot Q_m \cdot \lceil G^{\text{ACK}} / (2 \cdot N_L \cdot Q_m) \rceil ;$$
- if CSI is present for transmission on the PUSCH with UL-SCH, let

- $G^{\text{CSI-part1}}(1) = N_L \cdot Q_m \cdot \left\lfloor G^{\text{CSI-part1}} / (2 \cdot N_L \cdot Q_m) \right\rfloor$;
- $G^{\text{CSI-part1}}(2) = N_L \cdot Q_m \cdot \left\lceil G^{\text{CSI-part1}} / (2 \cdot N_L \cdot Q_m) \right\rceil$;
- $G^{\text{CSI-part2}}(1) = N_L \cdot Q_m \cdot \left\lfloor G^{\text{CSI-part2}} / (2 \cdot N_L \cdot Q_m) \right\rfloor$; and
- $G^{\text{CSI-part2}}(2) = N_L \cdot Q_m \cdot \left\lceil G^{\text{CSI-part2}} / (2 \cdot N_L \cdot Q_m) \right\rceil$;
- if only HARQ-ACK and CSI part 1 are present for transmission on the PUSCH without UL-SCH, let
 - $G^{\text{ACK}}(1) = \min \left(N_L \cdot Q_m \cdot \left\lfloor G^{\text{ACK}} / (2 \cdot N_L \cdot Q_m) \right\rfloor, M_1 \cdot N_L \cdot Q_m \right)$;
 - $G^{\text{ACK}}(2) = G^{\text{ACK}} - G^{\text{ACK}}(1)$;
 - $G^{\text{CSI-part1}}(1) = M_1 \cdot N_L \cdot Q_m - G^{\text{ACK}}(1)$; and
 - $G^{\text{CSI-part1}}(2) = G^{\text{CSI-part1}} - G^{\text{CSI-part1}}(1)$;
- if HARQ-ACK, CSI part 1 and CSI part 2 are present for transmission on the PUSCH without UL-SCH, let
 - $G^{\text{ACK}}(1) = \min \left(N_L \cdot Q_m \cdot \left\lfloor G^{\text{ACK}} / (2 \cdot N_L \cdot Q_m) \right\rfloor, M_1 \cdot N_L \cdot Q_m \right)$;
 - $G^{\text{ACK}}(2) = G^{\text{ACK}} - G^{\text{ACK}}(1)$;
- if the number of HARQ-ACK information bits is more than 2,
 - $G^{\text{CSI-part1}}(1) = \min \left(N_L \cdot Q_m \cdot \left\lfloor G^{\text{CSI-part1}} / (2 \cdot N_L \cdot Q_m) \right\rfloor, M_1 \cdot N_L \cdot Q_m - G^{\text{ACK}}(1) \right)$; otherwise,
 - $G^{\text{CSI-part1}}(1) = \min \left(N_L \cdot Q_m \cdot \left\lfloor G^{\text{CSI-part1}} / (2 \cdot N_L \cdot Q_m) \right\rfloor, M_1 \cdot N_L \cdot Q_m - G^{\text{ACK}}(1) \right)$
 - $G^{\text{CSI-part1}}(2) = G^{\text{CSI-part1}} - G^{\text{CSI-part1}}(1)$;
 - $G^{\text{CSI-part2}}(1) = M_1 \cdot N_L \cdot Q_m - G^{\text{CSI-part1}}(1)$ if the number of HARQ-ACK information bits is no more than 2, and
 - $G^{\text{CSI-part2}}(1) = M_1 \cdot N_L \cdot Q_m - G^{\text{ACK}}(1) - G^{\text{CSI-part1}}(1)$ otherwise; and
 - $G^{\text{CSI-part2}}(2) = M_2 \cdot N_L \cdot Q_m - G^{\text{CSI-part1}}(2)$ if the number of HARQ-ACK information bits is no more than 2, and
 - $G^{\text{CSI-part2}}(2) = M_2 \cdot N_L \cdot Q_m - G^{\text{ACK}}(2) - G^{\text{CSI-part1}}(2)$ otherwise;
- if CSI part 1 and CSI part 2 are present for transmission on the PUSCH without UL-SCH, let
 - $G^{\text{CSI-part1}}(1) = \min \left(N_L \cdot Q_m \cdot \left\lfloor G^{\text{CSI-part1}} / (2 \cdot N_L \cdot Q_m) \right\rfloor, M_1 \cdot N_L \cdot Q_m - G^{\text{ACK}}(1) \right)$;
 - $G^{\text{CSI-part1}}(2) = G^{\text{CSI-part1}} - G^{\text{CSI-part1}}(1)$;
 - $G^{\text{CSI-part2}}(1) = M_1 \cdot N_L \cdot Q_m - G^{\text{CSI-part1}}(1)$; and
 - $G^{\text{CSI-part2}}(2) = M_2 \cdot N_L \cdot Q_m - G^{\text{CSI-part1}}(2)$;
- let $N_{\text{hop}}^{\text{PUSCH}} = 2$, and denote $N_{\text{ymb,hop}}^{\text{PUSCH}}(1)$, $N_{\text{ymb,hop}}^{\text{PUSCH}}(2)$ as the number of OFDM symbols of the PUSCH in the first and second hop, respectively;
- N_L is the number of transmission layers of the PUSCH;

- Q_m is the modulation order of the PUSCH;

- $$M_1 = \sum_{l=0}^{N_{\text{sym,hop}}^{\text{PUSCH}}(1)-1} M_{\text{SC}}^{\text{UCI}}(l) ;$$

- $$M_2 = \sum_{l=N_{\text{sym,hop}}^{\text{PUSCH}}(1)}^{N_{\text{sym,hop}}^{\text{PUSCH}}(1)+N_{\text{sym,hop}}^{\text{PUSCH}}(2)-1} M_{\text{SC}}^{\text{UCI}}(l)$$

- $$M_3 = \sum_{l=l^{(1)}}^{N_{\text{sym,hop}}^{\text{PUSCH}}(1)-1} M_{\text{SC}}^{\text{UCI}}(l)$$

If frequency hopping is not configured for the PUSCH,

- denote $l^{(1)}$ as the OFDM symbol index of the first OFDM symbol after the first set of consecutive OFDM symbol(s) carrying DMRS;
- denote $l_{\text{CSI}}^{(1)}$ as the OFDM symbol index of the first OFDM symbol that does not carry DMRS;
- if HARQ-ACK is present for transmission on the PUSCH, let $G^{\text{ACK}}(1) = G^{\text{ACK}} ;$
- if CSI is present for transmission on the PUSCH, let $G^{\text{CSI-part1}}(1) = G^{\text{CSI-part1}}$ and $G^{\text{CSI-part2}}(1) = G^{\text{CSI-part2}} ;$
- let $N_{\text{hop}}^{\text{PUSCH}} = 1$ and $N_{\text{sym,hop}}^{\text{PUSCH}}(1) = N_{\text{sym,all}}^{\text{PUSCH}} .$

The multiplexed data and control coded bit sequence $g_0, g_1, g_2, g_3, \dots, g_{G-1}$ is obtained according to the following:

Step 1:

Set $\bar{\Phi}_l^{\text{UL-SCH}} = \Phi_l^{\text{UL-SCH}}$ for $l=0, 1, 2, \dots, N_{\text{sym,all}}^{\text{PUSCH}} - 1 ;$

Set $\bar{M}_{\text{sc}}^{\text{UL-SCH}}(l) = |\bar{\Phi}_l^{\text{UL-SCH}}|$ for $l=0, 1, 2, \dots, N_{\text{sym,all}}^{\text{PUSCH}} - 1 ;$

Set $\bar{\Phi}_l^{\text{UCI}} = \Phi_l^{\text{UCI}}$ for $l=0, 1, 2, \dots, N_{\text{sym,all}}^{\text{PUSCH}} - 1 ;$

Set $\bar{M}_{\text{sc}}^{\text{UCI}}(l) = |\bar{\Phi}_l^{\text{UCI}}|$ for $l=0, 1, 2, \dots, N_{\text{sym,all}}^{\text{PUSCH}} - 1 ;$

if the number of HARQ-ACK information bits to be transmitted on PUSCH is 0, 1 or 2 bits

the number of reserved resource elements for potential HARQ-ACK transmission is calculated according to

Subclause 6.3.2.4.1.1, by setting $O_{\text{ACK}} = 2 ;$

denote $G_{\text{rvd}}^{\text{ACK}}$ as the number of coded bits for potential HARQ-ACK transmission using the reserved resource elements;

if frequency hopping is configured for the PUSCH, let $G_{\text{rvd}}^{\text{ACK}}(1) = N_L \cdot Q_m \cdot \frac{G_{\text{rvd}}^{\text{ACK}}}{(2 \cdot N_L \cdot Q_m)}$ and $G_{\text{rvd}}^{\text{ACK}}(2) = N_L \cdot Q_m \cdot \frac{G_{\text{rvd}}^{\text{ACK}}}{(2 \cdot N_L \cdot Q_m)}$;

if frequency hopping is not configured for the PUSCH, let $G_{\text{rvd}}^{\text{ACK}}(1) = G_{\text{rvd}}^{\text{ACK}} ;$

denote $\bar{\Phi}_l^{\text{rvd}}$ as the set of reserved resource elements for potential HARQ-ACK transmission, in OFDM symbol l , for $l=0, 1, 2, \dots, N_{\text{sym,all}}^{\text{PUSCH}} - 1 ;$

Set $m_{\text{count}}^{\text{ACK}}(1)=0$;

Set $m_{\text{count}}^{\text{ACK}}(2)=0$;

$\bar{\Phi}_l^{\text{rvd}} = \emptyset$ for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}}-1$;

for $i=1$ to $N_{\text{hop}}^{\text{PUSCH}}$

$l=l^{(i)}$;

while $m_{\text{count}}^{\text{ACK}}(i) < G_{\text{rvd}}^{\text{ACK}}(i)$

if $\bar{M}_{\text{sc}}^{\text{UCI}}(l) > 0$

if $G_{\text{rvd}}^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i) \geq \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m$

$d=1$;

$m_{\text{count}}^{\text{RE}} = \bar{M}_{\text{sc}}^{\text{UL-SCH}}(l)$;

end if

if $G_{\text{rvd}}^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i) < \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m$

$d = \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m / (G_{\text{rvd}}^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i))$;

$m_{\text{count}}^{\text{RE}} = (G_{\text{rvd}}^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i)) / (N_L \cdot Q_m)$;

end if

for $j=0$ to $m_{\text{count}}^{\text{RE}}-1$

$\bar{\Phi}_l^{\text{rvd}} = \bar{\Phi}_l^{\text{rvd}} \cup \{ \bar{\Phi}_l^{\text{UL-SCH}}(j) \}$

$m_{\text{count}}^{\text{ACK}}(i) = m_{\text{count}}^{\text{ACK}}(i) + N_L \cdot Q_m$;

end for

end if

$l=l+1$;

end while

end for

else

$\bar{\Phi}_l^{\text{rvd}} = \emptyset$ for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}}-1$;

end if

Denote $\bar{M}_{\text{sc,rvd}}^{\bar{\Phi}}(l) = |\bar{\Phi}_l^{\text{rvd}}|$ as the number of elements in $\bar{\Phi}_l^{\text{rvd}}$.

Step 2:

if HARQ-ACK is present for transmission on the PUSCH and the number of HARQ-ACK information bits is more than 2,

Set $m_{\text{count}}^{\text{ACK}}(1)=0$;

Set $m_{\text{count}}^{\text{ACK}}(2)=0$;

Set $m_{\text{count,all}}^{\text{ACK}}=0$;

for $i=1$ to $N_{\text{hop}}^{\text{PUSCH}}$

$l=l^{(i)}$;

while $m_{\text{count}}^{\text{ACK}}(i) < G^{\text{ACK}}(i)$

if $\bar{M}_{\text{sc}}^{\text{UCI}}(l) > 0$

if $G^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i) \geq \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m$

$d=1$;

$m_{\text{count}}^{\text{RE}} = \bar{M}_{\text{sc}}^{\text{UCI}}(l)$;

end if

if $G^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i) < \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m$

$d = \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m / (G^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i))$;

$m_{\text{count}}^{\text{RE}} = \lceil (G^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i)) / (N_L \cdot Q_m) \rceil$;

end if

for $j=0$ to $m_{\text{count}}^{\text{RE}}-1$

$k = \Phi_l^{\text{UCI}}(j)$;

for $v=0$ to $N_L \cdot Q_m - 1$

$\bar{g}_{l,k,v} = g_{m_{\text{count,all}}^{\text{ACK}}}^{\text{ACK}}$;

$m_{\text{count,all}}^{\text{ACK}} = m_{\text{count,all}}^{\text{ACK}} + 1$;

$m_{\text{count}}^{\text{ACK}}(i) = m_{\text{count}}^{\text{ACK}}(i) + 1$;

end for

end for

```

 $\bar{\Phi}_{l,tmp}^{UCI} = \emptyset$ ;

for  $j=0$  to  $m_{count}^{RE}-1$ 
     $\bar{\Phi}_{l,tmp}^{UCI} = \bar{\Phi}_{l,tmp}^{UCI} \cup \bar{\Phi}_l^{UCI}(j)$ ;
end for

 $\bar{\Phi}_l^{UCI} = \bar{\Phi}_l^{UCI} \setminus \bar{\Phi}_{l,tmp}^{UCI}$ ;

 $\bar{\Phi}_l^{UL-SCH} = \bar{\Phi}_l^{UL-SCH} \setminus \bar{\Phi}_{l,tmp}^{UCI}$ ;

 $\bar{M}_{sc}^{UCI}(l) = |\bar{\Phi}_l^{UCI}|$ ;

 $\bar{M}_{sc}^{UL-SCH}(l) = |\bar{\Phi}_l^{UL-SCH}|$ ;

end if

 $l=l+1$  ;

end while

end for

end if

```

Step 3:

if CSI is present for transmission on the PUSCH,

```

Set  $m_{count}^{CSI-part1}(1)=0$  ;

Set  $m_{count}^{CSI-part1}(2)=0$  ;

Set  $m_{count,all}^{CSI-part1}=0$  ;

for  $i=1$  to  $N_{hop}^{PUSCH}$ 
     $l=l_{CSI}^{(i)}$  ;

    while  $\bar{M}_{sc}^{UCI}(l) - \bar{M}_{sc,rvd}^{\Phi}(l) \neq 0$ 
         $l=l+1$  ;
    end while

    while  $m_{count}^{CSI-part1}(i) < G^{CSI-part1}(i)$ 
        if  $\bar{M}_{sc}^{UCI}(l) - \bar{M}_{sc,rvd}^{\Phi}(l) > 0$ 
            if  $G^{CSI-part1}(i) - m_{count}^{CSI-part1}(i) \neq (\bar{M}_{sc}^{UCI}(l) - \bar{M}_{sc,rvd}^{\Phi}(l)) \neq N_L \neq Q_m$ 

```

$$d=1 \quad ;$$

$$m_{\text{count}}^{\text{RE}} = \bar{M}_{\text{sc}}^{\text{UCI}}(l) - \bar{M}_{\text{sc,rvd}}^{\Phi}(l) \quad ;$$

end if

$$\text{if } G^{\text{CSI-part1}}(i) - m_{\text{count}}^{\text{CSI-part1}}(i) < \left(\bar{M}_{\text{sc}}^{\text{UCI}}(l) - \bar{M}_{\text{sc,rvd}}^{\Phi}(l) \right) \cdot N_L \cdot Q_m$$

$$d = \left\lceil \frac{\bar{M}_{\text{sc}}^{\text{UCI}}(l) - \bar{M}_{\text{sc,rvd}}^{\Phi}(l)}{G^{\text{CSI-part1}}(i) - m_{\text{count}}^{\text{CSI-part1}}(i)} \right\rceil$$

$$m_{\text{count}}^{\text{RE}} = \left\lceil \left(G^{\text{CSI-part1}}(i) - m_{\text{count}}^{\text{CSI-part1}}(i) \right) / (N_L \cdot Q_m) \right\rceil \quad ;$$

end if

$$\bar{\Phi}_l^{\text{temp}} = \bar{\Phi}_l^{\text{UCI}} \setminus \bar{\Phi}_l^{\text{rvd}} \quad ;$$

$$\text{for } j=0 \quad \text{to} \quad m_{\text{count}}^{\text{RE}} - 1$$

$$k = \bar{\Phi}_l^{\text{temp}}(j) \quad ;$$

$$\text{for } v=0 \quad \text{to} \quad N_L \cdot Q_m - 1$$

$$\bar{g}_{l,k,v} = g_{m_{\text{count,all}}^{\text{CSI-part1}}}^{\text{CSI-part1}} \quad ;$$

$$m_{\text{count,all}}^{\text{CSI-part1}} = m_{\text{count,all}}^{\text{CSI-part1}} + 1 \quad ;$$

$$m_{\text{count}}^{\text{CSI-part1}}(i) = m_{\text{count}}^{\text{CSI-part1}}(i) + 1 \quad ;$$

end for

end for

$$\bar{\Phi}_{l,tmp}^{\text{UCI}} = \bar{\Phi}_l^{\text{UCI}} \quad ;$$

$$\text{for } j=0 \quad \text{to} \quad m_{\text{count}}^{\text{RE}} - 1$$

$$\bar{\Phi}_{l,tmp}^{\text{UCI}} = \bar{\Phi}_{l,tmp}^{\text{UCI}} \cup \bar{\Phi}_l^{\text{temp}}(j) \quad ;$$

end for

$$\bar{\Phi}_l^{\text{UCI}} = \bar{\Phi}_l^{\text{UCI}} \setminus \bar{\Phi}_{l,tmp}^{\text{UCI}} \quad ;$$

$$\bar{\Phi}_l^{\text{UL-SCH}} = \bar{\Phi}_l^{\text{UL-SCH}} \setminus \bar{\Phi}_{l,tmp}^{\text{UCI}} \quad ;$$

$$\bar{M}_{\text{sc}}^{\text{UCI}}(l) = |\bar{\Phi}_l^{\text{UCI}}| \quad ;$$

$$\bar{M}_{\text{sc}}^{\text{UL-SCH}}(l) = |\bar{\Phi}_l^{\text{UL-SCH}}| \quad ;$$

end if

$$l=l+1 \quad ;$$

```

    end while
end for

Set  $m_{\text{count}}^{\text{CSI-part2}}(1)=0$  ;

Set  $m_{\text{count}}^{\text{CSI-part2}}(2)=0$  ;

Set  $m_{\text{count,all}}^{\text{CSI-part2}}=0$  ;

for  $i=1$  to  $N_{\text{hop}}^{\text{PUSCH}}$ 
     $l=l_{\text{CSI}}^{(i)}$  ;

    while  $\bar{M}_{\text{sc}}^{\text{UCI}}(l) \neq 0$ 
         $l=l+1$  ;
    end while

    while  $m_{\text{count}}^{\text{CSI-part2}}(i) < G^{\text{CSI-part2}}(i)$ 
        if  $\bar{M}_{\text{sc}}^{\text{UCI}}(l) > 0$ 
            if  $G^{\text{CSI-part2}}(i) - m_{\text{count}}^{\text{CSI-part2}}(i) \neq \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m$ 
                 $d=1$  ;

                 $m_{\text{count}}^{\text{RE}} = \bar{M}_{\text{sc}}^{\text{UCI}}(l)$  ;

            end if

            if  $G^{\text{CSI-part2}}(i) - m_{\text{count}}^{\text{CSI-part2}}(i) < \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m$ 
                 $d = \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m / (G^{\text{CSI-part2}}(i) - m_{\text{count}}^{\text{CSI-part2}}(i))$  ;

                 $m_{\text{count}}^{\text{RE}} = \lceil (G^{\text{CSI-part2}}(i) - m_{\text{count}}^{\text{CSI-part2}}(i)) / (N_L \cdot Q_m) \rceil$  ;

            end if

            for  $j=0$  to  $m_{\text{count}}^{\text{RE}} - 1$ 
                 $k = \bar{\Phi}_l^{\text{UCI}}(j)$  ;

                for  $v=0$  to  $N_L \cdot Q_m - 1$ 
                     $\bar{g}_{l,k,v} = g_{m_{\text{count,all}}^{\text{CSI-part2}}}^{\text{CSI-part2}}$  ;

                     $m_{\text{count,all}}^{\text{CSI-part2}} = m_{\text{count,all}}^{\text{CSI-part2}} + 1$  ;
                end for
            end for
        end if
    end while
end for

```

$$m_{\text{count}}^{\text{CSI-part2}}(i) = m_{\text{count}}^{\text{CSI-part2}}(i) + 1 ;$$

end for

end for

$$\bar{\Phi}_{l,\text{tmp}}^{\text{UCI}} = \Phi ;$$

for $j=0$ to $m_{\text{count}}^{\text{RE}} - 1$

$$\bar{\Phi}_{l,\text{tmp}}^{\text{UCI}} = \bar{\Phi}_{l,\text{tmp}}^{\text{UCI}} \cup \bar{\Phi}_l^{\text{UCI}}(j) ;$$

end for

$$\bar{\Phi}_l^{\text{UCI}} = \bar{\Phi}_l^{\text{UCI}} \setminus \bar{\Phi}_{l,\text{tmp}}^{\text{UCI}} ;$$

$$\bar{\Phi}_l^{\text{UL-SCH}} = \bar{\Phi}_l^{\text{UL-SCH}} \setminus \bar{\Phi}_{l,\text{tmp}}^{\text{UCI}} ;$$

$$\bar{M}_{\text{sc}}^{\text{UCI}}(l) = |\bar{\Phi}_l^{\text{UCI}}| ;$$

$$\bar{M}_{\text{sc}}^{\text{UL-SCH}}(l) = |\bar{\Phi}_l^{\text{UL-SCH}}| ;$$

end if

$$l = l + 1 ;$$

end while

end for

end if

Step 4:

if UL-SCH is present for transmission on the PUSCH,

Set $m_{\text{count}}^{\text{UL-SCH}} = 0$;

for $l=0$ to $N_{\text{symb,all}}^{\text{PUSCH}} - 1$

if $\bar{M}_{\text{sc}}^{\text{UL-SCH}}(l) > 0$

for $j=0$ to $\bar{M}_{\text{sc}}^{\text{UL-SCH}}(l) - 1$

$$k = \bar{\Phi}_l^{\text{UL-SCH}}(j) ;$$

for $v=0$ to $N_L \cdot Q_m - 1$

$$\bar{g}_{l,k,v} = g_{m_{\text{count}}^{\text{UL-SCH}}}^{\text{UL-SCH}} ;$$

$$m_{\text{count}}^{\text{UL-SCH}} = m_{\text{count}}^{\text{UL-SCH}} + 1 ;$$

end for

end for
 end if
 end for
 end if

Step 5:

if HARQ-ACK is present for transmission on the PUSCH and the number of HARQ-ACK information bits is no more than 2,

Set $m_{\text{count}}^{\text{ACK}}(1)=0$;

Set $m_{\text{count}}^{\text{ACK}}(2)=0$;

Set $m_{\text{count,all}}^{\text{ACK}}=0$;

for $i=1$ to $N_{\text{hop}}^{\text{PUSCH}}$

$l=l^{(i)}$;

while $m_{\text{count}}^{\text{ACK}}(i)<G^{\text{ACK}}(i)$

if $\bar{M}_{\text{sc,rvd}}^{\Phi}(l) > 0$

if $G^{\text{ACK}}(i)-m_{\text{count}}^{\text{ACK}}(i) \geq \bar{M}_{\text{sc,rvd}}^{\Phi}(l) \cdot N_L \cdot Q_m$

$d=1$;

$m_{\text{count}}^{\text{RE}} = \bar{M}_{\text{sc,rvd}}^{\Phi}(l)$;

end if

if $G^{\text{ACK}}(i)-m_{\text{count}}^{\text{ACK}}(i) < \bar{M}_{\text{sc,rvd}}^{\Phi}(l) \cdot N_L \cdot Q_m$

$d = \lceil \bar{M}_{\text{sc,rvd}}^{\Phi}(l) \cdot N_L \cdot Q_m / (G^{\text{ACK}}(i)-m_{\text{count}}^{\text{ACK}}(i)) \rceil$;

$m_{\text{count}}^{\text{RE}} = \lceil (G^{\text{ACK}}(i)-m_{\text{count}}^{\text{ACK}}(i)) / (N_L \cdot Q_m) \rceil$;

end if

for $j=0$ to $m_{\text{count}}^{\text{RE}}-1$

$k = \bar{\Phi}_l^{\text{rvd}}(j)$;

for $v=0$ to $N_L \cdot Q_m - 1$

$\bar{g}_{l,k,v} = g_{m_{\text{count,all}}^{\text{ACK}}}^{\text{ACK}}$;

$m_{\text{count,all}}^{\text{ACK}} = m_{\text{count,all}}^{\text{ACK}} + 1$;

end for

end for

$$m_{\text{count}}^{\text{ACK}}(i) = m_{\text{count}}^{\text{ACK}}(i) + 1 ;$$

end for

end for

end if

$$l = l + 1 ;$$

end while

end for

end if

Step 6:

Set $t=0$;

for $l=0$ to $N_{\text{symb,all}}^{\text{PUSCH}} - 1$

for $j=0$ to $M_{\text{sc}}^{\text{UL-SCH}}(l) - 1$

$$k = \Phi_l^{\text{UL-SCH}}(j) ;$$

for $v=0$ to $N_L \cdot Q_m - 1$

$$g_t = \bar{g}_{l,k,v} ;$$

$$t = t + 1 ;$$

end for

end for

end for

6.3 Uplink control information

6.3.1 Uplink control information on PUCCH

The procedure in this subclause applies to PUCCH formats 2/3/4.

6.3.1.1 UCI bit sequence generation

6.3.1.1.1 HARQ-ACK/SR only

If only HARQ-ACK bits are transmitted on a PUCCH, the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is determined by setting $a_i = \tilde{o}_i^{\text{ACK}}$ for $i=0, 1, \dots, O^{\text{ACK}} - 1$ and $A = O^{\text{ACK}}$, where the HARQ-ACK bit sequence $\tilde{o}_0^{\text{ACK}}, \{\tilde{o}_1^{\text{ACK}}, \dots, \tilde{o}_{O^{\text{ACK}}-1}^{\text{ACK}}\}$ is given by Subclause 9.1 of [5, TS38.213].

If only HARQ-ACK and SR bits are transmitted on a PUCCH, the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is determined by setting $a_i = \tilde{o}_i^{\text{ACK}}$ for $i=0, 1, \dots, O^{\text{ACK}} - 1$, $a_i = \tilde{o}_i^{\text{SR}}$ for $i = O^{\text{ACK}}, O^{\text{ACK}} + 1, \dots, O^{\text{ACK}} + O^{\text{SR}} - 1$, and $A = O^{\text{ACK}} + O^{\text{SR}}$, where the HARQ-ACK bit sequence

$\tilde{o}_0^{ACK}, \{\tilde{o}_1^{ACK}, \dots, \tilde{o}_{O_{ACK}-1}^{ACK}\}$ is given by Subclause 9.1 of [5, TS 38.213], and the SR bit sequence $\tilde{o}_0^{SR}, \{\tilde{o}_1^{SR}, \dots, \tilde{o}_{O_{SR}-1}^{SR}\}$ is given by Subclause 9.2.5.1 of [5, TS 38.213].

6.3.1.1.2 CSI only

The bitwidth for PMI of *codebookType=typeI-SinglePanel* with 2 CSI-RS ports is 2 for Rank=1 and 1 for Rank=2, according to Subclause 5.2.2.2.1 in [6, TS 38.214].

The bitwidth for PMI of *codebookType=typeI-SinglePanel* with more than 2 CSI-RS ports is provided in Tables 6.3.1.1.2-1, where the values of (N_1, N_2) and (O_1, O_2) are given by Subclause 5.2.2.2.1 in [6, TS 38.214].

Table 6.3.1.1.2-1: PMI of *codebookType=typeI-SinglePanel*

	Information field X_1 for wideband PMI			Information field X_2 for wideband PMI or per subband PMI	
	$(i_{1,1}, i_{1,2})$		$i_{1,3}$	i_2	
	<i>codebookMode</i> =1	<i>codebookMode</i> =2		<i>codebookMode</i> =1	<i>codebookMode</i> =2
Rank = 1 with >2 CSI-RS ports, $N_2 > 1$	$(\lceil \log_2 N_1 O_1 \rceil, \lceil \log_2 N_2 O_2 \rceil)$	$(\lceil \log_2 \frac{N_1 O_1}{2} \rceil, \lceil \log_2 \frac{N_2 O_2}{2} \rceil)$	N/A	2	4
Rank = 1 with >2 CSI-RS ports, $N_2 = 1$	$(\lceil \log_2 N_1 O_1 \rceil, \lceil \log_2 N_2 O_2 \rceil)$	$(\lceil \log_2 \left(\frac{N_1 O_1}{2} \right) \rceil, 0)$	N/A	2	4
Rank=2 with 4 CSI-RS ports, $N_2 = 1$	$(\lceil \log_2 N_1 O_1 \rceil, \lceil \log_2 N_2 O_2 \rceil)$	$(\lceil \log_2 \left(\frac{N_1 O_1}{2} \right) \rceil, 0)$	1	1	3
Rank=2 with >4 CSI-RS ports, $N_2 > 1$	$(\lceil \log_2 N_1 O_1 \rceil, \lceil \log_2 N_2 O_2 \rceil)$	$(\lceil \log_2 \frac{N_1 O_1}{2} \rceil, \lceil \log_2 \frac{N_2 O_2}{2} \rceil)$	2	1	3
Rank=2 with >4 CSI-RS ports, $N_2 = 1$	$(\lceil \log_2 N_1 O_1 \rceil, \lceil \log_2 N_2 O_2 \rceil)$	$(\lceil \log_2 \left(\frac{N_1 O_1}{2} \right) \rceil, 0)$	2	1	3
Rank=3 or 4, with 4 CSI-RS ports	$(\lceil \log_2 N_1 O_1 \rceil, \lceil \log_2 N_2 O_2 \rceil)$		0	1	
Rank=3 or 4, with 8 or 12 CSI-RS ports	$(\lceil \log_2 N_1 O_1 \rceil, \lceil \log_2 N_2 O_2 \rceil)$		2	1	

Rank=3 or 4 , with ≥ 16 CSI-RS ports	$(\text{g}_2 \frac{N_1 O_1}{2}, \text{g}_2 N_2 O_2)$	2	1
Rank=5 or 6	$(\text{g}_2 N_1 O_1, \text{g}_2 N_2 O_2)$	N/A	1
Rank=7 or 8, $N_1 = 4, N_2 = 1$	$(\text{g}_2 \frac{N_1 O_1}{2}, \text{g}_2 N_2 O_2)$	N/A	1
Rank=7 or 8, $N_1 > 2, N_2 = 2$	$(\text{g}_2 N_1 O_1, \text{g}_2 \frac{N_2 O_2}{2})$	N/A	1
Rank=7 or 8, with $N_1 > 4, N_2 = 1$ or $N_1 = 2, N_2 = 2$ or $N_1 > 2, N_2 > 2$	$(\text{g}_2 N_1 O_1, \text{g}_2 N_2 O_2)$	N/A	1

The bitwidth for PMI of *codebookType*= *typeI-MultiPanel* is provided in Tables 6.3.1.1.2-2, where the values of (N_g, N_1, N_2) and (O_1, O_2) are given by Subclause 5.2.2.2.2 in [6, TS 38.214].

Table 6.3.1.1.2-2: PMI of *codebookType*= *typeI-MultiPanel*

	Information fields X_1 for wideband					Information fields X_2 for wideband or per subband			
	$(i_{1,1}, i_{1,2})$	$i_{1,3}$	$i_{1,4,1}$	$i_{1,4,2}$	$i_{1,4,3}$	i_2	$i_{2,0}$	$i_{2,1}$	$i_{2,2}$
Rank=1 with $N_g=2$ <i>codebookMode</i> =1	$(\text{g}_2 N_1 O_1, \text{g}_2 N_2 O_2)$	N/A	2	N/A	N/A	2	N/A	N/A	N/A
Rank=1 with $N_g=4$ <i>codebookMode</i> =1	$(\text{g}_2 N_1 O_1, \text{g}_2 N_2 O_2)$	N/A	2	2	2	2	N/A	N/A	N/A
Rank=2 with $N_g=2$, $N_1 N_2 = 2$ <i>codebookMode</i> =1	$(\text{g}_2 N_1 O_1, \text{g}_2 N_2 O_2)$	1	2	N/A	N/A	1	N/A	N/A	N/A
Rank=3 or 4 with $N_g=2$, $N_1 N_2 = 2$ <i>codebookMode</i> =1	$(\text{g}_2 N_1 O_1, \text{g}_2 N_2 O_2)$	0	2	N/A	N/A	1	N/A	N/A	N/A
Rank=2 or 3 or 4 with $N_g=2$, $N_1 N_2 > 2$ <i>codebookMode</i> =1	$(\text{g}_2 N_1 O_1, \text{g}_2 N_2 O_2)$	2	2	N/A	N/A	1	N/A	N/A	N/A

Rank=2 with $N_g=4$, $N_1N_2=2$ <i>codebookMode=1</i>	$(\begin{matrix} \text{g}_2 N_1 O_1 \\ \text{g}_2 N_2 O_2 \end{matrix})$	1	2	2	2	1	N/A	N/A	N/A
Rank=3 or 4 with $N_g=4$, $N_1N_2=2$ <i>codebookMode=1</i>	$(\begin{matrix} \text{g}_2 N_1 O_1 \\ \text{g}_2 N_2 O_2 \end{matrix})$	0	2	2	2	1	N/A	N/A	N/A
Rank=2 or 3 or 4 with $N_g=4$, $N_1N_2 > 2$ <i>codebookMode=1</i>	$(\begin{matrix} \text{g}_2 N_1 O_1 \\ \text{g}_2 N_2 O_2 \end{matrix})$	2	2	2	2	1	N/A	N/A	N/A
Rank=1 with $N_g=2$ <i>codebookMode=2</i>	$(\begin{matrix} \text{g}_2 N_1 O_1 \\ \text{g}_2 N_2 O_2 \end{matrix})$	N/A	2	2	N/A	N/A	2	1	1
Rank=2 with $N_g=2$, $N_1N_2=2$ <i>codebookMode=2</i>	$(\begin{matrix} \text{g}_2 N_1 O_1 \\ \text{g}_2 N_2 O_2 \end{matrix})$	1	2	2	N/A	N/A	1	1	1
Rank=3 or 4 with $N_g=2$, $N_1N_2=2$ <i>codebookMode=2</i>	$(\begin{matrix} \text{g}_2 N_1 O_1 \\ \text{g}_2 N_2 O_2 \end{matrix})$	0	2	2	N/A	N/A	1	1	1
Rank=2 or 3 or 4 with $N_g=2$, $N_1N_2 > 2$ <i>codebookMode=2</i>	$(\begin{matrix} \text{g}_2 N_1 O_1 \\ \text{g}_2 N_2 O_2 \end{matrix})$	2	2	2	N/A	N/A	1	1	1

The bitwidth for PMI with 1 CSI-RS port is 0.

The bitwidth for RI/LI/CQI/CRI of *codebookType=typeI-SinglePanel* is provided in Tables 6.3.1.1.2-3.

Table 6.3.1.1.2-3: RI, LI, CQI, and CRI of *codebookType=typeI-SinglePanel*

Field	Bitwidth				
	1 antenna port	2 antenna ports	4 antenna ports	>4 antenna ports	
Rank Indicator	0	$\min(1, \lceil \log_2 n_{\text{RI}} \rceil)$	$\min(2, \lceil \log_2 n_{\text{RI}} \rceil)$	$\lceil \log_2 n_{\text{RI}} \rceil$	$\lceil \log_2 n_{\text{RI}} \rceil$
Layer Indicator	0	$\lceil \log_2 v \rceil$	$\min(2, \lceil \log_2 v \rceil)$	$\min(2, \lceil \log_2 v \rceil)$	$\min(2, \lceil \log_2 v \rceil)$
Wide-band CQI for the first TB	4	4	4	4	4
Wideband CQI for the second TB	0	0	0	0	4
Subband differential CQI for the first TB	2	2	2	2	2
Subband differential CQI for the second TB	0	0	0	0	2
CRI	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$

n_{RI} in Table 6.3.1.1.2-3 is the number of allowed rank indicator values according to Subclause 5.2.2.2.1 [6, TS 38.214]. v is the value of the rank. The value of $K_s^{\text{CSI-RS}}$ is the number of CSI-RS resources in the corresponding

resource set. The values of the rank indicator field are mapped to allowed rank indicator values with increasing order, where '0' is mapped to the smallest allowed rank indicator value.

The bitwidth for RI/LI/CQI/CRI of *codebookType*= *typeI-MultiPanel* is provided in Table 6.3.1.1.2-4.

Table 6.3.1.1.2-4: RI, LI, CQI, and CRI of *codebookType*=*typeI-MultiPanel*

Field	Bitwidth
Rank Indicator	$\min(2, \lceil \log_2 n_{\text{RI}} \rceil)$
Layer Indicator	$\min(2, \lceil \log_2 v \rceil)$
Wide-band CQI	4
Subband differential CQI	2
CRI	$\lceil \log_2 (K_s^{\text{CSI-RS}}) \rceil$

where n_{RI} is the number of allowed rank indicator values according to Subclause 5.2.2.2.2 [6, TS 38.214], v is the value of the rank, and $K_s^{\text{CSI-RS}}$ is the number of CSI-RS resources in the corresponding resource set. The values of the rank indicator field are mapped to allowed rank indicator values with increasing order, where '0' is mapped to the smallest allowed rank indicator value.

The bitwidth for RI/LI/CQI of *codebookType*= *typeII* or *codebookType*=*typeII-PortSelection* is provided in Table 6.3.1.1.2-5.

Table 6.3.1.1.2-5: RI, LI, and CQI of *codebookType*=*typeII* or *typeII-PortSelection*

Field	Bitwidth
Rank Indicator	$\min(1, \lceil \log_2 n_{\text{RI}} \rceil)$
Layer Indicator	$\min(2, \lceil \log_2 v \rceil)$
Wide-band CQI	4
Subband differential CQI	2
Indicator of the number of non-zero wideband amplitude coefficients M_l for layer l	$\lceil \log_2 (2L - 1) \rceil$

where n_{RI} is the number of allowed rank indicator values according to Subclauses 5.2.2.2.3 and 5.2.2.2.4 [6, TS 38.214] and v is the value of the rank. The values of the rank indicator field are mapped to allowed rank indicator values with increasing order, where '0' is mapped to the smallest allowed rank indicator value.

The bitwidth for CRI, SSBRI, RSRP, and differential RSRP are provided in Table 6.3.1.1.2-6.

Table 6.3.1.1.2-6: CRI, SSBRI, and RSRP

Field	Bitwidth
CRI	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$
SSBRI	$\lceil \log_2(K_s^{\text{SSB}}) \rceil$
RSRP	7
Differential RSRP	4

where $K_s^{\text{CSI-RS}}$ is the number of CSI-RS resources in the corresponding resource set, and K_s^{SSB} is the configured number of SS/PBCH blocks in the corresponding resource set for reporting 'ssb-Index-RSRP'.

Table 6.3.1.1.2-7: Mapping order of CSI fields of one CSI report, *pmi-FormatIndicator=widebandPMI* and *cqi-FormatIndicator=widebandCQI*

CSI report number	CSI fields
CSI report #n	CRI as in Tables 6.3.1.1.2-3/4, if reported
	Rank Indicator as in Tables 6.3.1.1.2-3/4, if reported
	Layer Indicator as in Tables 6.3.1.1.2-3/4, if reported
	Zero padding bits O_p , if needed
	PMI wideband information fields X_1 , from left to right as in Tables 6.3.1.1.2-1/2, if reported
	PMI wideband information fields X_2 , from left to right as in Tables 6.3.1.1.2-1/2, or codebook index for 2 antenna ports according to Subclause 5.2.2.2.1 in [6, TS38.214], if reported
	Wideband CQI for the first TB as in Tables 6.3.1.1.2-3/4, if reported
	Wideband CQI for the second TB as in Tables 6.3.1.1.2-3/4, if reported

The number of zero padding bits O_p in Table 6.3.1.1.2-7 is 0 for 1 CSI-RS port and $O_p = N_{\max} - N_{\text{reported}}$ for more than 1 CSI-RS port, where

- $N_{\max} = \max_{r \in S_{\text{Rank}}} B(r)$ and S_{Rank} is the set of rank values r that are allowed to be reported;
- $N_{\text{reported}} = B(R)$, where R is the reported rank;
- For 2 CSI-RS ports, $B(r) = N_{\text{PMI}}(r) + N_{\text{CQI}}(r) + N_{\text{LI}}(r)$;
- For more than 2 CSI-RS ports, $B(r) = N_{\text{PMI},i1}(r) + N_{\text{PMI},i2}(r) + N_{\text{CQI}}(r) + N_{\text{LI}}(r)$;
- if PMI is reported, $N_{\text{PMI}}(1) = 2$ and $N_{\text{PMI}}(2) = 1$; otherwise, $N_{\text{PMI}}(r) = 0$;
- if PMI $i1$ is reported, $N_{\text{PMI},i1}(r)$ is obtained according to Tables 6.3.1.1.2-1/2; otherwise, $N_{\text{PMI},i1}(r) = 0$;
- if PMI $i2$ is reported, $N_{\text{PMI},i2}(r)$ is obtained according to Tables 6.3.1.1.2-1/2; otherwise, $N_{\text{PMI},i2}(r) = 0$;
- if CQI is reported, $N_{\text{CQI}}(r)$ is obtained according to Tables 6.3.1.1.2-3/4; otherwise, $N_{\text{CQI}}(r) = 0$;
- if LI is reported, $N_{\text{LI}}(r)$ is obtained according to Tables 6.3.1.1.2-3/4; otherwise, $N_{\text{LI}}(r) = 0$.

Table 6.3.1.1.2-8: Mapping order of CSI fields of one report for CRI/RSRP or SSBRI/RSRP reporting

CSI report number	CSI fields
CSI report #n	CRI or SSBRI #1 as in Table 6.3.1.1.2-6, if reported
	CRI or SSBRI #2 as in Table 6.3.1.1.2-6, if reported
	CRI or SSBRI #3 as in Table 6.3.1.1.2-6, if reported
	CRI or SSBRI #4 as in Table 6.3.1.1.2-6, if reported
	RSRP #1 as in Table 6.3.1.1.2-6, if reported
	Differential RSRP #2 as in Table 6.3.1.1.2-6, if reported
	Differential RSRP #3 as in Table 6.3.1.1.2-6, if reported
	Differential RSRP #4 as in Table 6.3.1.1.2-6, if reported

Table 6.3.1.1.2-9: Mapping order of CSI fields of one CSI report, CSI part 1, *pmi-FormatIndicator*=*subbandPMI* or *cqi-FormatIndicator*=*subbandCQI*

CSI report number	CSI fields
CSI report #n CSI part 1	CRI as in Tables 6.3.1.1.2-3/4, if reported
	Rank Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	Wideband CQI for the first TB as in Tables 6.3.1.1.2-3/4/5, if reported
	Subband differential CQI for the first TB with increasing order of subband number as in Tables 6.3.1.1.2-3/4/5, if reported
	Indicator of the number of non-zero wideband amplitude coefficients M_0 for layer 0 as in Table 6.3.1.1.2-5, if reported
	Indicator of the number of non-zero wideband amplitude coefficients M_1 for layer 1 as in Table 6.3.1.1.2-5 (if the rank according to the reported RI is equal to one, this field is set to all zeros), if 2-layer PMI reporting is allowed according to the rank restriction in Subclauses 5.2.2.2.3 and 5.2.2.2.4 [6, TS 38.214] and if reported
Note: Subbands for given CSI report <i>n</i> indicated by the higher layer parameter <i>csi-ReportingBand</i> are numbered continuously in the increasing order with the lowest subband of <i>csi-ReportingBand</i> as subband 0.	

Table 6.3.1.1.2-10: Mapping order of CSI fields of one CSI report, CSI part 2 wideband, *pmi-FormatIndicator*=*subbandPMI* or *cqi-FormatIndicator*=*subbandCQI*

CSI report number	CSI fields
CSI report #n CSI part 2 wideband	Wideband CQI for the second TB as in Tables 6.3.1.1.2-3/4/5, if present and reported
	Layer Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	PMI wideband information fields X_1 , from left to right as in Tables 6.3.1.1.2-1/2, if reported
	PMI wideband information fields X_2 , from left to right as in Tables 6.3.1.1.2-1/2, or codebook index for 2 antenna ports according to Subclause 5.2.2.2.1 in [6, TS38.214], if <i>pmi-FormatIndicator</i> = <i>widebandPMI</i> and if reported

Table 6.3.1.1.2-11: Mapping order of CSI fields of one CSI report, CSI part 2 subband, *pmi-FormatIndicator*= *subbandPMI* or *cqi-FormatIndicator*=*subbandCQI*

CSI report #n Part 2 subband	Subband differential CQI for the second TB of all even subbands with increasing order of subband number, as in Tables 6.3.1.1.2-3/4/5, if <i>cqi-FormatIndicator</i> = <i>subbandCQI</i> and if reported
	PMI subband information fields X_2 of all even subbands with increasing order of subband number, from left to right as in Tables 6.3.1.1.2-1/2, or codebook index for 2 antenna ports according to Subclause 5.2.2.2.1 in [6, TS38.214] of all even subbands with increasing order of subband number, if <i>pmi-FormatIndicator</i> = <i>subbandPMI</i> and if reported
	Subband differential CQI for the second TB of all odd subbands with increasing order of subband number, as in Tables 6.3.1.1.2-3/4/5, if <i>cqi-FormatIndicator</i> = <i>subbandCQI</i> and if reported
	PMI subband information fields X_2 of all odd subbands with increasing order of subband number, from left to right as in Tables 6.3.1.1.2-1/2, or codebook index for 2 antenna ports according to Subclause 5.2.2.2.1 in [6, TS38.214] of all odd subbands with increasing order of subband number, if <i>pmi-FormatIndicator</i> = <i>subbandPMI</i> and if reported

Note: Subbands for given CSI report n indicated by the higher layer parameter *csi-ReportingBand* are numbered continuously in the increasing order with the lowest subband of *csi-ReportingBand* as subband 0.

If none of the CSI reports for transmission on a PUCCH is of two parts, the CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-12, are mapped to the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ starting with a_0 . The most significant bit of each field is mapped to the lowest order information bit for that field, e.g. the most significant bit of the first field is mapped to a_0 .

Table 6.3.1.1.2-12: Mapping order of CSI reports to UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, without two-part CSI report(s)

UCI bit sequence	CSI report number
a_0	CSI report #1 as in Table 6.3.1.1.2-7/8
a_1	CSI report #2 as in Table 6.3.1.1.2-7/8
a_2	...
a_3	
$\vdots$	
a_{A-1}	CSI report #n as in Table 6.3.1.1.2-7/8

If at least one of the CSI reports for transmission on a PUCCH is of two parts, two UCI bit sequences are generated, $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ and $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$. The CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-13, are mapped to the UCI bit sequence $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ starting with $a_0^{(1)}$. The most significant bit of each field is mapped to the lowest order information bit for that field, e.g. the most significant bit of the first field is mapped to $a_0^{(1)}$. The CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-14, are mapped to the UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ starting with $a_0^{(2)}$. The most significant bit of each field is mapped to the lowest order information bit for that field, e.g. the most significant bit of the first field is mapped to $a_0^{(2)}$. If the length of UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ is less than 3 bits, zeros shall be appended to the UCI bit sequence until its length equals 3.

Table 6.3.1.1.2-13: Mapping order of CSI reports to UCI bit sequence with two-part CSI report(s) $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$,

UCI bit sequence	CSI report number
$a_0^{(1)}$	CSI report #1 if CSI report #1 is not of two parts, or CSI report #1, CSI part 1, if CSI report #1 is of two parts, as in Table 6.3.1.1.2-7/8/9
$a_1^{(1)}$	CSI report #2 if CSI report #2 is not of two parts, or CSI report #2, CSI part 1, if CSI report #2 is of two parts, as in Table 6.3.1.1.2-7/8/9
$a_2^{(1)}$	...
$a_3^{(1)}$	
$\vdots$	
$a_{A^{(1)}-1}^{(1)}$	CSI report #n if CSI report #n is not of two parts, or CSI report #n, CSI part 1, if CSI report #n is of two parts, as in Table 6.3.1.1.2-7/8/9

where CSI report #1, CSI report #2, ..., CSI report #n in Table 6.3.1.1.2-13 correspond to the CSI reports in increasing order of CSI report priority values according to Subclause 5.2.5 of [6, TS38.214].

Table 6.3.1.1.2-14: Mapping order of CSI reports to UCI bit sequence with two-part CSI report(s) $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$,

UCI bit sequence	CSI report number
$a_0^{(2)}$ $a_1^{(2)}$ $a_2^{(2)}$ $a_3^{(2)}$ $\vdots$ $a_{A^{(2)}-1}^{(2)}$	CSI report #1, CSI part 2 wideband, as in Table 6.3.1.1.2-10 if CSI part 2 exists for CSI report #1
	CSI report #2, CSI part 2 wideband, as in Table 6.3.1.1.2-10 if CSI part 2 exists for CSI report #2
	...
	CSI report #n, CSI part 2 wideband, as in Table 6.3.1.1.2-10 if CSI part 2 exists for CSI report #n
	CSI report #1, CSI part 2 subband, as in Table 6.3.1.1.2-11 if CSI part 2 exists for CSI report #1
	CSI report #2, CSI part 2 subband, as in Table 6.3.1.1.2-11 if CSI part 2 exists for CSI report #2
	...
	CSI report #n, CSI part 2 subband, as in Table 6.3.1.1.2-11 if CSI part 2 exists for CSI report #n

where CSI report #1, CSI report #2, ..., CSI report #n in Table 6.3.1.1.2-14 correspond to the CSI reports in increasing order of CSI report priority values according to Subclause 5.2.5 of [6, TS38.214].

6.3.1.1.3 HARQ-ACK/SR and CSI

If none of the CSI reports for transmission on a PUCCH is of two parts, the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is generated according to the following, where $A = O^{\text{ACK}} + O^{\text{SR}} + O^{\text{CSI}}$:

- if there is HARQ-ACK for transmission on the PUCCH, the HARQ-ACK bits are mapped to the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{O^{\text{ACK}}-1}$, where $a_i = \tilde{o}_i^{\text{ACK}}$ for $i=0, 1, \dots, O^{\text{ACK}}-1$, the HARQ-ACK bit sequence $\tilde{o}_0^{\text{ACK}}, \{\tilde{o}_1^{\text{ACK}}, \dots, \tilde{o}_{O^{\text{ACK}}-1}^{\text{ACK}}\}$ is given by Subclause 9.1 of [5, TS38.213], and O^{ACK} is number of HARQ-ACK bits; if there is no HARQ-ACK for transmission on the PUCCH, set $O^{\text{ACK}}=0$;

- if there is SR for transmission on the PUCCH, set $a_i = \tilde{o}_i^{SR}$ for $i = O^{ACK}, O^{ACK}+1, \dots, O^{ACK}+O^{SR}-1$, where the SR bit sequence $\tilde{o}_0^{SR}, \{\tilde{o}_1^{SR}, \dots, \tilde{o}_{O^{SR}-1}^{SR}\}$ is given by Subclause 9.2.5.1 of [5, TS 38.213]; if there is no SR for transmission on the PUCCH, set $O^{SR}=0$;
- the CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-12, are mapped to the UCI bit sequence $a_{O^{ACK}+O^{SR}}, a_{O^{ACK}+O^{SR}+1}, \dots, a_{O^{ACK}+O^{SR}+O^{CSI}-1}$ starting with $a_{O^{ACK}+O^{SR}}$, where O^{CSI} is the number of CSI bits.

If at least one of the CSI reports for transmission on a PUCCH is of two parts, two UCI bit sequences are generated,

$a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ and $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$, according to the following, where $A^{(1)} = O^{ACK} + O^{SR} + O^{CSI-part1}$ and $A^{(2)} = O^{CSI-part2}$:

- if there is HARQ-ACK for transmission on the PUCCH, the HARQ-ACK bits are mapped to the UCI bit sequence $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{O^{ACK}-1}^{(1)}$, where $a_i^{(1)} = \tilde{o}_i^{ACK}$ for $i = 0, 1, \dots, O^{ACK}-1$, the HARQ-ACK bit sequence $\tilde{o}_0^{ACK}, \{\tilde{o}_1^{ACK}, \dots, \tilde{o}_{O^{ACK}-1}^{ACK}\}$ is given by Subclause 9.1 of [5, TS38.213], and O^{ACK} is number of HARQ-ACK bits; if there is no HARQ-ACK for transmission on the PUCCH, set $O^{ACK}=0$;
- if there is SR for transmission on the PUCCH, set $a_i = \tilde{o}_i^{SR}$ for $i = O^{ACK}, O^{ACK}+1, \dots, O^{ACK}+O^{SR}-1$, where the SR bit sequence $\tilde{o}_0^{SR}, \{\tilde{o}_1^{SR}, \dots, \tilde{o}_{O^{SR}-1}^{SR}\}$ is given by Subclause 9.2.5.1 of [5, TS 38.213]; if there is no SR for transmission on the PUCCH, set $O^{SR}=0$;
- the CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-13, are mapped to the UCI bit sequence $a_{O^{ACK}+O^{SR}}, a_{O^{ACK}+O^{SR}+1}, \dots, a_{O^{ACK}+O^{SR}+O^{CSI-part1}-1}^{(1)}$ starting with $a_{O^{ACK}+O^{SR}}^{(1)}$, where $O^{CSI-part1}$ is the number of CSI bits in CSI part 1 of all CSI reports;
- the CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-14, are mapped to the UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ starting with $a_0^{(2)}$, where $O^{CSI-part2}$ is the number of CSI bits in CSI part 2 of all CSI reports. If the length of UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ is less than 3 bits, zeros shall be appended to the UCI bit sequence until its length equals 3.

6.3.1.2 Code block segmentation and CRC attachment

The UCI bit sequence from subclause 6.3.1.1 is denoted by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, where A is the payload size. The procedure in 6.3.1.2.1 applies for $A \geq 12$ and the procedure in Subclause 6.3.1.2.2 applies for $A \leq 11$.

6.3.1.2.1 UCI encoded by Polar code

If the payload size $A \geq 12$, code block segmentation and CRC attachment is performed according to Subclause 5.2.1. If ($A \geq 360$ and $E \geq 1088$) or if $A \neq 1013$, $I_{seg} = 1$; otherwise $I_{seg} = 0$, where E is the rate matching output sequence length as given in Subclause 6.3.1.4.1.

If $12 \leq A \leq 19$, the parity bits $p_{r0}, p_{r1}, p_{r2}, \dots, p_{r(L-1)}$ in Subclause 5.2.1 are computed by setting L to 6 bits and using the generator polynomial $g_{CRC6}(D)$ in Subclause 5.1, resulting in the sequence $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r(K_r-1)}$ where r is the code block number and K_r is the number of bits for code block number r .

If $A \geq 20$, the parity bits $p_{r0}, p_{r1}, p_{r2}, \dots, p_{r(L-1)}$ in Subclause 5.2.1 are computed by setting L to 11 bits and using the generator polynomial $g_{CRC11}(D)$ in Subclause 5.1, resulting in the sequence $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r(K_r-1)}$ where r is the code block number and K_r is the number of bits for code block number r .

6.3.1.2.2 UCI encoded by channel coding of small block lengths

If the payload size $A \leq 11$, CRC bits are not attached.

The output bit sequence is denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where $c_i = a_i$ for $i=0, 1, \dots, A-1$ and $K=A$.

6.3.1.3 Channel coding of UCI

6.3.1.3.1 UCI encoded by Polar code

Information bits are delivered to the channel coding block. They are denoted by $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r(K_r-1)}$, where r is the code block number, and K_r is the number of bits in code block number r . The total number of code blocks is denoted by C and each code block is individually encoded by the following:

If $18 \leq K_r \leq 25$, the information bits are encoded via Polar coding according to Subclause 5.3.1, by setting $n_{\max} = 10$, $I_{\text{IL}} = 0$, $n_{\text{PC}} = 3$, $n_{\text{PC}}^{\text{wm}} = 1$ if $E_r - K_r + 3 > 192$ and $n_{\text{PC}}^{\text{wm}} = 0$ if $E_r - K_r + 3 \leq 192$, where E_r is the rate matching output sequence length as given in Subclause 6.3.1.4.1.

If $K_r > 30$, the information bits are encoded via Polar coding according to Subclause 5.3.1, by setting $n_{\max} = 10$, $I_{\text{IL}} = 0$, $n_{\text{PC}} = 0$, and $n_{\text{PC}}^{\text{wm}} = 0$.

After encoding the bits are denoted by $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$, where N_r is the number of coded bits in code block number r .

6.3.1.3.2 UCI encoded by channel coding of small block lengths

Information bits are delivered to the channel coding block. They are denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits.

The information bits are encoded according to Subclause 5.3.3.

After encoding the bits are denoted by $d_0, d_1, d_2, d_3, \dots, d_{N-1}$, where N is the number of coded bits.

6.3.1.4 Rate matching

For PUCCH formats 2/3/4, the total rate matching output sequence length E_{tot} is given by Table 6.3.1.4-1, where $N_{\text{symb, UCI}}^{\text{PUCCH, 2}}$, $N_{\text{symb, UCI}}^{\text{PUCCH, 3}}$, and $N_{\text{symb, UCI}}^{\text{PUCCH, 4}}$ are the number of symbols carrying UCI for PUCCH formats 2/3/4 respectively; $N_{\text{PRB}}^{\text{PUCCH, 2}}$ and $N_{\text{PRB}}^{\text{PUCCH, 3}}$ are the number of PRBs that are determined by the UE for PUCCH formats 2/3 transmission respectively according to Subclause 9.2 of [5, TS38.213]; and $N_{\text{SF}}^{\text{PUCCH, 4}}$ is the spreading factor for PUCCH format 4.

Table 6.3.1.4-1: Total rate matching output sequence length E_{tot}

PUCCH format	Modulation order	
	QPSK	$\pi/2$ -BPSK
PUCCH format 2	$16 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 2}} \cdot N_{\text{PRB}}^{\text{PUCCH, 2}}$	N/A
PUCCH format 3	$24 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 3}} \cdot N_{\text{PRB}}^{\text{PUCCH, 3}}$	$12 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 3}} \cdot N_{\text{PRB}}^{\text{PUCCH, 3}}$
PUCCH format 4	$24 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 4}} / N_{\text{SF}}^{\text{PUCCH, 4}}$	$12 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 4}} / N_{\text{SF}}^{\text{PUCCH, 4}}$

6.3.1.4.1 UCI encoded by Polar code

The input bit sequence to rate matching is $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$ where r is the code block number, and N_r is the number of coded bits in code block number r .

Table 6.3.1.4.1-1: Rate matching output sequence length E_{UCI}

UCI(s) for transmission on a PUCCH	UCI for encoding	Value of E_{UCI}
HARQ-ACK	HARQ-ACK	$E_{\text{UCI}} = E_{\text{tot}}$
HARQ-ACK, SR	HARQ-ACK, SR	$E_{\text{UCI}} = E_{\text{tot}}$
CSI (CSI not of two parts)	CSI	$E_{\text{UCI}} = E_{\text{tot}}$
HARQ-ACK, CSI (CSI not of two parts)	HARQ-ACK, CSI	$E_{\text{UCI}} = E_{\text{tot}}$
HARQ-ACK, SR, CSI (CSI not of two parts)	HARQ-ACK, SR, CSI	$E_{\text{UCI}} = E_{\text{tot}}$
CSI (CSI of two parts)	CSI part 1	$E_{\text{UCI}} = \min(E_{\text{tot}}, \lceil (O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\text{max}} / Q_m \rceil \cdot Q_m)$
	CSI part 2	$E_{\text{UCI}} = E_{\text{tot}} - \min(E_{\text{tot}}, \lceil (O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\text{max}} / Q_m \rceil \cdot Q_m)$
HARQ-ACK, CSI (CSI of two parts)	HARQ-ACK, CSI part 1	$E_{\text{UCI}} = \min(E_{\text{tot}}, \lceil (O^{\text{ACK}} + O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\text{max}} / Q_m \rceil \cdot Q_m)$
	CSI part 2	$E_{\text{UCI}} = E_{\text{tot}} - \min(E_{\text{tot}}, \lceil (O^{\text{ACK}} + O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\text{max}} / Q_m \rceil \cdot Q_m)$
HARQ-ACK, SR, CSI (CSI of two parts)	HARQ-ACK, SR, CSI part 1	$E_{\text{UCI}} = \min(E_{\text{tot}}, \lceil (O^{\text{ACK}} + O^{\text{SR}} + O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\text{max}} / Q_m \rceil \cdot Q_m)$
	CSI part 2	$E_{\text{UCI}} = E_{\text{tot}} - \min(E_{\text{tot}}, \lceil (O^{\text{ACK}} + O^{\text{SR}} + O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\text{max}} / Q_m \rceil \cdot Q_m)$

Rate matching is performed according to Subclause 5.4.1 by setting $I_{\text{BL}} = 1$ and the rate matching output sequence length to $E_r = \lceil E_{\text{UCI}} / C_{\text{UCI}} \rceil$, where C_{UCI} is the number of code blocks for UCI determined according to Subclause 6.3.1.2.1 and the value of E_{UCI} is given by Table 6.3.1.4.1-1:

- O^{ACK} is the number of bits for HARQ-ACK for transmission on the current PUCCH;
- O^{SR} is the number of bits for SR for transmission on the current PUCCH;
- $O^{\text{CSI-part1}}$ is the number of bits for CSI part 1 for transmission on the current PUCCH;
- $O^{\text{CSI-part2}}$ is the number of bits for CSI part 2 for transmission on the current PUCCH;
- if $A \geq 360$, $L = 11$; otherwise, L is the number of CRC bits determined according to subclause 6.3.1.2.1, where A equals $O^{\text{CSI-part1}}$ for "CSI (CSI of two parts)", equals $O^{\text{ACK}} + O^{\text{CSI-part1}}$ for "HARQ-ACK, CSI (CSI of two parts)", and equals $O^{\text{ACK}} + O^{\text{SR}} + O^{\text{CSI-part1}}$ for "HARQ-ACK, SR, CSI (CSI of two parts)" respectively in Table 6.3.1.4.1-1;;
- $R_{\text{UCI}}^{\text{max}}$ is the configured maximum PUCCH coding rate;
- E_{tot} is given by Table 6.3.1.4-1.

The output bit sequence after rate matching is denoted as $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$ where E_r is the length of rate matching output sequence in code block number r .

6.3.1.4.2 UCI encoded by channel coding of small block lengths

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$.

The value of E_{UCI} is determined according to Table 6.3.1.4.1-1 by setting $L=0$.

Rate matching is performed according to Subclause 5.4.3 by setting the rate matching output sequence length $E=E_{\text{UCI}}$.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

6.3.1.5 Code block concatenation

The input bit sequence for the code block concatenation block are the sequences $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$, for $r=0, \dots, C-1$ and where E_r is the number of rate matched bits for the r -th code block.

Code block concatenation is performed according to Subclause 5.5.

The bits after code block concatenation are denoted by $g_0, g_1, g_2, g_3, \dots, g_{G'-1}$, where $G' = \lceil E_{\text{UCI}} / C_{\text{UCI}} \rceil \cdot C_{\text{UCI}}$ with the values of E_{UCI} and C_{UCI} given in Subclause 6.3.1.4.1. Let G be the total number of coded bits for transmission and $G = G' + \text{mod}(E_{\text{UCI}}, C_{\text{UCI}})$. Set $g_i = 0$ for $i = G', G'+1, \dots, G-1$.

6.3.1.6 Multiplexing of coded UCI bits to PUCCH

If CSI of two parts are transmitted on a PUCCH, the coded bits corresponding to UCI bit sequence

$a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ is denoted by $g_0^{(1)}, g_1^{(1)}, g_2^{(1)}, g_3^{(1)}, \dots, g_{G^{(1)}-1}^{(1)}$ and the coded bits corresponding to UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ is denoted by $g_0^{(2)}, g_1^{(2)}, g_2^{(2)}, g_3^{(2)}, \dots, g_{G^{(2)}-1}^{(2)}$. The coded bit sequence $g_0, g_1, g_2, g_3, \dots, g_{G-1}$, where $G = G^{(1)} + G^{(2)}$, is generated according to the following.

Table 6.3.1.6-1: PUCCH DMRS and UCI symbols

PUCCH duration (symbols)	PUCCH DMRS symbol indices	Number of UCI symbol indices sets $N_{\text{UCI}}^{\text{set}}$	1 st UCI symbol indices set $S_{\text{UCI}}^{(1)}$	2 nd UCI symbol indices set $S_{\text{UCI}}^{(2)}$	3 rd UCI symbol indices set $S_{\text{UCI}}^{(3)}$
4	{1}	2	{0,2}	{3}	-
4	{0,2}	1	{1,3}	-	-
5	{0, 3}	1	{1, 2, 4}	-	-
6	{1, 4}	1	{0, 2, 3, 5}	-	-
7	{1, 4}	2	{0, 2, 3, 5}	{6}	-
8	{1, 5}	2	{0, 2, 4, 6}	{3, 7}	-
9	{1, 6}	2	{0, 2, 5, 7}	{3, 4, 8}	-
10	{2, 7}	2	{1, 3, 6, 8}	{0, 4, 5, 9}	-
10	{1, 3, 6, 8}	1	{0, 2, 4, 5, 7, 9}	-	-
11	{2, 7}	3	{1, 3, 6, 8}	{0, 4, 5, 9}	{10}
11	{1, 3, 6, 9}	1	{0, 2, 4, 5, 7, 8, 10}	-	-
12	{2, 8}	3	{1, 3, 7, 9}	{0, 4, 6, 10}	{5, 11}
12	{1, 4, 7, 10}	1	{0, 2, 3, 5, 6, 8, 9, 11}	-	-
13	{2, 9}	3	{1, 3, 8, 10}	{0, 4, 7, 11}	{5, 6, 12}
13	{1, 4, 7, 11}	2	{0, 2, 3, 5, 6, 8, 10, 12}	{9}	-
14	{3, 10}	3	{2, 4, 9, 11}	{1, 5, 8, 12}	{0, 6, 7, 13}
14	{1, 5, 8, 12}	2	{0, 2, 4, 6, 7, 9, 11, 13}	{3, 10}	-

Denote s_l as UCI OFDM symbol index. Denote $N_{\text{UCI}}^{(i)}$ as the number of elements in UCI symbol indices set $S_{\text{UCI}}^{(i)}$ for $i=1, \dots, N_{\text{UCI}}^{\text{set}}$, where $S_{\text{UCI}}^{(i)}$ and $N_{\text{UCI}}^{\text{set}}$ are given by Table 6.3.1.6-1 according to the PUCCH

duration and the PUCCH DMRS configuration. Denote $N_{\text{symb, UCI}}^{\text{PUCCH}} = \sum_{i=1}^{N_{\text{UCI}}^{\text{set}}} N_{\text{UCI}}^{(i)}$ as the number of OFDM symbols carrying UCI in the PUCCH. Denote Q_m as the modulation order of the PUCCH.

For PUCCH format 3, set $N_{\text{UCI}}^{\text{symbol}} = 12 \cdot N_{\text{PRB}}^{\text{PUCCH,3}}$, where $N_{\text{PRB}}^{\text{PUCCH,3}}$ is the number of PRBs that is determined by the UE for PUCCH format 3 transmission according to Subclause 9.2 of [5, TS 38.213].

For PUCCH format 4, set $N_{\text{UCI}}^{\text{symbol}} = 12 / N_{\text{SF}}^{\text{PUCCH,4}}$, where $N_{\text{SF}}^{\text{PUCCH,4}}$ is the spreading factor for PUCCH format 4.

Find the smallest $j > 0$ such that $\left(\sum_{i=1}^j N_{\text{UCI}}^{(i)} \right) \cdot N_{\text{UCI}}^{\text{symbol}} \cdot Q_m \geq G^{(1)}$.

Set $n_1 = 0$;

Set $n_2 = 0$;

Set $\bar{N}_{\text{UCI}}^{\text{symbol}} = \left\lceil \left(G^{(1)} - \left(\sum_{i=1}^{j-1} N_{\text{UCI}}^{(i)} \right) \cdot N_{\text{UCI}}^{\text{symbol}} \cdot Q_m \right) / \left(N_{\text{UCI}}^{(j)} \cdot Q_m \right) \right\rceil$;

Set $M = \text{mod} \left(\left(G^{(1)} - \left(\sum_{i=1}^{j-1} N_{\text{UCI}}^{(i)} \right) \cdot N_{\text{UCI}}^{\text{symbol}} \cdot Q_m \right) / Q_m, N_{\text{UCI}}^{(j)} \right)$;

for $l = 0$ to $N_{\text{symb, UCI}}^{\text{PUCCH}} - 1$

if $s_l \in \bigcup_{i=1}^{j-1} S_{\text{UCI}}^{(i)}$

for $k = 0$ to $N_{\text{UCI}}^{\text{symbol}} - 1$

for $v = 0$ to $Q_m - 1$

$\bar{g}_{l,k,v} = g_{n_1}^{(1)}$;

$n_1 = n_1 + 1$;

end for

end for

elseif $s_l \in S_{\text{UCI}}^{(j)}$

if $M > 0$

$\gamma = 1$;

else

$\gamma = 0$;

end if

$M = M - 1$;

```

for  $k=0$  to  $\bar{N}_{\text{UCI}}^{\text{symbol}} + \gamma - 1$ 
    for  $v=0$  to  $Q_m - 1$ 
         $\bar{g}_{l,k,v} = g_{n_1}^{(1)}$  ;

         $n_1 = n_1 + 1$  ;

    end for
end for

for  $k = \bar{N}_{\text{UCI}}^{\text{symbol}} + \gamma$  to  $N_{\text{UCI}}^{\text{symbol}} - 1$ 
    for  $v=0$  to  $Q_m - 1$ 
         $\bar{g}_{l,k,v} = g_{n_2}^{(2)}$  ;

         $n_2 = n_2 + 1$  ;

    end for
end for

else
    for  $k=0$  to  $N_{\text{UCI}}^{\text{symbol}} - 1$ 
        for  $v=0$  to  $Q_m - 1$ 
             $\bar{g}_{l,k,v} = g_{n_2}^{(2)}$  ;

             $n_2 = n_2 + 1$  ;

        end for
    end for

end if
end for

Set  $n=0$ 

for  $l=0$  to  $N_{\text{symb,UCI}}^{\text{PUCCH}} - 1$ 
    for  $k=0$  to  $N_{\text{UCI}}^{\text{symbol}} - 1$ 
        for  $v=0$  to  $Q_m - 1$ 
             $g_n = \bar{g}_{l,k,v}$  ;

             $n = n + 1$  ;

        end for
    end for
end for

```

end for

6.3.2 Uplink control information on PUSCH

6.3.2.1 UCI bit sequence generation

6.3.2.1.1 HARQ-ACK

If HARQ-ACK bits are transmitted on a PUSCH, the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is determined as follows:

- If UCI is transmitted on PUSCH without UL-SCH and the UCI includes CSI part 1 without CSI part 2,
- if there is no HARQ-ACK bit given by Subclause 9.1 of [5, TS 38.213], set $a_0=0$, $a_1=0$, and $A=2$;
- if there is only one HARQ-ACK bit $\tilde{o}_0^{ACK}$ given by Subclause 9.1 of [5, TS 38.213], set $a_0=\tilde{o}_0^{ACK}$, $a_1=0$, and $A=2$;
- otherwise, set $a_i=\tilde{o}_i^{ACK}$ for $i=0, 1, \dots, O^{ACK}-1$ and $A=O^{ACK}$, where the HARQ-ACK bit sequence $\tilde{o}_0^{ACK}, \{\tilde{o}_1^{ACK}, \dots, \tilde{o}_{O^{ACK}-1}^{ACK}\}$ is given by Subclause 9.1 of [5, TS 38.213].

6.3.2.1.2 CSI

The bitwidth for PMI of *codebookType=typeI-SinglePanel* and *codebookType=typeI-MultiPanel* is specified in Subclause 6.3.1.1.2.

The bitwidth for RI/LI/CQI/CRI of *codebookType=typeI-SinglePanel* and *codebookType=typeI-MultiPanel* is specified in Subclause 6.3.1.1.2.

The bitwidth for PMI of *codebookType=typeII* is provided in Tables 6.3.2.1.2-1, where the values of (N_1, N_2) , (O_1, O_2) , L , N_{PSK} , M_1 , M_2 , and $K^{(2)}$ are given by Subclause 5.2.2.2.3 in [6, TS 38.214].

Table 6.3.2.1.2-1: PMI of *codebookType= typeII*

	Information fields X_1 for wideband PMI						Information fields X_2 for wideband PMI or per subband PMI			
	$i_{1,1}$	$i_{1,2}$	$i_{1,3,1}$	$i_{1,4,1}$	$i_{1,3,2}$	$i_{1,4,2}$	$i_{2,1,1}$	$i_{2,1,2}$	$i_{2,2,1}$	$i_{2,2,2}$
Rank=1 SBamp off	$\lceil \log_2(O_1 O_2) \rceil$	$\lceil \log_2\left(\frac{N_1 N_2}{L}\right) \rceil$	$\lceil \log_2(2L) \rceil$	$3\lceil 2L \rceil$	N/A	N/A	$(M_1 - 1) \cdot \log_2 N_{PSK}$	N/A	N/A	N/A
Rank=2 SBamp off	$\lceil \log_2(O_1 O_2) \rceil$	$\lceil \log_2\left(\frac{N_1 N_2}{L}\right) \rceil$	$\lceil \log_2(2L) \rceil$	$3\lceil 2L \rceil$	$\lceil \log_2(2L) \rceil$	$3\lceil 2L \rceil$	$(M_1 - 1) \cdot \log_2 N_{PSK}$	$(M_2 - 1) \cdot \log_2 N_{PSK}$	N/A	N/A
Rank=1 SBamp on	$\lceil \log_2(O_1 O_2) \rceil$	$\lceil \log_2\left(\frac{N_1 N_2}{L}\right) \rceil$	$\lceil \log_2(2L) \rceil$	$3\lceil 2L \rceil$	N/A	N/A	$\min(M_1, K^{(2)}) \cdot \log_2 N_{PSK} - \log_2 N_{PSK} + 2 \cdot (M_1 - \min(M_1, K^{(2)}))$	N/A	$\min(M_1, K^{(2)}) - 1$	N/A

Rank=2 SBAmpon	$\lceil \log_2(O_1 O_2) \rceil$	$\lceil \log_2 \left(\frac{N_1 N_2}{L} \right) \rceil$	$\lceil \log_2(2L) \rceil$	$3(2L-1)$	$\lceil \log_2(2L) \rceil$	$3(2L-1)$	$\min(M_1, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_1 - \min(M_1, K^{(2)}))$	$\min(M_2, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_2 - \min(M_2, K^{(2)}))$	$\min(M_1, K^{(2)}) - 1$	$\min(M_2, K^{(2)}) - 1$
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The bitwidth for PMI of *codebookType= typeII-PortSelection* is provided in Tables 6.3.2.1.2-2, where the values of $P_{\text{CSI-RS}}$, d , L , N_{PSK} , M_1 , M_2 , and $K^{(2)}$ are given by Subclause 5.2.2.2.4 in [6, TS 38.214].

Table 6.3.2.1.2-2: PMI of *codebookType= typeII-PortSelection*

	Information fields X_1 for wideband PMI					Information fields X_2 for wideband PMI or per subband PMI			
	$i_{1,1}$	$i_{1,3,1}$	$i_{1,4,1}$	$i_{1,3,2}$	$i_{1,4,2}$	$i_{2,1,1}$	$i_{2,1,2}$	$i_{2,2,1}$	$i_{2,2,2}$
Rank=1 SBAmpon	$\lceil \log_2 \left[\frac{P_{\text{CSI-RS}}}{2d} \right] \rceil$	$\lceil \log_2(2L) \rceil$	$3(2L-1)$	N/A	N/A	$(M_1 - 1) \cdot \log_2 N_{\text{PSK}}$	N/A	N/A	N/A
Rank=2 SBAmpon	$\lceil \log_2 \left[\frac{P_{\text{CSI-RS}}}{2d} \right] \rceil$	$\lceil \log_2(2L) \rceil$	$3(2L-1)$	$\lceil \log_2(2L) \rceil$	$3(2L-1)$	$(M_1 - 1) \cdot \log_2 N_{\text{PSK}}$	$(M_2 - 1) \cdot \log_2 N_{\text{PSK}}$	N/A	N/A
Rank=1 SBAmpon	$\lceil \log_2 \left[\frac{P_{\text{CSI-RS}}}{2d} \right] \rceil$	$\lceil \log_2(2L) \rceil$	$3(2L-1)$	N/A	N/A	$\min(M_1, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_1 - \min(M_1, K^{(2)}))$	N/A	$\min(M_1, K^{(2)}) - 1$	N/A
Rank=2 SBAmpon	$\lceil \log_2 \left[\frac{P_{\text{CSI-RS}}}{2d} \right] \rceil$	$\lceil \log_2(2L) \rceil$	$3(2L-1)$	$\lceil \log_2(2L) \rceil$	$3(2L-1)$	$\min(M_1, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_1 - \min(M_1, K^{(2)}))$	$\min(M_2, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_2 - \min(M_2, K^{(2)}))$	$\min(M_1, K^{(2)}) - 1$	$\min(M_2, K^{(2)}) - 1$

For CSI on PUSCH, two UCI bit sequences are generated, $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ and

$a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$. The CSI fields of all CSI reports, in the order from upper part to lower part in Table

6.3.2.1.2-6, are mapped to the UCI bit sequence $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ starting with $a_0^{(1)}$. The CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.2.1.2-7, are mapped to the UCI bit sequence

$a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ starting with $a_0^{(2)}$.

Table 6.3.2.1.2-3: Mapping order of CSI fields of one CSI report, CSI part 1

CSI report number	CSI fields
CSI report #n CSI part 1	CRI or SSBRI as in Tables 6.3.1.1.2-3/4/6, if reported
	Rank Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	Wideband CQI for the first TB as in Tables 6.3.1.1.2-3/4/5, if reported
	Subband differential CQI for the first TB with increasing order of subband number as in Tables 6.3.1.1.2-3/4/5, if reported
	Indicator of the number of non-zero wideband amplitude coefficients M_0 for layer 0 as in Table 6.3.1.1.2-5, if reported
	Indicator of the number of non-zero wideband amplitude coefficients M_1 for layer 1 as in Table 6.3.1.1.2-5 (if the rank according to the reported RI is equal to one, this field is set to all zeros), if 2-layer PMI reporting is allowed according to the rank restriction in Subclauses 5.2.2.2.3 and 5.2.2.2.4 [6, TS 38.214] and if reported
	RSRP as in Table 6.3.1.1.2-6, if reported
Note: Subbands for given CSI report n indicated by the higher layer parameter <i>csi-ReportingBand</i> are numbered continuously in the increasing order with the lowest subband of <i>csi-ReportingBand</i> as subband 0.	

Table 6.3.2.1.2-4: Mapping order of CSI fields of one CSI report, CSI part 2 wideband

CSI report number	CSI fields
CSI report #n CSI part 2 wideband	Wideband CQI for the second TB as in Tables 6.3.1.1.2-3/4/5, if present and reported
	Layer Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	PMI wideband information fields X_1 , from left to right as in Tables 6.3.1.1.2-1/2 or 6.3.2.1.2-1/2, if reported
	PMI wideband information fields X_2 , from left to right as in Tables 6.3.1.1.2-1/2 or 6.3.2.1.2-1/2, or codebook index for 2 antenna ports according to Subclause 5.2.2.2.1 in [6, TS38.214], if <i>pmi-FormatIndicator</i> = <i>widebandPMI</i> and if reported

Table 6.3.2.1.2-5: Mapping order of CSI fields of one CSI report, CSI part 2 subband

CSI report #n Part 2 subband	Subband differential CQI for the second TB of all even subbands with increasing order of subband number, as in Tables 6.3.1.1.2-3/4/5, if <i>cqi-FormatIndicator</i> = <i>subbandCQI</i> and if reported
	PMI subband information fields X_2 of all even subbands with increasing order of subband number, from left to right as in Tables 6.3.1.1.2-1/2 or 6.3.2.1.2-1/2, or codebook index for 2 antenna ports according to Subclause 5.2.2.2.1 in [6, TS38.214] of all even subbands with increasing order of subband number, if <i>pmi-FormatIndicator</i> = <i>subbandPMI</i> and if reported
	Subband differential CQI for the second TB of all odd subbands with increasing order of subband number, as in Tables 6.3.1.1.2-3/4/5, if <i>cqi-FormatIndicator</i> = <i>subbandCQI</i> and if reported
	PMI subband information fields X_2 of all odd subbands with increasing order of subband number, from left to right as in Tables 6.3.1.1.2-1/2 or 6.3.2.1.2-1/2, or codebook index for 2 antenna ports according to Subclause 5.2.2.2.1 in [6, TS38.214] of all odd subbands with increasing order of subband number, if <i>pmi-FormatIndicator</i> = <i>subbandPMI</i> and if reported

Note: Subbands for given CSI report n indicated by the higher layer parameter *csi-ReportingBand* are numbered continuously in the increasing order with the lowest subband of *csi-ReportingBand* as subband 0.

Table 6.3.2.1.2-6: Mapping order of CSI reports to UCI bit sequence with two-part CSI report(s) $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$,

UCI bit sequence	CSI report number
$a_0^{(1)}$	CSI part 1 of CSI report #1 as in Table 6.3.2.1.2-3
$a_1^{(1)}$	CSI part 1 of CSI report #2 as in Table 6.3.2.1.2-3
$a_2^{(1)}$	
$a_3^{(1)}$	...
$\vdots$	
$a_{A^{(1)}-1}^{(1)}$	CSI part 1 of CSI report #n as in Table 6.3.2.1.2-3

where CSI report #1, CSI report #2, ..., CSI report #n in Table 6.3.2.1.2-6 correspond to the CSI reports in increasing order of CSI report priority values according to Subclause 5.2.5 of [6, TS38.214].

Table 6.3.2.1.2-7: Mapping order of CSI reports to UCI bit sequence with two-part CSI report(s) $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$,

UCI bit sequence	CSI report number
	CSI report #1, CSI part 2 wideband, as in Table 6.3.2.1.2-4 if CSI part 2 exists for CSI report #1
	CSI report #2, CSI part 2 wideband, as in Table 6.3.2.1.2-4 if CSI part 2 exists for CSI report #2
$a_0^{(2)}$	...
$a_1^{(2)}$	
$a_2^{(2)}$	CSI report #n, CSI part 2 wideband, as in Table 6.3.2.1.2-4 if CSI part 2 exists for CSI report #n
$a_3^{(2)}$	CSI report #1, CSI part 2 subband, as in Table 6.3.2.1.2-5 if CSI part 2 exists for CSI report #1
$\vdots$	
$a_{A^{(2)}-1}^{(2)}$	CSI report #2, CSI part 2 subband, as in Table 6.3.2.1.2-5 if CSI part 2 exists for CSI report #2
	...
	CSI report #n, CSI part 2 subband, as in Table 6.3.2.1.2-5 if CSI part 2 exists for CSI report #n

where CSI report #1, CSI report #2, ..., CSI report #n in Table 6.3.2.1.2-7 correspond to the CSI reports in increasing order of CSI report priority values according to Subclause 5.2.5 of [6, TS38.214].

6.3.2.2 Code block segmentation and CRC attachment

Denote the bits of the payload by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, where A is the payload size. The procedure in 6.3.2.2.1 applies for $A \geq 12$ and the procedure in Subclause 6.3.2.2.2 applies for $A \leq 11$.

6.3.2.2.1 UCI encoded by Polar code

Code block segmentation and CRC attachment is performed according to Subclause 6.3.1.2.1.

6.3.2.2.2 UCI encoded by channel coding of small block lengths

The procedure in Subclause 6.3.1.2.2 applies.

6.3.2.3 Channel coding of UCI

6.3.2.3.1 UCI encoded by Polar code

Channel coding is performed according to Subclause 6.3.1.3.1, except that the rate matching output sequence length E_r is given in Subclause 6.3.2.4.1.

6.3.2.3.2 UCI encoded by channel coding of small block lengths

Information bits are delivered to the channel coding block. They are denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits.

The information bits are encoded according to Subclause 5.3.3.

After encoding the bits are denoted by $d_0, d_1, d_2, d_3, \dots, d_{N-1}$, where N is the number of coded bits.

6.3.2.4 Rate matching

6.3.2.4.1 UCI encoded by Polar code

6.3.2.4.1.1 HARQ-ACK

For HARQ-ACK transmission on PUSCH with UL-SCH, the number of coded modulation symbols per layer for

HARQ-ACK transmission, denoted as Q'_{ACK} , is determined as follows:

$$Q'_{\text{ACK}} = \min \left\{ \left\lceil \frac{(O_{\text{ACK}} + L_{\text{ACK}}) \cdot \beta_{\text{offset}}^{\text{PUSCH}} \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{\text{sc}}^{\text{UCI}}(l)}{\sum_{r=0}^{C_{\text{UL-SCH}} - 1} K_r} \right\rceil, \left\lceil \alpha \cdot \sum_{l=l_0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{\text{sc}}^{\text{UCI}}(l) \right\rceil \right\}$$

where

- O_{ACK} is the number of HARQ-ACK bits;
- if $O_{\text{ACK}} \geq 360$, $L_{\text{ACK}} = 11$; otherwise L_{ACK} is the number of CRC bits for HARQ-ACK determined according to Subclause 6.3.1.2.1;
- $\beta_{\text{offset}}^{\text{PUSCH}} = \beta_{\text{offset}}^{\text{HARQ-ACK}}$;
- $C_{\text{UL-SCH}}$ is the number of code blocks for UL-SCH of the PUSCH transmission;
- if the DCI format scheduling the PUSCH transmission includes a CBGTI field indicating that the UE shall not transmit the r -th code block, $K_r = 0$; otherwise, K_r is the r -th code block size for UL-SCH of the PUSCH transmission;
- $M_{\text{sc}}^{\text{PUSCH}}$ is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{\text{sc}}^{\text{PT-RS}}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- $M_{\text{sc}}^{\text{UCI}}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l = 0, 1, 2, \dots, N_{\text{symb,all}}^{\text{PUSCH}} - 1$, in the PUSCH transmission and $N_{\text{symb,all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;

- for any OFDM symbol that carries DMRS of the PUSCH, $M_{sc}^{UCI}(l) = 0$;
- for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{sc}^{UCI}(l) = M_{sc}^{PUSCH} - M_{sc}^{PT-RS}(l)$;
- α is configured by higher layer parameter *scaling*;
- l_0 is the symbol index of the first OFDM symbol that does not carry DMRS of the PUSCH, after the first DMRS symbol(s), in the PUSCH transmission.

For HARQ-ACK transmission on PUSCH without UL-SCH, the number of coded modulation symbols per layer for HARQ-ACK transmission, denoted as Q'_{ACK} , is determined as follows:

$$Q'_{ACK} = \min \left(\frac{O_{ACK} + L_{ACK}}{R Q_m}, \beta_{offset}^{PUSCH} M_{sc}^{PUSCH} \right) \quad \text{for } l = l_0, l_0+1, \dots, N_{symb,all}^{PUSCH}-1$$

where

- O_{ACK} is the number of HARQ-ACK bits;
- if $O_{ACK} \leq 360$, $L_{ACK} = 11$; otherwise L_{ACK} is the number of CRC bits for HARQ-ACK defined according to Subclause 6.3.1.2.1;
- $\beta_{offset}^{PUSCH} = \beta_{offset}^{HARQ-ACK}$;
- M_{sc}^{PUSCH} is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{sc}^{PT-RS}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- $M_{sc}^{UCI}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l=0, 1, 2, \dots, N_{symb,all}^{PUSCH}-1$, in the PUSCH transmission and $N_{symb,all}^{PUSCH}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;
- for any OFDM symbol that carries DMRS of the PUSCH, $M_{sc}^{UCI}(l) = 0$;
- for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{sc}^{UCI}(l) = M_{sc}^{PUSCH} - M_{sc}^{PT-RS}(l)$;
- l_0 is the symbol index of the first OFDM symbol that does not carry DMRS of the PUSCH, after the first DMRS symbol(s), in the PUSCH transmission;
- R is the code rate of the PUSCH, determined according to Subclause 6.1.4.1 of [6, TS38.214];
- Q_m is the modulation order of the PUSCH;
- α is configured by higher layer parameter *scaling*.

The input bit sequence to rate matching is $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$ where r is the code block number, and N_r is the number of coded bits in code block number r .

Rate matching is performed according to Subclause 5.4.1 by setting $I_{BL}=1$ and the rate matching output sequence length to $E_r = \lfloor E_{UCI} / C_{UCI} \rfloor$, where

- C_{UCI} is the number of code blocks for UCI determined according to Subclause 5.2.1;
- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH;
- $E_{UCI} = N_L \cdot Q'_{ACK} \cdot Q_m$.

The output bit sequence after rate matching is denoted as $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$ where E_r is the length of rate matching output sequence in code block number r .

6.3.2.4.1.2 CSI part 1

For CSI part 1 transmission on PUSCH with UL-SCH, the number of coded modulation symbols per layer for CSI part

1 transmission, denoted as $Q'_{CSI-part1}$, is determined as follows:

$$Q'_{CSI-1} = \min \left\{ \left\lceil \frac{(O_{CSI-1} + L_{CSI-1}) \cdot \beta_{offset}^{PUSCH} \cdot \sum_{l=0}^{N_{sym,all}^{PUSCH}-1} M_{sc}^{UCI}(l)}{\sum_{r=0}^{C_{UL-SCH}-1} K_r} \right\rceil, \left\lceil \alpha \cdot \sum_{l=0}^{N_{sym,all}^{PUSCH}-1} M_{sc}^{UCI}(l) \right\rceil - Q'_{ACK} \right\}$$

where

- O_{CSI-1} is the number of bits for CSI part 1;
- if $O_{CSI-1} \geq 360$, $L_{CSI-1} = 11$; otherwise L_{CSI-1} is the number of CRC bits for CSI part 1 determined according to Subclause 6.3.1.2.1;
- $\beta_{offset}^{PUSCH} = \beta_{offset}^{CSI-part1}$;
- C_{UL-SCH} is the number of code blocks for UL-SCH of the PUSCH transmission;
- if the DCI format scheduling the PUSCH transmission includes a CBGTI field indicating that the UE shall not transmit the r -th code block, $K_r = 0$; otherwise, K_r is the r -th code block size for UL-SCH of the PUSCH transmission;
- M_{sc}^{PUSCH} is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{sc}^{PT-RS}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- Q'_{ACK} is the number of coded modulation symbols per layer for HARQ-ACK transmitted on the PUSCH if

number of HARQ-ACK information bits is more than 2, and $Q'_{ACK} = \sum_{l=0}^{N_{sym,all}^{PUSCH}-1} \bar{M}_{sc,rvd}^{ACK}(l)$ if the number of HARQ-ACK information bits is no more than 2 bits, where $\bar{M}_{sc,rvd}^{ACK}(l)$ is the number of reserved resource

elements for potential HARQ-ACK transmission in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}}-1$, in the PUSCH transmission, defined in Subclause 6.2.7;

- $M_{\text{sc}}^{\text{UCI}}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}}-1$, in the PUSCH transmission and $N_{\text{symb,all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;
- for any OFDM symbol that carries DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = 0$;
- for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = M_{\text{sc}}^{\text{PUSCH}} - M_{\text{sc}}^{\text{PT-RS}}(l)$;
- α is configured by higher layer parameter *scaling*.

For CSI part 1 transmission on PUSCH without UL-SCH, the number of coded modulation symbols per layer for CSI part 1 transmission, denoted as $Q'_{\text{CSI-part1}}$, is determined as follows:

if there is CSI part 2 to be transmitted on the PUSCH,

$$Q'_{\text{CSI-1}} = \min \left(\frac{O_{\text{CSI-1}} + L_{\text{CSI-1}}}{R_{Q_m}} \cdot \beta_{\text{offset}}^{\text{PUSCH}} \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}}-1} M_{\text{sc}}^{\text{UCI}}(l) - Q_{\text{ACK}} \right)$$

else

$$Q'_{\text{CSI-1}} = \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}}-1} M_{\text{sc}}^{\text{UCI}}(l) - Q_{\text{ACK}}$$

end if

where

- $O_{\text{CSI-1}}$ is the number of bits for CSI part 1;
- if $O_{\text{CSI-1}} \geq 860$, $L_{\text{CSI-1}} = 11$; otherwise $L_{\text{CSI-1}}$ is the number of CRC bits for CSI part 1 determined according to Subclause 6.3.1.2.1;
- $\beta_{\text{offset}}^{\text{PUSCH}} = \beta_{\text{offset}}^{\text{CSI-part1}}$;
- $M_{\text{sc}}^{\text{PUSCH}}$ is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{\text{sc}}^{\text{PT-RS}}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- Q'_{ACK} is the number of coded modulation symbols per layer for HARQ-ACK transmitted on the PUSCH if

number of HARQ-ACK information bits is more than 2, and $Q'_{\text{ACK}} = \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}}-1} \bar{M}_{\text{sc,rvd}}^{\text{ACK}}(l)$ if the number of

HARQ-ACK information bits is no more than 2 bits, where $\bar{M}_{\text{sc,rvd}}^{\text{ACK}}(l)$ is the number of reserved resource

elements for potential HARQ-ACK transmission in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}}-1$, in the PUSCH transmission, defined in Subclause 6.2.7;

- $M_{sc}^{UCI}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}}-1$, in the PUSCH transmission and $N_{\text{symb,all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;
- for any OFDM symbol that carries DMRS of the PUSCH, $M_{sc}^{UCI}(l) = 0$;
- for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{sc}^{UCI}(l) = M_{sc}^{\text{PUSCH}} - M_{sc}^{\text{PT-RS}}(l)$;
- R is the code rate of the PUSCH, determined according to Subclause 6.1.4.1 of [6, TS38.214];
- Q_m is the modulation order of the PUSCH.

The input bit sequence to rate matching is $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$ where r is the code block number, and N_r is the number of coded bits in code block number r .

Rate matching is performed according to Subclause 5.4.1 by setting $I_{\text{BIL}}=1$ and the rate matching output sequence length to $E_r = \lfloor E_{\text{UCI}} / C_{\text{UCI}} \rfloor$, where

- C_{UCI} is the number of code blocks for UCI determined according to Subclause 5.2.1;
- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH;
- $E_{\text{UCI}} = N_L \cdot Q'_{\text{CSI-1}} \cdot Q_m$.

The output bit sequence after rate matching is denoted as $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$ where E_r is the length of rate matching output sequence in code block number r .

6.3.2.4.1.3 CSI part 2

For CSI part 2 transmission on PUSCH with UL-SCH, the number of coded modulation symbols per layer for CSI part 2 transmission, denoted as $Q'_{\text{CSI-part2}}$, is determined as follows:

$$Q'_{\text{CSI-2}} = \min \left\{ \left\lceil \frac{(O_{\text{CSI-2}} + L_{\text{CSI-2}}) \cdot \beta_{\text{offset}}^{\text{PUSCH}} \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}}-1} M_{sc}^{UCI}(l)}{C_{\text{UL-SCH}}^{-1} \sum_{r=0} K_r} \right\rceil, \left\lceil \alpha \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}}-1} M_{sc}^{UCI}(l) \right\rceil - Q'_{\text{ACK}} - Q'_{\text{CSI-1}} \right\}$$

where

- $O_{\text{CSI-2}}$ is the number of bits for CSI part 2;
- if $O_{\text{CSI-2}} \geq 860$, $L_{\text{CSI-2}} = 11$; otherwise $L_{\text{CSI-2}}$ is the number of CRC bits for CSI part 2 determined according to Subclause 6.3.1.2.1;
- $\beta_{\text{offset}}^{\text{PUSCH}} = \beta_{\text{offset}}^{\text{CSI-part2}}$;

- $C_{\text{UL-SCH}}$ is the number of code blocks for UL-SCH of the PUSCH transmission;
- if the DCI format scheduling the PUSCH transmission includes a CBGTI field indicating that the UE shall not transmit the r -th code block, $K_r = 0$; otherwise, K_r is the r -th code block size for UL-SCH of the PUSCH transmission;
- $M_{\text{sc}}^{\text{PUSCH}}$ is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{\text{sc}}^{\text{PT-RS}}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- Q'_{ACK} is the number of coded modulation symbols per layer for HARQ-ACK transmitted on the PUSCH if number of HARQ-ACK information bits is more than 2, and $Q'_{\text{ACK}} = 0$ if the number of HARQ-ACK information bits is 1 or 2 bits;
- $Q'_{\text{CSI-1}}$ is the number of coded modulation symbols per layer for CSI part 1 transmitted on the PUSCH;
- $M_{\text{sc}}^{\text{UCI}}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l=0, 1, 2, \dots, N_{\text{symb,all}}^{\text{PUSCH}} - 1$, in the PUSCH transmission and $N_{\text{symb,all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;
 - for any OFDM symbol that carries DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = 0$;
 - for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = M_{\text{sc}}^{\text{PUSCH}} - M_{\text{sc}}^{\text{PT-RS}}(l)$.
- α is configured by higher layer parameter *scaling*.

For CSI part 2 transmission on PUSCH without UL-SCH, the number of coded modulation symbols per layer for CSI part 2 transmission, denoted as $Q'_{\text{CSI-part2}}$, is determined as follows:

$$Q'_{\text{CSI-2}} = \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{\text{sc}}^{\text{UCI}}(l) - Q'_{\text{ACK}} - Q'_{\text{CSI-1}}$$

where

- $M_{\text{sc}}^{\text{PUSCH}}$ is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{\text{sc}}^{\text{PT-RS}}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- Q'_{ACK} is the number of coded modulation symbols per layer for HARQ-ACK transmitted on the PUSCH if number of HARQ-ACK information bits is more than 2, and $Q'_{\text{ACK}} = 0$ if the number of HARQ-ACK information bits is 1 or 2 bits;
- $Q'_{\text{CSI-1}}$ is the number of coded modulation symbols per layer for CSI part 1 transmitted on the PUSCH;
- $M_{\text{sc}}^{\text{UCI}}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l=0, 1, 2, \dots, N_{\text{symb,all}}^{\text{PUSCH}} - 1$, in the PUSCH transmission and $N_{\text{symb,all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;

- for any OFDM symbol that carries DMRS of the PUSCH, $M_{sc}^{UCI}(l) = 0$;
- for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{sc}^{UCI}(l) = M_{sc}^{PUSCH} - M_{sc}^{PT-RS}(l)$.

The input bit sequence to rate matching is $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$ where r is the code block number, and N_r is the number of coded bits in code block number r .

Rate matching is performed according to Subclause 5.4.1 by setting $I_{BIL} = 1$ and the rate matching output sequence length to $E_r = \lfloor E_{UCI} / C_{UCI} \rfloor$, where

- C_{UCI} is the number of code blocks for UCI determined according to Subclause 5.2.1;
- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH;
- $E_{UCI} = N_L \cdot Q'_{CSI,2} \cdot Q_m$.

The output bit sequence after rate matching is denoted as $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$ where E_r is the length of rate matching output sequence in code block number r .

6.3.2.4.2 UCI encoded by channel coding of small block lengths

6.3.2.4.2.1 HARQ-ACK

For HARQ-ACK transmission on PUSCH, the number of coded modulation symbols per layer for HARQ-ACK

transmission, denoted as Q'_{ACK} , is determined according to Subclause 6.3.2.4.1.1, by setting the number of CRC bits $L=0$.

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$.

Rate matching is performed according to Subclause 5.4.3, by setting the rate matching output sequence length $E = N_L \cdot Q'_{ACK} \cdot Q_m$, where

- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

6.3.2.4.2.2 CSI part 1

For CSI part 1 transmission on PUSCH, the number of coded modulation symbols per layer for CSI part 1 transmission,

denoted as $Q'_{CSI,1}$, is determined according to Subclause 6.3.2.4.1.2, by setting the number of CRC bits $L=0$.

Rate matching is performed according to Subclause 5.4.3, by setting the rate matching output sequence length

$E = N_L \cdot Q'_{CSI,1} \cdot Q_m$, where

- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

6.3.2.4.2.3 CSI part 2

For CSI part 2 transmission on PUSCH, the number of coded modulation symbols per layer for CSI part 2 transmission, denoted as $Q'_{\text{CSI},2}$, is determined according to Subclause 6.3.2.4.1.3, by setting the number of CRC bits $L=0$.

Rate matching is performed according to Subclause 5.4.3, by setting the rate matching output sequence length

$$E = N_L \cdot Q'_{\text{CSI},2} \cdot Q_m, \text{ where}$$

- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

6.3.2.5 Code block concatenation

Code block concatenation is performed according to Subclause 6.3.1.5, except that the values of E_{UCI} and C_{UCI} given in Subclause 6.3.2.4.1.

6.3.2.6 Multiplexing of coded UCI bits to PUSCH

The coded UCI bits are multiplexed onto PUSCH according to the procedures in Subclause 6.2.7.

7 Downlink transport channels and control information

7.1 Broadcast channel

Data arrives to the coding unit in the form of a maximum of one transport block every 80ms. The following coding steps can be identified:

- Payload generation
- Scrambling
- Transport block CRC attachment
- Channel coding
- Rate matching

7.1.1 PBCH payload generation

Denote the bits in a transport block delivered to layer 1 by $\bar{a}_0, \bar{a}_1, \bar{a}_2, \bar{a}_3, \dots, \bar{a}_{\bar{A}-1}$, where $\bar{A}$ is the payload size generated by higher layers. The lowest order information bit $\bar{a}_0$ is mapped to the most significant bit of the transport block as defined in Subclause [6.1.4] of [8, TS 38.321].

Generate the following additional timing related PBCH payload bits $\bar{a}_{\bar{A}}, \bar{a}_{\bar{A}+1}, \bar{a}_{\bar{A}+2}, \bar{a}_{\bar{A}+3}, \dots, \bar{a}_{\bar{A}+7}$, where:

- $\bar{a}_{\bar{A}}, \bar{a}_{\bar{A}+1}, \bar{a}_{\bar{A}+2}, \bar{a}_{\bar{A}+3}$ are the 4th, 3rd, 2nd, and 1st LSB of SFN, respectively;
- $\bar{a}_{\bar{A}+4}$ is the half frame bit $\bar{a}_{\text{HRF}}$;
- if $L_{\text{max}} = 64$

$\bar{a}_{\bar{A}+5}, \bar{a}_{\bar{A}+6}, \bar{a}_{\bar{A}+7}$ are the 6th, 5th, and 4th bits of SS/PBCH block index, respectively.

else

$\bar{a}_{\bar{A}+5}$ is the MSB of k_{SSB} as defined in Subclause 7.4.3.1 of [4, TS 38.211].

$\bar{a}_{\bar{A}+6}, \bar{a}_{\bar{A}+7}$ are reserved.

end if

Let $A = \bar{A} + 8$; $j_{SFN} = 0$; $j_{HRF} = 10$; $j_{SSB} = 11$; $j_{other} = 14$;

for $i = 0$ to $A - 1$

if $\bar{a}_i$ is an SFN bit

$$a_{G(j_{SFN})} = \bar{a}_i ;$$

$$j_{SFN} = j_{SFN} + 1 ;$$

elseif $\bar{a}_i$ is the half radio frame bit

$$a_{G(j_{HRF})} = \bar{a}_i$$

elseif $\bar{A} + 5 \leq i \leq \bar{A} + 7$

$$a_{G(j_{SSB})} = \bar{a}_i ;$$

$$j_{SSB} = j_{SSB} + 1 ;$$

else

$$a_{G(j_{Other})} = \bar{a}_i ;$$

$$j_{Other} = j_{Other} + 1 ;$$

end if

end for

where L_{max} is the number of candidate SS/PBCH blocks in a half frame according to Subclause 4.1 of [5, TS38.213], and the value of $G(j)$ is given by Table 7.1.1-1.

Table 7.1.1-1: Value of PBCH payload interleaver pattern $G(j)$

j	$G(j)$	j	$G(j)$	j	$G(j)$	j	$G(j)$	j	$G(j)$	j	$G(j)$	j	$G(j)$	j	$G(j)$
0	16	4	8	8	24	12	3	16	9	20	14	24	21	28	27
1	23	5	30	9	7	13	2	17	11	21	15	25	22	29	28
2	18	6	10	10	0	14	1	18	12	22	19	26	25	30	29
3	17	7	6	11	5	15	4	19	13	23	20	27	26	31	31

7.1.2 Scrambling

For PBCH transmission in a frame, the bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is scrambled into a bit sequence

$a'_0, a'_1, a'_2, a'_3, \dots, a'_{A-1}$, where $a'_i = (a_i + s_i) \bmod 2$ for $i=0, 1, \dots, A-1$ and $s_0, s_1, s_2, s_3, \dots, s_{A-1}$ is generated according to the following:

$i=0$;

$j=0$;

while $i < A$

if a_i corresponds to any one of the bits belonging to the SS/PBCH block index, the half frame index, and 2nd and 3rd least significant bits of the system frame number

$s_i = 0$;

else

$s_i = c(j + vM)$;

$j = j + 1$;

end if

$i = i + 1$;

end while

The scrambling sequence $c(i)$ is given by Subclause 5.2.1 of [4, TS38.211] and initialized with $c_{\text{init}} = N_{\text{ID}}^{\text{cell}}$ at the start of each SFN satisfying $\text{mod}(\text{SFN}, 8) = 0$; $M = A - 3$ for $L_{\text{max}} = 4$ or $L_{\text{max}} = 8$, and $M = A - 6$ for $L_{\text{max}} = 64$, where L_{max} is the number of candidate SS/PBCH blocks in a half frame according to Subclause 4.1 of [5, TS38.213]; and v is determined according to Table 7.1.2-1 using the 3rd and 2nd LSB of the SFN in which the PBCH is transmitted.

Table 7.1.2-1: Value of v for PBCH scrambling

(3 rd LSB of SFN, 2 nd LSB of SFN)	Value of v
(0, 0)	0
(0, 1)	1
(1, 0)	2
(1, 1)	3

7.1.3 Transport block CRC attachment

Error detection is provided on BCH transport blocks through a Cyclic Redundancy Check (CRC).

The entire transport block is used to calculate the CRC parity bits. The input bit sequence is denoted by

$a'_0, a'_1, a'_2, a'_3, \dots, a'_{A-1}$, and the parity bits by $p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the payload size and L is the number of parity bits.

The parity bits are computed and attached to the BCH transport block according to Subclause 5.1 by setting L to 24 bits and using the generator polynomial $g_{\text{CRC24C}}(D)$, resulting in the sequence $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B = A + L$.

The bit sequence $b_0, b_1, b_2, b_3, \dots, b_{B-1}$ is the input bit sequence $c_0, c_1, c_2, c_3, \dots, c_{K-1}$ to the channel encoder, where $c_i = b_i$ for $i=0, 1, \dots, B-1$ and $K=B$.

7.1.4 Channel coding

Information bits are delivered to the channel coding block. They are denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits, and they are encoded via Polar coding according to Subclause 5.3.1, by setting $n_{\max}=9$, $I_{IL}=1$, $n_{PC}=0$, and $n_{PC}^{wm}=0$.

After encoding the bits are denoted by $d_0, d_1, d_2, d_3, \dots, d_{N-1}$, where N is the number of coded bits.

7.1.5 Rate matching

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$.

The rate matching output sequence length $E=864$.

Rate matching is performed according to Subclause 5.4.1 by setting $I_{BIL}=0$.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

7.2 Downlink shared channel and paging channel

7.2.1 Transport block CRC attachment

Error detection is provided on each transport block through a Cyclic Redundancy Check (CRC).

The entire transport block is used to calculate the CRC parity bits. Denote the bits in a transport block delivered to layer 1 by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, and the parity bits by $p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the payload size and

L is the number of parity bits. The lowest order information bit a_0 is mapped to the most significant bit of the transport block as defined in Subclause 6.1.1 of [TS38.321].

The parity bits are computed and attached to the DL-SCH transport block according to Subclause 5.1, by setting L to 24 bits and using the generator polynomial $g_{\text{CRC24A}}(D)$ if $A > 3824$; and by setting L to 16 bits and using the generator polynomial $g_{\text{CRC16}}(D)$ otherwise.

The bits after CRC attachment are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B=A+L$.

7.2.2 LDPC base graph selection

For initial transmission of a transport block with coding rate R indicated by the MCS index according to Subclause 5.1.3.1 in [6, TS 38.214] and subsequent re-transmission of the same transport block, each code block of the transport block is encoded with either LDPC base graph 1 or 2 according to the following:

- if $A \leq 292$, or if $A \leq 3824$ and $R \leq 0.67$, or if $R \leq 0.25$, LDPC base graph 2 is used;
- otherwise, LDPC base graph 1 is used,

where A is the payload size in Subclause 7.2.1.

7.2.3 Code block segmentation and code block CRC attachment

The bits input to the code block segmentation are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$ where B is the number of bits in the transport block (including CRC).

Code block segmentation and code block CRC attachment are performed according to Subclause 5.2.2.

The bits after code block segmentation are denoted by $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r(K_r-1)}$, where r is the code block number and K_r is the number of bits for code block number r according to Subclause 5.2.2.

7.2.4 Channel coding

Code blocks are delivered to the channel coding block. The bits in a code block are denoted by

$c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r(K_r-1)}$, where r is the code block number, and K_r is the number of bits in code block number r . The total number of code blocks is denoted by C and each code block is individually LDPC encoded according to Subclause 5.3.2.

After encoding the bits are denoted by $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$, where the values of N_r is given in Subclause 5.3.2.

7.2.5 Rate matching

Coded bits for each code block, denoted as $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$, are delivered to the rate match block, where r is the code block number, and N_r is the number of encoded bits in code block number r . The total number of code blocks is denoted by C and each code block is individually rate matched according to Subclause 5.4.2 by setting $I_{LBRM}=1$.

After rate matching, the bits are denoted by $f_{r0}, f_{r1}, f_{r2}, f_{r3}, \dots, f_{r(E_r-1)}$, where E_r is the number of rate matched bits for code block number r .

7.2.6 Code block concatenation

The input bit sequence for the code block concatenation block are the sequences $f_{r0}, f_{r1}, f_{r2}, f_{r3}, \dots, f_{r(E_r-1)}$, for $r=0, \dots, C-1$ and where E_r is the number of rate matched bits for the r -th code block.

Code block concatenation is performed according to Subclause 5.5.

The bits after code block concatenation are denoted by $g_0, g_1, g_2, g_3, \dots, g_{G-1}$, where G is the total number of coded bits for transmission.

7.3 Downlink control information

A DCI transports downlink control information for one or more cells with one RNTI.

The following coding steps can be identified:

- Information element multiplexing
- CRC attachment
- Channel coding
- Rate matching

7.3.1 DCI formats

The DCI formats defined in table 7.3.1-1 are supported.

Table 7.3.1-1: DCI formats

DCI format	Usage
0_0	Scheduling of PUSCH in one cell
0_1	Scheduling of PUSCH in one cell
1_0	Scheduling of PDSCH in one cell
1_1	Scheduling of PDSCH in one cell
2_0	Notifying a group of UEs of the slot format
2_1	Notifying a group of UEs of the PRB(s) and OFDM symbol(s) where UE may assume no transmission is intended for the UE
2_2	Transmission of TPC commands for PUCCH and PUSCH
2_3	Transmission of a group of TPC commands for SRS transmissions by one or more UEs

The fields defined in the DCI formats below are mapped to the information bits a_0 to a_{A-1} as follows.

Each field is mapped in the order in which it appears in the description, including the zero-padding bit(s), if any, with the first field mapped to the lowest order information bit a_0 and each successive field mapped to higher order information bits. The most significant bit of each field is mapped to the lowest order information bit for that field, e.g. the most significant bit of the first field is mapped to a_0 .

If the number of information bits in a DCI format is less than 12 bits, zeros shall be appended to the DCI format until the payload size equals 12.

The size of each DCI format is determined by the configuration of the corresponding active bandwidth part of the scheduled cell and shall be adjusted as described in clause 7.3.1.0 if necessary.

7.3.1.0 DCI size alignment

If necessary, padding or truncation shall be applied to the DCI formats according to the following steps executed in the order below:

Step 0:

- Determine DCI format 0_0 monitored in a common search space according to clause 7.3.1.1.1 where $N_{RB}^{UL,BWP}$ is the size of the initial UL bandwidth part.
- Determine DCI format 1_0 monitored in a common search space according to clause 7.3.1.2.1 where $N_{RB}^{DL,BWP}$ is given by
 - the size of CORESET 0 if CORESET 0 is configured for the cell; and
 - the size of initial DL bandwidth part if CORESET 0 is not configured for the cell.
- If DCI format 0_0 is monitored in common search space and if the number of information bits in the DCI format 0_0 prior to padding is less than the payload size of the DCI format 1_0 monitored in common search space for scheduling the same serving cell, a number of zero padding bits are generated for the DCI format 0_0 until the payload size equals that of the DCI format 1_0.
- If DCI format 0_0 is monitored in common search space and if the number of information bits in the DCI format 0_0 prior to truncation is larger than the payload size of the DCI format 1_0 monitored in common search space for scheduling the same serving cell, the bitwidth of the frequency domain resource assignment field in the DCI format 0_0 is reduced by truncating the first few most significant bits such that the size of DCI format 0_0 equals the size of the DCI format 1_0.

Step 1:

- Determine DCI format 0_0 monitored in a UE-specific search space according to clause 7.3.1.1.1 where $N_{RB}^{UL,BWP}$ is the size of the active UL bandwidth part.

- Determine DCI format 1_0 monitored in a UE-specific search space according to clause 7.3.1.2.1 where $N_{RB}^{DL,BWP}$ is the size of the active DL bandwidth part.
- For a UE configured with *supplementaryUplink* in *ServingCellConfig* in a cell, if PUSCH is configured to be transmitted on both the SUL and the non-SUL of the cell and if the number of information bits in DCI format 0_0 in UE-specific search space for the SUL is not equal to the number of information bits in DCI format 0_0 in UE-specific search space for the non-SUL, a number of zero padding bits are generated for the smaller DCI format 0_0 until the payload size equals that of the larger DCI format 0_0.
- If DCI format 0_0 is monitored in UE-specific search space and if the number of information bits in the DCI format 0_0 prior to padding is less than the payload size of the DCI format 1_0 monitored in UE-specific search space for scheduling the same serving cell, a number of zero padding bits are generated for the DCI format 0_0 until the payload size equals that of the DCI format 1_0.
- If DCI format 1_0 is monitored in UE-specific search space and if the number of information bits in the DCI format 1_0 prior to padding is less than the payload size of the DCI format 0_0 monitored in UE-specific search space for scheduling the same serving cell, zeros shall be appended to the DCI format 1_0 until the payload size equals that of the DCI format 0_0

Step 2:

- For a UE configured with *supplementaryUplink* in *ServingCellConfig* in a cell, if PUSCH is configured to be transmitted on both the SUL and the non-SUL of the cell and if the number of information bits in format 0_1 for the SUL is not equal to the number of information bits in format 0_1 for the non-SUL, zeros shall be appended to smaller format 0_1 until the payload size equals that of the larger format 0_1.
- If the size of DCI format 0_1 monitored in a UE-specific search space equals that of a DCI format 0_0/1_0 monitored in another UE-specific search space, one bit of zero padding shall be appended to DCI format 0_1.
- If the size of DCI format 1_1 monitored in a UE-specific search space equals that of a DCI format 0_0/1_0 monitored in another UE-specific search space, one bit of zero padding shall be appended to DCI format 1_1.

Step 3:

- If both of the following conditions are fulfilled the size alignment procedure is complete
 - the total number of different DCI sizes configured to monitor is no more than 4 for the cell
 - the total number of different DCI sizes with C-RNTI configured to monitor is no more than 3 for the cell

Step 4:

- Otherwise
 - Remove the padding bit (if any) introduced in step 2 above.
 - Determine DCI format 1_0 monitored in a UE-specific search space according to clause 7.3.1.2.1 where $N_{RB}^{DL,BWP}$ is given by
 - the size of CORESET 0 if CORESET 0 is configured for the cell; and
 - the size of initial DL bandwidth part if CORESET 0 is not configured for the cell.
 - Determine DCI format 0_0 monitored in a UE-specific search space according to clause 7.3.1.1.1 where $N_{RB}^{UL,BWP}$ is the size of the initial UL bandwidth part.
 - If the number of information bits in the DCI format 0_0 monitored in a UE-specific search space prior to padding is less than the payload size of the DCI format 1_0 monitored in UE-specific search space for scheduling the same serving cell, a number of zero padding bits are generated for the DCI format 0_0 monitored in a UE-specific search space until the payload size equals that of the DCI format 1_0 monitored in a UE-specific search space.
 - If the number of information bits in the DCI format 0_0 monitored in a UE-specific search space prior to truncation is larger than the payload size of the DCI format 1_0 monitored in UE-specific search space for

scheduling the same serving cell, the bitwidth of the frequency domain resource assignment field in the DCI format 0_0 is reduced by truncating the first few most significant bits such that the size of DCI format 0_0 monitored in a UE-specific search space equals the size of the DCI format 1_0 monitored in a UE-specific search space.

The UE is not expected to handle a configuration that, after applying the above steps, results in

- the total number of different DCI sizes configured to monitor is more than 4 for the cell; or
- the total number of different DCI sizes with C-RNTI configured to monitor is more than 3 for the cell; or
- the size of DCI format 0_0 in a UE-specific search space is equal to DCI format 0_1 in another UE-specific search space; or
- the size of DCI format 1_0 in a UE-specific search space is equal to DCI format 1_1 in another UE-specific search space

7.3.1.1 DCI formats for scheduling of PUSCH

7.3.1.1.1 Format 0_0

DCI format 0_0 is used for the scheduling of PUSCH in one cell.

The following information is transmitted by means of the DCI format 0_0 with CRC scrambled by C-RNTI or CS-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bit
 - The value of this bit field is always set to 0, indicating an UL DCI format
- Frequency domain resource assignment – $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil$ bits where $N_{RB}^{UL,BWP}$ is defined in subclause 7.3.1.0
 - For PUSCH hopping with resource allocation type 1:
 - $N_{UL,hop}$ MSB bits are used to indicate the frequency offset according to Subclause 6.3 of [6, TS 38.214], where $N_{UL,hop}=1$ if the higher layer parameter *frequencyHoppingOffsetLists* contains two offset values and $N_{UL,hop}=2$ if the higher layer parameter *frequencyHoppingOffsetLists* contains four offset values
 - $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil - N_{UL,hop}$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
 - For non-PUSCH hopping with resource allocation type 1:
 - $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
- Time domain resource assignment – 4 bits as defined in Subclause 6.1.2.1 of [6, TS 38.214]
- Frequency hopping flag – 1 bit according to Table 7.3.1.1.1-3, as defined in Subclause 6.3 of [6, TS 38.214]
- Modulation and coding scheme – 5 bits as defined in Subclause 6.1.4.1 of [6, TS 38.214]
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits
- TPC command for scheduled PUSCH – 2 bits as defined in Subclause 7.1.1 of [5, TS 38.213]

- Padding bits, if required.
- UL/SUL indicator – 1 bit for UEs configured with *supplementaryUplink* in *ServingCellConfig* in the cell as defined in Table 7.3.1.1.1-1 and the number of bits for DCI format 1_0 before padding is larger than the number of bits for DCI format 0_0 before padding; 0 bit otherwise. The UL/SUL indicator, if present, locates in the last bit position of DCI format 0_0, after the padding bit(s).
- If the UL/SUL indicator is present in DCI format 0_0 and the higher layer parameter *pusch-Config* is not configured on both UL and SUL the UE ignores the UL/SUL indicator field in DCI format 0_0, and the corresponding PUSCH scheduled by the DCI format 0_0 is for the UL or SUL for which high layer parameter *pucch-Config* is configured;
- If the UL/SUL indicator is not present in DCI format 0_0 and *pucch-Config* is configured, the corresponding PUSCH scheduled by the DCI format 0_0 is for the UL or SUL for which high layer parameter *pucch-Config* is configured.
- If the UL/SUL indicator is not present in DCI format 0_0 and *pucch-Config* is not configured, the corresponding PUSCH scheduled by the DCI format 0_0 is for the uplink on which the latest PRACH is transmitted.

The following information is transmitted by means of the DCI format 0_0 with CRC scrambled by TC-RNTI:

- Identifier for DCI formats – 1 bit
 - The value of this bit field is always set to 0, indicating an UL DCI format
- Frequency domain resource assignment – $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil$ bits where
 - $N_{RB}^{UL,BWP}$ is the size of the initial UL bandwidth part.
 - For PUSCH hopping with resource allocation type 1:
 - N_{UL_hop} MSB bits are used to indicate the frequency offset according to Table 8.3-1 in Subclause 8.3 of [5, TS 38.213], where $N_{UL_hop}=1$ if $N_{RB}^{UL,BWP} < 50$ and $N_{UL_hop}=2$ otherwise
 - $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil - N_{UL_hop}$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
 - For non-PUSCH hopping with resource allocation type 1:
 - $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
- Time domain resource assignment – 4 bits as defined in Subclause 6.1.2.1 of [6, TS 38.214]
- Frequency hopping flag – 1 bit according to Table 7.3.1.1.1-3, as defined in Subclause 6.3 of [6, TS 38.214]
- Modulation and coding scheme – 5 bits as defined in Subclause 6.1.4.1 of [6, TS 38.214]
- New data indicator – 1 bit, reserved
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits, reserved
- TPC command for scheduled PUSCH – 2 bits as defined in Subclause 7.1.1 of [5, TS 38.213]
- Padding bits, if required.

- UL/SUL indicator – 1 bit if the cell has two ULs and the number of bits for DCI format 1_0 before padding is larger than the number of bits for DCI format 0_0 before padding; 0 bit otherwise. The UL/SUL indicator, if present, locates in the last bit position of DCI format 0_0, after the padding bit(s).
- If 1 bit, reserved, and the corresponding PUSCH is always on the same UL carrier as the previous transmission of the same TB

Table 7.3.1.1.1-1: UL/SUL indicator

Value of UL/SUL indicator	Uplink
0	The non-supplementary uplink
1	The supplementary uplink

Table 7.3.1.1.1-2: Redundancy version

Value of the Redundancy version field	Value of $r_{v_{id}}$ to be applied
00	0
01	1
10	2
11	3

Table 7.3.1.1.1-3: Frequency hopping indication

Bit field mapped to index	PUSCH frequency hopping
0	Disabled
1	Enabled

7.3.1.1.2 Format 0_1

DCI format 0_1 is used for the scheduling of PUSCH in one cell.

The following information is transmitted by means of the DCI format 0_1 with CRC scrambled by C-RNTI or CS-RNTI or SP-CSI-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bit
 - The value of this bit field is always set to 0, indicating an UL DCI format
- Carrier indicator – 0 or 3 bits, as defined in Subclause 10.1 of [5, TS38.213].
- UL/SUL indicator – 0 bit for UEs not configured with *supplementaryUplink* in *ServingCellConfig* in the cell or UEs configured with *supplementaryUplink* in *ServingCellConfig* in the cell but only PUCCH carrier in the cell is configured for PUSCH transmission; otherwise, 1 bit as defined in Table 7.3.1.1.1-1.
- Bandwidth part indicator – 0, 1 or 2 bits as determined by the number of UL BWPs $n_{BWP,RRC}$ configured by higher layers, excluding the initial UL bandwidth part. The bitwidth for this field is determined as $\lceil \log_2(n_{BWP}) \rceil$ bits, where
 - $n_{BWP} = n_{BWP,RRC} + 1$ if $n_{BWP,RRC} \neq 0$, in which case the bandwidth part indicator is equivalent to the ascending order of the higher layer parameter *BWP-Id*;
 - otherwise $n_{BWP} = n_{BWP,RRC}$, in which case the bandwidth part indicator is defined in Table 7.3.1.1.2-1;

If a UE does not support active BWP change via DCI, the UE ignores this bit field.

- Frequency domain resource assignment – number of bits determined by the following, where $N_{RB}^{UL,BWP}$ is the size of the active UL bandwidth part:
 - N_{RBG} bits if only resource allocation type 0 is configured, where N_{RBG} is defined in Subclause 6.1.2.2.1 of [6, TS 38.214],
 - $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil$ bits if only resource allocation type 1 is configured, or $\max(\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil, N_{RBG})+1$ bits if both resource allocation type 0 and 1 are configured.
 - If both resource allocation type 0 and 1 are configured, the MSB bit is used to indicate resource allocation type 0 or resource allocation type 1, where the bit value of 0 indicates resource allocation type 0 and the bit value of 1 indicates resource allocation type 1.
 - For resource allocation type 0, the N_{RBG} LSBs provide the resource allocation as defined in Subclause 6.1.2.2.1 of [6, TS 38.214].
 - For resource allocation type 1, the $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil$ LSBs provide the resource allocation as follows:
 - For PUSCH hopping with resource allocation type 1:
 - N_{UL_hop} MSB bits are used to indicate the frequency offset according to Subclause 6.3 of [6, TS 38.214], where $N_{UL_hop}=1$ if the higher layer parameter *frequencyHoppingOffsetLists* contains two offset values and $N_{UL_hop}=2$ if the higher layer parameter *frequencyHoppingOffsetLists* contains four offset values
 - $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil - N_{UL_hop}$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
 - For non-PUSCH hopping with resource allocation type 1:
 - $\lceil \log_2(N_{RB}^{UL,BWP}(N_{RB}^{UL,BWP}+1)/2) \rceil$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
- If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part and if both resource allocation type 0 and 1 are configured for the indicated bandwidth part, the UE assumes resource allocation type 0 for the indicated bandwidth part if the bitwidth of the "Frequency domain resource assignment" field of the active bandwidth part is smaller than the bitwidth of the "Frequency domain resource assignment" field of the indicated bandwidth part.
- Time domain resource assignment – 0, 1, 2, 3, or 4 bits as defined in Subclause 6.1.2.1 of [6, TS38.214]. The bitwidth for this field is determined as $\lceil \log_2(I) \rceil$ bits, where I is the number of entries in the higher layer parameter *pusch-TimeDomainAllocationList* if the higher layer parameter is configured; otherwise I is the number of entries in the default table.
 - Frequency hopping flag – 0 or 1 bit:
 - 0 bit if only resource allocation type 0 is configured or if the higher layer parameter *frequencyHopping* is not configured;
 - 1 bit according to Table 7.3.1.1.1-3 otherwise, only applicable to resource allocation type 1, as defined in Subclause 6.3 of [6, TS 38.214].
 - Modulation and coding scheme – 5 bits as defined in Subclause 6.1.4.1 of [6, TS 38.214]
 - New data indicator – 1 bit

- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits
- 1st downlink assignment index – 1 or 2 bits:
 - 1 bit for semi-static HARQ-ACK codebook;
 - 2 bits for dynamic HARQ-ACK codebook.
- 2nd downlink assignment index – 0 or 2 bits:
 - 2 bits for dynamic HARQ-ACK codebook with two HARQ-ACK sub-codebooks;
 - 0 bit otherwise.
- TPC command for scheduled PUSCH – 2 bits as defined in Subclause 7.1.1 of [5, TS38.213]

- SRS resource indicator – $\left\lceil \log_2 \left(\sum_{k=1}^{\min(L_{\max}, N_{\text{SRS}})} \binom{N_{\text{SRS}}}{k} \right) \right\rceil$ or $\left\lceil \log_2(N_{\text{SRS}}) \right\rceil$ bits, where N_{SRS} is the number of configured SRS resources in the SRS resource set associated with the higher layer parameter *usage* of value 'codeBook' or 'nonCodeBook',

- $\left\lceil \log_2 \left(\sum_{k=1}^{\min(L_{\max}, N_{\text{SRS}})} \binom{N_{\text{SRS}}}{k} \right) \right\rceil$ bits according to Tables 7.3.1.1.2-28/29/30/31 if the higher layer parameter *txConfig* = *nonCodebook*, where N_{SRS} is the number of configured SRS resources in the SRS resource set associated with the higher layer parameter *usage* of value 'nonCodeBook' and
 - if UE supports operation with *maxMIMO-Layers* and the higher layer parameter *maxMIMO-Layers* of *PUSCH-ServingCellConfig* of the serving cell is configured, $L_{\max}$ is given by that parameter
 - otherwise, $L_{\max}$ is given by the maximum number of layers for PUSCH supported by the UE for the serving cell for non-codebook based operation.
- $\left\lceil \log_2(N_{\text{SRS}}) \right\rceil$ bits according to Tables 7.3.1.1.2-32 if the higher layer parameter *txConfig* = *codebook*, where N_{SRS} is the number of configured SRS resources in the SRS resource set associated with the higher layer parameter *usage* of value 'codeBook'.

- Precoding information and number of layers – number of bits determined by the following:
 - 0 bits if the higher layer parameter *txConfig* = *nonCodeBook*;
 - 0 bits for 1 antenna port and if the higher layer parameter *txConfig* = *codebook*;
 - 4, 5, or 6 bits according to Table 7.3.1.1.2-2 for 4 antenna ports, if *txConfig* = *codebook*, and according to whether transform precoder is enabled or disabled, and the values of higher layer parameters *maxRank*, and *codebookSubset*;
 - 2, 4, or 5 bits according to Table 7.3.1.1.2-3 for 4 antenna ports, if *txConfig* = *codebook*, and according to whether transform precoder is enabled or disabled, and the values of higher layer parameters *maxRank*, and *codebookSubset*;
 - 2 or 4 bits according to Table 7.3.1.1.2-4 for 2 antenna ports, if *txConfig* = *codebook*, and according to whether transform precoder is enabled or disabled, and the values of higher layer parameters *maxRank* and *codebookSubset*;
 - 1 or 3 bits according to Table 7.3.1.1.2-5 for 2 antenna ports, if *txConfig* = *codebook*, and according to whether transform precoder is enabled or disabled, and the values of higher layer parameters *maxRank* and *codebookSubset*.

- Antenna ports – number of bits determined by the following

- 2 bits as defined by Tables 7.3.1.1.2-6, if transform precoder is enabled, *dmrs-Type*=1, and *maxLength*=1;
- 4 bits as defined by Tables 7.3.1.1.2-7, if transform precoder is enabled, *dmrs-Type*=1, and *maxLength*=2;
- 3 bits as defined by Tables 7.3.1.1.2-8/9/10/11, if transform precoder is disabled, *dmrs-Type*=1, and *maxLength*=1, and the value of rank is determined according to the SRS resource indicator field if the higher layer parameter *txConfig* = *nonCodebook* and according to the Precoding information and number of layers field if the higher layer parameter *txConfig* = *codebook*;
- 4 bits as defined by Tables 7.3.1.1.2-12/13/14/15, if transform precoder is disabled, *dmrs-Type*=1, and *maxLength*=2, and the value of rank is determined according to the SRS resource indicator field if the higher layer parameter *txConfig* = *nonCodebook* and according to the Precoding information and number of layers field if the higher layer parameter *txConfig* = *codebook*;
- 4 bits as defined by Tables 7.3.1.1.2-16/17/18/19, if transform precoder is disabled, *dmrs-Type*=2, and *maxLength*=1, and the value of rank is determined according to the SRS resource indicator field if the higher layer parameter *txConfig* = *nonCodebook* and according to the Precoding information and number of layers field if the higher layer parameter *txConfig* = *codebook*;
- 5 bits as defined by Tables 7.3.1.1.2-20/21/22/23, if transform precoder is disabled, *dmrs-Type*=2, and *maxLength*=2, and the value of rank is determined according to the SRS resource indicator field if the higher layer parameter *txConfig* = *nonCodebook* and according to the Precoding information and number of layers field if the higher layer parameter *txConfig* = *codebook*.

where the number of CDM groups without data of values 1, 2, and 3 in Tables 7.3.1.1.2-6 to 7.3.1.1.2-23 refers to CDM groups {0}, {0,1}, and {0, 1,2} respectively.

If a UE is configured with both *dmrs-UplinkForPUSCH-MappingTypeA* and *dmrs-UplinkForPUSCH-*

MappingTypeB, the bitwidth of this field equals $\max\{X_A, X_B\}$, where X_A is the "Antenna ports" bitwidth derived according to *dmrs-UplinkForPUSCH-MappingTypeA* and X_B is the "Antenna ports" bitwidth derived according to *dmrs-UplinkForPUSCH-MappingTypeB*. A number of $|X_A - X_B|$ zeros are padded in the MSB of this field, if the mapping type of the PUSCH corresponds to the smaller value of X_A and X_B .

- SRS request – 2 bits as defined by Table 7.3.1.1.2-24 for UEs not configured with *supplementaryUplink* in *ServingCellConfig* in the cell; 3 bits for UEs configured with *supplementaryUplink* in *ServingCellConfig* in the cell where the first bit is the non-SUL/SUL indicator as defined in Table 7.3.1.1.1-1 and the second and third bits are defined by Table 7.3.1.1.2-24. This bit field may also indicate the associated CSI-RS according to Subclause 6.1.1.2 of [6, TS 38.214].
- CSI request – 0, 1, 2, 3, 4, 5, or 6 bits determined by higher layer parameter *reportTriggerSize*.
- CBG transmission information (CBGTI) – 0 bit if higher layer parameter *codeBlockGroupTransmission* for PDSCH is not configured, otherwise, 2, 4, 6, or 8 bits determined by higher layer parameter *maxCodeBlockGroupsPerTransportBlock* for PUSCH.
- PTRS-DMRS association – number of bits determined as follows
 - 0 bit if *PTRS-UplinkConfig* is not configured and transform precoder is disabled, or if transform precoder is enabled, or if *maxRank*=1;
 - 2 bits otherwise, where Table 7.3.1.1.2-25 and 7.3.1.1.2-26 are used to indicate the association between PTRS port(s) and DMRS port(s) for transmission of one PT-RS port and two PT-RS ports respectively, and the DMRS ports are indicated by the Antenna ports field.

If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part and the "PTRS-DMRS association" field is present for the indicated bandwidth part but not present for the active bandwidth part, the UE assumes the "PTRS-DMRS association" field is not present for the indicated bandwidth part.

- beta_offset indicator – 0 if the higher layer parameter *betaOffsets* = *semiStatic*; otherwise 2 bits as defined by Table 9.3-3 in [5, TS 38.213].

- DMRS sequence initialization – 0 bit if transform precoder is enabled; 1 bit if transform precoder is disabled.
- UL-SCH indicator – 1 bit. A value of "1" indicates UL-SCH shall be transmitted on the PUSCH and a value of "0" indicates UL-SCH shall not be transmitted on the PUSCH. Except for DCI format 0_1 with CRC scrambled by SP-CSI-RNTI, a UE is not expected to receive a DCI format 0_1 with UL-SCH indicator of "0" and CSI request of all zero(s).

A UE does not expect that the bit width of a field in DCI format 0_1 with CRC scrambled by CS-RNTI is larger than corresponding bit width of same field in DCI format 0_1 with CRC scrambled by C-RNTI for the same serving cell. If the bit width of a field in the DCI format 0_1 with CRC scrambled by CS-RNTI is not equal to that of the corresponding field in the DCI format 0_1 with CRC scrambled by C-RNTI for the same serving cell, a number of most significant bits with value set to '0' are inserted to the field in DCI format 0_1 with CRC scrambled by CS-RNTI until the bit width equals that of the corresponding field in the DCI format 0_1 with CRC scrambled by C-RNTI for the same serving cell.

Table 7.3.1.1.2-1: Bandwidth part indicator

Value of BWP indicator field 2 bits	Bandwidth part
00	Configured BWP with BWP-Id = 1
01	Configured BWP with BWP-Id = 2
10	Configured BWP with BWP-Id = 3
11	Configured BWP with BWP-Id = 4

Table 7.3.1.1.2-2: Precoding information and number of layers, for 4 antenna ports, if transform precoder is disabled and $maxRank = 2$ or 3 or 4

Bit field mapped to index	<i>codebookSubset = fullyAndPartialAndNonCoherent</i>	Bit field mapped to index	<i>codebookSubset = partialAndNonCoherent</i>	Bit field mapped to index	<i>codebookSubset = nonCoherent</i>
0	1 layer: TPMI=0	0	1 layer: TPMI=0	0	1 layer: TPMI=0
1	1 layer: TPMI=1	1	1 layer: TPMI=1	1	1 layer: TPMI=1
...	...	...	...	...	...
3	1 layer: TPMI=3	3	1 layer: TPMI=3	3	1 layer: TPMI=3
4	2 layers: TPMI=0	4	2 layers: TPMI=0	4	2 layers: TPMI=0
...	...	...	...	...	...
9	2 layers: TPMI=5	9	2 layers: TPMI=5	9	2 layers: TPMI=5
10	3 layers: TPMI=0	10	3 layers: TPMI=0	10	3 layers: TPMI=0
11	4 layers: TPMI=0	11	4 layers: TPMI=0	11	4 layers: TPMI=0
12	1 layer: TPMI=4	12	1 layer: TPMI=4	12-15	reserved
...	...	...	...		
19	1 layer: TPMI=11	19	1 layer: TPMI=11		
20	2 layers: TPMI=6	20	2 layers: TPMI=6		
...	...	...	...		
27	2 layers: TPMI=13	27	2 layers: TPMI=13		
28	3 layers: TPMI=1	28	3 layers: TPMI=1		
29	3 layers: TPMI=2	29	3 layers: TPMI=2		
30	4 layers: TPMI=1	30	4 layers: TPMI=1		
31	4 layers: TPMI=2	31	4 layers: TPMI=2		
32	1 layers: TPMI=12				
...	...				
47	1 layers: TPMI=27				
48	2 layers: TPMI=14				
...	...				
55	2 layers: TPMI=21				
56	3 layers: TPMI=3				
...	...				
59	3 layers: TPMI=6				
60	4 layers: TPMI=3				
61	4 layers: TPMI=4				
62-63	reserved				

Table 7.3.1.1.2-3: Precoding information and number of layers for 4 antenna ports, if transform precoder is enabled, or if transform precoder is disabled and $maxRank = 1$

Bit field mapped to index	<i>codebookSubset = fullyAndPartialAndNonCoherent</i> <i>t</i>	Bit field mapped to index	<i>codebookSubset = partialAndNonCoherent</i> <i>t</i>	Bit field mapped to index	<i>codebookSubset = nonCoherent</i>
0	1 layer: TPMI=0	0	1 layer: TPMI=0	0	1 layer: TPMI=0
1	1 layer: TPMI=1	1	1 layer: TPMI=1	1	1 layer: TPMI=1
...	...	...	...	...	...
3	1 layer: TPMI=3	3	1 layer: TPMI=3	3	1 layer: TPMI=3
4	1 layer: TPMI=4	4	1 layer: TPMI=4		
...	...	...	...		
11	1 layer: TPMI=11	11	1 layer: TPMI=11		
12	1 layers: TPMI=12	12-15	reserved		
...	...				
27	1 layers: TPMI=27				
28-31	reserved				

Table 7.3.1.1.2-4: Precoding information and number of layers, for 2 antenna ports, if transform precoder is disabled and $maxRank = 2$

Bit field mapped to index	<i>codebookSubset = fullyAndPartialAndNonCoherent</i> <i>t</i>	Bit field mapped to index	<i>codebookSubset = nonCoherent</i>
0	1 layer: TPMI=0	0	1 layer: TPMI=0
1	1 layer: TPMI=1	1	1 layer: TPMI=1
2	2 layers: TPMI=0	2	2 layers: TPMI=0
3	1 layer: TPMI=2	3	reserved
4	1 layer: TPMI=3		
5	1 layer: TPMI=4		
6	1 layer: TPMI=5		
7	2 layers: TPMI=1		
8	2 layers: TPMI=2		
9-15	reserved		

Table 7.3.1.1.2-5: Precoding information and number of layers, for 2 antenna ports, if transform precoder is enabled, or if transform precoder is disabled and $maxRank = 1$

Bit field mapped to index	<i>codebookSubset = fullyAndPartialAndNonCoherent</i> <i>t</i>	Bit field mapped to index	<i>codebookSubset = nonCoherent</i>
0	1 layer: TPMI=0	0	1 layer: TPMI=0
1	1 layer: TPMI=1	1	1 layer: TPMI=1
2	1 layer: TPMI=2		
3	1 layer: TPMI=3		
4	1 layer: TPMI=4		
5	1 layer: TPMI=5		
6-7	reserved		

Table 7.3.1.1.2-6: Antenna port(s), transform precoder is enabled, $dmrs\text{-}Type=1$, $maxLength=1$

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0
1	2	1
2	2	2
3	2	3

Table 7.3.1.1.2-7: Antenna port(s), transform precoder is enabled, *dmrs-Type*=1, *maxLength*=2

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0	1
1	2	1	1
2	2	2	1
3	2	3	1
4	2	0	2
5	2	1	2
6	2	2	2
7	2	3	2
8	2	4	2
9	2	5	2
10	2	6	2
11	2	7	2
12-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-8: Antenna port(s), transform precoder is disabled, *dmrs-Type*=1, *maxLength*=1, rank = 1

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0
1	1	1
2	2	0
3	2	1
4	2	2
5	2	3
6-7	Reserved	Reserved

Table 7.3.1.1.2-9: Antenna port(s), transform precoder is disabled, *dmrs-Type*=1, *maxLength*=1, rank = 2

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0,1
1	2	0,1
2	2	2,3
3	2	0,2
4-7	Reserved	Reserved

Table 7.3.1.1.2-10: Antenna port(s), transform precoder is disabled, *dmrs-Type*=1, *maxLength*=1, rank = 3

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0-2
2-7	Reserved	Reserved

Table 7.3.1.1.2-11: Antenna port(s), transform precoder is disabled, *dmrs-Type*=1, *maxLength*=1, rank = 4

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0-3
2-7	Reserved	Reserved

Table 7.3.1.1.2-12: Antenna port(s), transform precoder is disabled, *dmrs-Type*=1, *maxLength*=2, rank = 1

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0	1
1	1	1	1
2	2	0	1
3	2	1	1
4	2	2	1
5	2	3	1
6	2	0	2
7	2	1	2
8	2	2	2
9	2	3	2
10	2	4	2
11	2	5	2
12	2	6	2
13	2	7	2
14-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-13: Antenna port(s), transform precoder is disabled, *dmrs-Type*=1, *maxLength*=2, rank = 2

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0,1	1
1	2	0,1	1
2	2	2,3	1
3	2	0,2	1
4	2	0,1	2
5	2	2,3	2
6	2	4,5	2
7	2	6,7	2
8	2	0,4	2
9	2	2,6	2
10-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-14: Antenna port(s), transform precoder is disabled, *dmrs-Type*=1, *maxLength*=2, rank = 3

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0-2	1
1	2	0,1,4	2
2	2	2,3,6	2
3-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-15: Antenna port(s), transform precoder is disabled, *dmrs-Type*=1, *maxLength*=2, rank = 4

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0-3	1
1	2	0,1,4,5	2
2	2	2,3,6,7	2
3	2	0,2,4,6	2
4-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-16: Antenna port(s), transform precoder is disabled, *dmrs-Type*=2, *maxLength*=1, rank=1

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0
1	1	1
2	2	0
3	2	1
4	2	2
5	2	3
6	3	0
7	3	1
8	3	2
9	3	3
10	3	4
11	3	5
12-15	Reserved	Reserved

Table 7.3.1.1.2-17: Antenna port(s), transform precoder is disabled, *dmrs-Type*=2, *maxLength*=1, rank=2

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0,1
1	2	0,1
2	2	2,3
3	3	0,1
4	3	2,3
5	3	4,5
6	2	0,2
7-15	Reserved	Reserved

Table 7.3.1.1.2-18: Antenna port(s), transform precoder is disabled, *dmrs-Type*=2, *maxLength*=1, rank=3

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0-2
1	3	0-2
2	3	3-5
3-15	Reserved	Reserved

Table 7.3.1.1.2-19: Antenna port(s), transform precoder is disabled, *dmrs-Type*=2, *maxLength*=1, rank=4

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0-3
1	3	0-3
2-15	Reserved	Reserved

Table 7.3.1.1.2-20: Antenna port(s), transform precoder is disabled, *dmrs-Type*=2, *maxLength*=2, rank=1

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0	1
1	1	1	1
2	2	0	1
3	2	1	1
4	2	2	1
5	2	3	1
6	3	0	1
7	3	1	1
8	3	2	1
9	3	3	1
10	3	4	1
11	3	5	1
12	3	0	2
13	3	1	2
14	3	2	2
15	3	3	2
16	3	4	2
17	3	5	2
18	3	6	2
19	3	7	2
20	3	8	2
21	3	9	2
22	3	10	2
23	3	11	2
24	1	0	2
25	1	1	2
26	1	6	2
27	1	7	2
28-31	Reserved	Reserved	Reserved

Table 7.3.1.1.2-21: Antenna port(s), transform precoder is disabled, *dmrs-Type*=2, *maxLength*=2, rank=2

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0,1	1
1	2	0,1	1
2	2	2,3	1
3	3	0,1	1
4	3	2,3	1
5	3	4,5	1
6	2	0,2	1
7	3	0,1	2
8	3	2,3	2
9	3	4,5	2
10	3	6,7	2
11	3	8,9	2
12	3	10,11	2
13	1	0,1	2
14	1	6,7	2
15	2	0,1	2
16	2	2,3	2
17	2	6,7	2
18	2	8,9	2
19-31	Reserved	Reserved	Reserved

Table 7.3.1.1.2-22: Antenna port(s), transform precoder is disabled, *dmrs-Type*=2, *maxLength*=2, rank=3

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0-2	1
1	3	0-2	1
2	3	3-5	1
3	3	0,1,6	2
4	3	2,3,8	2
5	3	4,5,10	2
6-31	Reserved	Reserved	Reserved

Table 7.3.1.1.2-23: Antenna port(s), transform precoder is disabled, *dmrs-Type*=2, *maxLength*=2, rank=4

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0-3	1
1	3	0-3	1
2	3	0,1,6,7	2
3	3	2,3,8,9	2
4	3	4,5,10,11	2
5-31	Reserved	Reserved	Reserved

Table 7.3.1.1.2-24: SRS request

Value of SRS request field	Triggered aperiodic SRS resource set(s) for DCI format 0_1, 1_1, and 2_3 configured with higher layer parameter <i>srs-TPC-PDCCH-Group</i> set to 'typeB'	Triggered aperiodic SRS resource set(s) for DCI format 2_3 configured with higher layer parameter <i>srs-TPC-PDCCH-Group</i> set to 'typeA'
00	No aperiodic SRS resource set triggered	No aperiodic SRS resource set triggered
01	SRS resource set(s) configured with higher layer parameter <i>aperiodicSRS-ResourceTrigger</i> set to 1 or an entry in <i>aperiodicSRS-ResourceTriggerList</i> set to 1	SRS resource set(s) configured with higher layer parameter <i>usage</i> in <i>SRS-ResourceSet</i> set to 'antennaSwitching' and <i>resourceType</i> in <i>SRS-ResourceSet</i> set to 'aperiodic' for a 1 st set of serving cells configured by higher layers
10	SRS resource set(s) configured with higher layer parameter <i>aperiodicSRS-ResourceTrigger</i> set to 2 or an entry in <i>aperiodicSRS-ResourceTriggerList</i> set to 2	SRS resource set(s) configured with higher layer parameter <i>usage</i> in <i>SRS-ResourceSet</i> set to 'antennaSwitching' and <i>resourceType</i> in <i>SRS-ResourceSet</i> set to 'aperiodic' for a 2 nd set of serving cells configured by higher layers
11	SRS resource set(s) configured with higher layer parameter <i>aperiodicSRS-ResourceTrigger</i> set to 3 or an entry in <i>aperiodicSRS-ResourceTriggerList</i> set to 3	SRS resource set(s) configured with higher layer parameter <i>usage</i> in <i>SRS-ResourceSet</i> set to 'antennaSwitching' and <i>resourceType</i> in <i>SRS-ResourceSet</i> set to 'aperiodic' for a 3 rd set of serving cells configured by higher layers

Table 7.3.1.1.2-25: PTRS-DMRS association for UL PTRS port 0

Value	DMRS port
0	1 st scheduled DMRS port
1	2 nd scheduled DMRS port
2	3 rd scheduled DMRS port
3	4 th scheduled DMRS port

Table 7.3.1.1.2-26: PTRS-DMRS association for UL PTRS ports 0 and 1

Value of MSB	DMRS port		Value of LSB	DMRS port
0	1 st DMRS port which shares PTRS port 0		0	1 st DMRS port which shares PTRS port 1
1	2 nd DMRS port which shares PTRS port 0		1	2 nd DMRS port which shares PTRS port 1

Table 7.3.1.1.2-27: void**Table 7.3.1.1.2-28: SRI indication for non-codebook based PUSCH transmission, $L_{\max}=1$**

Bit field mapped to index	SRI(s), $N_{\text{SRS}}=2$	Bit field mapped to index	SRI(s), $N_{\text{SRS}}=3$	Bit field mapped to index	SRI(s), $N_{\text{SRS}}=4$
0	0	0	0	0	0
1	1	1	1	1	1
		2	2	2	2
		3	reserved	3	3

Table 7.3.1.1.2-29: SRI indication for non-codebook based PUSCH transmission, $L_{\max}=2$

Bit field mapped to index	SRI(s), $N_{\text{SRS}}=2$	Bit field mapped to index	SRI(s), $N_{\text{SRS}}=3$	Bit field mapped to index	SRI(s), $N_{\text{SRS}}=4$
0	0	0	0	0	0
1	1	1	1	1	1
2	0,1	2	2	2	2
3	reserved	3	0,1	3	3
		4	0,2	4	0,1
		5	1,2	5	0,2
		6-7	reserved	6	0,3
				7	1,2
				8	1,3
				9	2,3
				10-15	reserved

Table 7.3.1.1.2-30: SRI indication for non-codebook based PUSCH transmission, $L_{\max}=3$

Bit field mapped to index	SRI(s), $N_{\text{SRS}}=2$	Bit field mapped to index	SRI(s), $N_{\text{SRS}}=3$	Bit field mapped to index	SRI(s), $N_{\text{SRS}}=4$
0	0	0	0	0	0
1	1	1	1	1	1
2	0,1	2	2	2	2
3	reserved	3	0,1	3	3
		4	0,2	4	0,1
		5	1,2	5	0,2
		6	0,1,2	6	0,3
		7	reserved	7	1,2
				8	1,3
				9	2,3
				10	0,1,2
				11	0,1,3
				12	0,2,3
				13	1,2,3
				14-15	reserved

Table 7.3.1.1.2-31: SRI indication for non-codebook based PUSCH transmission, $L_{\max}=4$

Bit field mapped to index	SRI(s), $N_{\text{SRS}}=2$	Bit field mapped to index	SRI(s), $N_{\text{SRS}}=3$	Bit field mapped to index	SRI(s), $N_{\text{SRS}}=4$
0	0	0	0	0	0
1	1	1	1	1	1
2	0,1	2	2	2	2
3	reserved	3	0,1	3	3
		4	0,2	4	0,1
		5	1,2	5	0,2
		6	0,1,2	6	0,3
		7	reserved	7	1,2
				8	1,3
				9	2,3
				10	0,1,2
				11	0,1,3
				12	0,2,3
				13	1,2,3
				14	0,1,2,3
				15	reserved

Table 7.3.1.1.2-32: SRI indication for codebook based PUSCH transmission

Bit field mapped to index	SRI(s), $N_{\text{SRS}}=2$
0	0
1	1

Table 7.3.1.1.2-33: Void

7.3.1.2 DCI formats for scheduling of PDSCH

7.3.1.2.1 Format 1_0

DCI format 1_0 is used for the scheduling of PDSCH in one DL cell.

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by C-RNTI or CS-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bits
- The value of this bit field is always set to 1, indicating a DL DCI format
- Frequency domain resource assignment – $\lceil \log_2(N_{\text{RB}}^{\text{DL,BWP}}(N_{\text{RB}}^{\text{DL,BWP}}+1)/2) \rceil$ bits where $N_{\text{RB}}^{\text{DL,BWP}}$ is given by subclause 7.3.1.0

If the CRC of the DCI format 1_0 is scrambled by C-RNTI and the "Frequency domain resource assignment" field are of all ones, the DCI format 1_0 is for random access procedure initiated by a PDCCH order, with all remaining fields set as follows:

- Random Access Preamble index – 6 bits according to *ra-PreambleIndex* in Subclause 5.1.2 of [8, TS38.321]
- UL/SUL indicator – 1 bit. If the value of the "Random Access Preamble index" is not all zeros and if the UE is configured with *supplementaryUplink* in *ServingCellConfig* in the cell, this field indicates which UL carrier in the cell to transmit the PRACH according to Table 7.3.1.1.1-1; otherwise, this field is reserved
- SS/PBCH index – 6 bits. If the value of the "Random Access Preamble index" is not all zeros, this field indicates the SS/PBCH that shall be used to determine the RACH occasion for the PRACH transmission; otherwise, this field is reserved.

- PRACH Mask index – 4 bits. If the value of the "Random Access Preamble index" is not all zeros, this field indicates the RACH occasion associated with the SS/PBCH indicated by "SS/PBCH index" for the PRACH transmission, according to Subclause 5.1.1 of [8, TS38.321]; otherwise, this field is reserved
- Reserved bits – 10 bits

Otherwise, all remaining fields are set as follows:

- Time domain resource assignment – 4 bits as defined in Subclause 5.1.2.1 of [6, TS 38.214]
- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5
- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3 of [6, TS 38.214]
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits
- Downlink assignment index – 2 bits as defined in Subclause 9.1.3 of [5, TS 38.213], as counter DAI
- TPC command for scheduled PUCCH – 2 bits as defined in Subclause 7.2.1 of [5, TS 38.213]
- PUCCH resource indicator – 3 bits as defined in Subclause 9.2.3 of [5, TS 38.213]
- PDSCH-to-HARQ_feedback timing indicator – 3 bits as defined in Subclause 9.2.3 of [5, TS38.213]

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by P-RNTI:

- Short Messages Indicator – 2 bits according to Table 7.3.1.2.1-1.
- Short Messages – 8 bits, according to Subclause 6.5 of [9, TS38.331]. If only the scheduling information for Paging is carried, this bit field is reserved.
- Frequency domain resource assignment – $\lceil \log_2(N_{RB}^{DL,BWP}(N_{RB}^{DL,BWP}+1)/2) \rceil$ bits. If only the short message is carried, this bit field is reserved.
- $N_{RB}^{DL,BWP}$ is the size of CORESET 0
- Time domain resource assignment – 4 bits as defined in Subclause 5.1.2.1 of [6, TS38.214]. If only the short message is carried, this bit field is reserved.
- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5. If only the short message is carried, this bit field is reserved.
- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1. If only the short message is carried, this bit field is reserved.
- TB scaling – 2 bits as defined in Subclause 5.1.3.2 of [6, TS38.214]. If only the short message is carried, this bit field is reserved.
- Reserved bits – 6 bits

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by SI-RNTI:

- Frequency domain resource assignment – $\lceil \log_2(N_{RB}^{DL,BWP}(N_{RB}^{DL,BWP}+1)/2) \rceil$ bits
- $N_{RB}^{DL,BWP}$ is the size of CORESET 0
- Time domain resource assignment – 4 bits as defined in Subclause 5.1.2.1 of [6, TS38.214]

- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5
- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- System information indicator – 1 bit as defined in Table 7.3.1.2.1-2
- Reserved bits – [15] bits

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by RA-RNTI:

- Frequency domain resource assignment – $\lceil \log_2(N_{RB}^{DL,BWP}(N_{RB}^{DL,BWP}+1)/2) \rceil$ bits
- $N_{RB}^{DL,BWP}$ is the size of CORESET 0 if CORESET 0 is configured for the cell and $N_{RB}^{DL,BWP}$ is the size of initial DL bandwidth part if CORESET 0 is not configured for the cell
- Time domain resource assignment – 4 bits as defined in Subclause 5.1.2.1 of [6, TS38.214]
- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5
- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1
- TB scaling – 2 bits as defined in Subclause 5.1.3.2 of [6, TS38.214]
- Reserved bits – 16 bits

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by TC-RNTI:

- Identifier for DCI formats – 1 bit
 - The value of this bit field is always set to 1, indicating a DL DCI format
- Frequency domain resource assignment – $\lceil \log_2(N_{RB}^{DL,BWP}(N_{RB}^{DL,BWP}+1)/2) \rceil$ bits
- $N_{RB}^{DL,BWP}$ is the size of CORESET 0
- Time domain resource assignment – 4 bits as defined in Subclause 5.1.2.1 of [6, TS38.214]
- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5
- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits
- Downlink assignment index – 2 bits, reserved
- TPC command for scheduled PUCCH – 2 bits as defined in Subclause 7.2.1 of [5, TS38.213]
- PUCCH resource indicator – 3 bits as defined in Subclause 9.2.3 of [5, TS38.213]
- PDSCH-to-HARQ_feedback timing indicator – 3 bits as defined in Subclause 9.2.3 of [5, TS38.213]

Table 7.3.1.2.1-1: Short Message indicator

Bit field	Short Message indicator
00	Reserved
01	Only scheduling information for Paging is present in the DCI
10	Only short message is present in the DCI
11	Both scheduling information for Paging and short message are present in the DCI

Table 7.3.1.2.1-2: System information indicator

Bit field	System information indicator
0	SIB1 [9, TS38.331, Subclause 5.2.1]
1	SI message [9, TS38.331, Subclause 5.2.1]

7.3.1.2.2 Format 1_1

DCI format 1_1 is used for the scheduling of PDSCH in one cell.

The following information is transmitted by means of the DCI format 1_1 with CRC scrambled by C-RNTI or CS-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bits
- The value of this bit field is always set to 1, indicating a DL DCI format
- Carrier indicator – 0 or 3 bits as defined in Subclause 10.1 of [5, TS 38.213].
- Bandwidth part indicator – 0, 1 or 2 bits as determined by the number of DL BWPs $n_{\text{BWP,RRC}}$ configured by higher layers, excluding the initial DL bandwidth part. The bitwidth for this field is determined as $\lceil \log_2(n_{\text{BWP}}) \rceil$ bits, where
 - $n_{\text{BWP}} = n_{\text{BWP,RRC}} + 1$ if $n_{\text{BWP,RRC}} \neq 0$, in which case the bandwidth part indicator is equivalent to the ascending order of the higher layer parameter *BWP-Id*;
 - otherwise $n_{\text{BWP}} = n_{\text{BWP,RRC}}$, in which case the bandwidth part indicator is defined in Table 7.3.1.1.2-1;

If a UE does not support active BWP change via DCI, the UE ignores this bit field.

- Frequency domain resource assignment – number of bits determined by the following, where $N_{\text{RB}}^{\text{DL,BWP}}$ is the size of the active DL bandwidth part:
 - N_{RBG} bits if only resource allocation type 0 is configured, where N_{RBG} is defined in Subclause 5.1.2.2.1 of [6, TS38.214],
 - $\lceil \log_2(N_{\text{RB}}^{\text{DL,BWP}}(N_{\text{RB}}^{\text{DL,BWP}} + 1)/2) \rceil$ bits if only resource allocation type 1 is configured, or
 - $\max(\lceil \log_2(N_{\text{RB}}^{\text{DL,BWP}}(N_{\text{RB}}^{\text{DL,BWP}} + 1)/2) \rceil, N_{\text{RBG}}) + 1$ bits if both resource allocation type 0 and 1 are configured.
 - If both resource allocation type 0 and 1 are configured, the MSB bit is used to indicate resource allocation type 0 or resource allocation type 1, where the bit value of 0 indicates resource allocation type 0 and the bit value of 1 indicates resource allocation type 1.
 - For resource allocation type 0, the N_{RBG} LSBs provide the resource allocation as defined in Subclause 5.1.2.2.1 of [6, TS 38.214].

- For resource allocation type 1, the $\lceil \log_2(N_{RB}^{DL,BWP}(N_{RB}^{DL,BWP}+1)/2) \rceil$ LSBs provide the resource allocation as defined in Subclause 5.1.2.2.2 of [6, TS 38.214]

If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part and if both resource allocation type 0 and 1 are configured for the indicated bandwidth part, the UE assumes resource allocation type 0 for the indicated bandwidth part if the bitwidth of the "Frequency domain resource assignment" field of the active bandwidth part is smaller than the bitwidth of the "Frequency domain resource assignment" field of the indicated bandwidth part.

- Time domain resource assignment – 0, 1, 2, 3, or 4 bits as defined in Subclause 5.1.2.1 of [6, TS 38.214]. The bitwidth for this field is determined as $\lceil \log_2(I) \rceil$ bits, where I is the number of entries in the higher layer parameter *pdsch-TimeDomainAllocationList* if the higher layer parameter is configured; otherwise I is the number of entries in the default table.
- VRB-to-PRB mapping – 0 or 1 bit:
 - 0 bit if only resource allocation type 0 is configured or if interleaved VRB-to-PRB mapping is not configured by high layers;
 - 1 bit according to Table 7.3.1.2.2-5 otherwise, only applicable to resource allocation type 1, as defined in Subclause 7.3.1.6 of [4, TS 38.211].
- PRB bundling size indicator – 0 bit if the higher layer parameter *prb-BundlingType* is not configured or is set to 'static', or 1 bit if the higher layer parameter *prb-BundlingType* is set to 'dynamic' according to Subclause 5.1.2.3 of [6, TS 38.214].
- Rate matching indicator – 0, 1, or 2 bits according to higher layer parameters *rateMatchPatternGroup1* and *rateMatchPatternGroup2*, where the MSB is used to indicate *rateMatchPatternGroup1* and the LSB is used to indicate *rateMatchPatternGroup2* when there are two groups.
- ZP CSI-RS trigger – 0, 1, or 2 bits as defined in Subclause 5.1.4.2 of [6, TS 38.214]. The bitwidth for this field is determined as $\lceil \log_2(n_{ZP}+1) \rceil$ bits, where n_{ZP} is the number of aperiodic ZP CSI-RS resource sets configured by higher layer.

For transport block 1:

- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3.1 of [6, TS 38.214]
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2

For transport block 2 (only present if *maxNrofCodeWordsScheduledByDCI* equals 2):

- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3.1 of [6, TS 38.214]
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2

If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part and the value of *maxNrofCodeWordsScheduledByDCI* for the indicated bandwidth part equals 2 and the value of *maxNrofCodeWordsScheduledByDCI* for the active bandwidth part equals 1, the UE assumes zeros are padded when interpreting the "Modulation and coding scheme", "New data indicator", and "Redundancy version" fields of transport block 2 according to Subclause 12 of [5, TS38.213], and the UE ignores the "Modulation and coding scheme", "New data indicator", and "Redundancy version" fields of transport block 2 for the indicated bandwidth part.

- HARQ process number – 4 bits
- Downlink assignment index – number of bits as defined in the following
 - 4 bits if more than one serving cell are configured in the DL and the higher layer parameter *pdsch-HARQ-ACK-Codebook=dynamic*, where the 2 MSB bits are the counter DAI and the 2 LSB bits are the total DAI;

- 2 bits if only one serving cell is configured in the DL and the higher layer parameter *pdsch-HARQ-ACK-Codebook=dynamic*, where the 2 bits are the counter DAI;
- 0 bits otherwise.
- TPC command for scheduled PUCCH – 2 bits as defined in Subclause 7.2.1 of [5, TS 38.213]
- PUCCH resource indicator – 3 bits as defined in Subclause 9.2.3 of [5, TS 38.213]
- PDSCH-to-HARQ_feedback timing indicator – 0, 1, 2, or 3 bits as defined in Subclause 9.2.3 of [5, TS 38.213]. The bitwidth for this field is determined as $\lceil \log_2(I) \rceil$ bits, where I is the number of entries in the higher layer parameter *dl-DataToUL-ACK*.
- Antenna port(s) – 4, 5, or 6 bits as defined by Tables 7.3.1.2.2-1/2/3/4, where the number of CDM groups without data of values 1, 2, and 3 refers to CDM groups {0}, {0,1}, and {0, 1,2} respectively. The antenna ports $\{p_0, \dots, p_{v-1}\}$ shall be determined according to the ordering of DMRS port(s) given by Tables 7.3.1.2.2-1/2/3/4.

If a UE is configured with both *dmrs-DownlinkForPDSCH-MappingTypeA* and *dmrs-DownlinkForPDSCH-MappingTypeB*, the bitwidth of this field equals $\max\{x_A, x_B\}$, where x_A is the "Antenna ports" bitwidth derived according to *dmrs-DownlinkForPDSCH-MappingTypeA* and x_B is the "Antenna ports" bitwidth derived according to *dmrs-DownlinkForPDSCH-MappingTypeB*. A number of $|x_A - x_B|$ zeros are padded in the MSB of this field, if the mapping type of the PDSCH corresponds to the smaller value of x_A and x_B .

- Transmission configuration indication – 0 bit if higher layer parameter *tci-PresentInDCI* is not enabled; otherwise 3 bits as defined in Subclause 5.1.5 of [6, TS38.214].

If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part,

- if the higher layer parameter *tci-PresentInDCI* is not enabled for the CORESET used for the PDCCH carrying the DCI format 1_1,
 - the UE assumes *tci-PresentInDCI* is not enabled for all CORESETs in the indicated bandwidth part;
- otherwise,
 - the UE assumes *tci-PresentInDCI* is enabled for all CORESETs in the indicated bandwidth part.
- SRS request – 2 bits as defined by Table 7.3.1.1.2-24 for UEs not configured with *supplementaryUplink* in *ServingCellConfig* in the cell; 3 bits for UEs configured with *supplementaryUplink* in *ServingCellConfig* in the cell where the first bit is the non-SUL/SUL indicator as defined in Table 7.3.1.1.1-1 and the second and third bits are defined by Table 7.3.1.1.2-24. This bit field may also indicate the associated CSI-RS according to Subclause 6.1.1.2 of [6, TS 38.214].
- CBG transmission information (CBGTI) – 0 bit if higher layer parameter *codeBlockGroupTransmission* for PDSCH is not configured, otherwise, 2, 4, 6, or 8 bits as defined in Subclause 5.1.7 of [6, TS38.214], determined by the higher layer parameters *maxCodeBlockGroupsPerTransportBlock* and *maxNrofCodeWordsScheduledByDCI* for the PDSCH.
- CBG flushing out information (CBGFI) – 1 bit if higher layer parameter *codeBlockGroupFlushIndicator* is configured as "TRUE", 0 bit otherwise.
- DMRS sequence initialization – 1 bit.

If DCI formats 1_1 are monitored in multiple search spaces associated with multiple CORESETs in a BWP for scheduling the same serving cell, zeros shall be appended until the payload size of the DCI formats 1_1 monitored in the multiple search spaces equal to the maximum payload size of the DCI format 1_1 monitored in the multiple search spaces.

Table 7.3.1.2.2-1: Antenna port(s) (1000 + DMRS port), *dmrs-Type*=1, *maxLength*=1

One Codeword: Codeword 0 enabled, Codeword 1 disabled		
Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0
1	1	1
2	1	0,1
3	2	0
4	2	1
5	2	2
6	2	3
7	2	0,1
8	2	2,3
9	2	0-2
10	2	0-3
11	2	0,2
12-15	Reserved	Reserved

Table 7.3.1.2.2-2: Antenna port(s) (1000 + DMRS port), *dmrs-Type*=1, *maxLength*=2

One Codeword: Codeword 0 enabled, Codeword 1 disabled				Two Codewords: Codeword 0 enabled, Codeword 1 enabled			
Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols	Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0	1	0	2	0-4	2
1	1	1	1	1	2	0,1,2,3,4,6	2
2	1	0,1	1	2	2	0,1,2,3,4,5,6	2
3	2	0	1	3	2	0,1,2,3,4,5,6,7	2
4	2	1	1	4-31	reserved	reserved	reserved
5	2	2	1				
6	2	3	1				
7	2	0,1	1				
8	2	2,3	1				
9	2	0-2	1				
10	2	0-3	1				
11	2	0,2	1				
12	2	0	2				
13	2	1	2				
14	2	2	2				
15	2	3	2				
16	2	4	2				
17	2	5	2				
18	2	6	2				
19	2	7	2				
20	2	0,1	2				
21	2	2,3	2				
22	2	4,5	2				
23	2	6,7	2				
24	2	0,4	2				
25	2	2,6	2				
26	2	0,1,4	2				
27	2	2,3,6	2				
28	2	0,1,4,5	2				
29	2	2,3,6,7	2				
30	2	0,2,4,6	2				
31	Reserved	Reserved	Reserved				

Table 7.3.1.2.2-3: Antenna port(s) (1000 + DMRS port), *dmrs-Type*=2, *maxLength*=1

One codeword: Codeword 0 enabled, Codeword 1 disabled			Two codewords: Codeword 0 enabled, Codeword 1 enabled		
Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0	0	3	0-4
1	1	1	1	3	0-5
2	1	0,1	2-31	reserved	reserved
3	2	0			
4	2	1			
5	2	2			
6	2	3			
7	2	0,1			
8	2	2,3			
9	2	0-2			
10	2	0-3			
11	3	0			
12	3	1			
13	3	2			
14	3	3			
15	3	4			
16	3	5			
17	3	0,1			
18	3	2,3			
19	3	4,5			
20	3	0-2			
21	3	3-5			
22	3	0-3			
23	2	0,2			
24-31	Reserved	Reserved			

Table 7.3.1.2.2-4: Antenna port(s) (1000 + DMRS port), *dmrs-Type=2*, *maxLength=2*

One codeword: Codeword 0 enabled, Codeword 1 disabled				Two Codewords: Codeword 0 enabled, Codeword 1 enabled			
Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols	Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0	1	0	3	0-4	1
1	1	1	1	1	3	0-5	1
2	1	0,1	1	2	2	0,1,2,3,6	2
3	2	0	1	3	2	0,1,2,3,6,8	2
4	2	1	1	4	2	0,1,2,3,6,7,8	2
5	2	2	1	5	2	0,1,2,3,6,7,8,9	2
6	2	3	1	6-63	Reserved	Reserved	Reserved
7	2	0,1	1				
8	2	2,3	1				
9	2	0-2	1				
10	2	0-3	1				
11	3	0	1				
12	3	1	1				
13	3	2	1				
14	3	3	1				
15	3	4	1				
16	3	5	1				
17	3	0,1	1				
18	3	2,3	1				
19	3	4,5	1				
20	3	0-2	1				
21	3	3-5	1				
22	3	0-3	1				
23	2	0,2	1				
24	3	0	2				
25	3	1	2				
26	3	2	2				
27	3	3	2				
28	3	4	2				
29	3	5	2				
30	3	6	2				
31	3	7	2				
32	3	8	2				
33	3	9	2				
34	3	10	2				
35	3	11	2				
36	3	0,1	2				
37	3	2,3	2				
38	3	4,5	2				
39	3	6,7	2				
40	3	8,9	2				
41	3	10,11	2				
42	3	0,1,6	2				
43	3	2,3,8	2				
44	3	4,5,10	2				
45	3	0,1,6,7	2				
46	3	2,3,8,9	2				
47	3	4,5,10,11	2				
48	1	0	2				
49	1	1	2				
50	1	6	2				
51	1	7	2				
52	1	0,1	2				
53	1	6,7	2				
54	2	0,1	2				
55	2	2,3	2				
56	2	6,7	2				

57	2	8,9	2				
58-63	Reserved	Reserved	Reserved				

Table 7.3.1.2.2-5: VRB-to-PRB mapping

Bit field mapped to index	VRB-to-PRB mapping
0	Non-interleaved
1	Interleaved

7.3.1.3 DCI formats for other purposes

7.3.1.3.1 Format 2_0

DCI format 2_0 is used for notifying the slot format.

The following information is transmitted by means of the DCI format 2_0 with CRC scrambled by SFI-RNTI:

- Slot format indicator 1, Slot format indicator 2, ..., Slot format indicator N .

The size of DCI format 2_0 is configurable by higher layers up to 128 bits, according to Subclause 11.1.1 of [5, TS 38.213].

7.3.1.3.2 Format 2_1

DCI format 2_1 is used for notifying the PRB(s) and OFDM symbol(s) where UE may assume no transmission is intended for the UE.

The following information is transmitted by means of the DCI format 2_1 with CRC scrambled by INT-RNTI:

- Pre-emption indication 1, Pre-emption indication 2, ..., Pre-emption indication N .

The size of DCI format 2_1 is configurable by higher layers up to 126 bits, according to Subclause 11.2 of [5, TS 38.213]. Each pre-emption indication is 14 bits.

7.3.1.3.3 Format 2_2

DCI format 2_2 is used for the transmission of TPC commands for PUCCH and PUSCH.

The following information is transmitted by means of the DCI format 2_2 with CRC scrambled by TPC-PUSCH-RNTI or TPC-PUCCH-RNTI:

- block number 1, block number 2, ..., block number N

The parameter *tpc-PUSCH* or *tpc-PUCCH* provided by higher layers determines the index to the block number for an UL of a cell, with the following fields defined for each block:

- Closed loop indicator – 0 or 1 bit.
- For DCI format 2_2 with TPC-PUSCH-RNTI, 0 bit if the UE is not configured with high layer parameter *twoPUSCH-PC-AdjustmentStates*, in which case UE assumes each block in the DCI format 2_2 is of 2 bits; 1 bit otherwise, in which case UE assumes each block in the DCI format 2_2 is of 3 bits;
- For DCI format 2_2 with TPC-PUCCH-RNTI, 0 bit if the UE is not configured with high layer parameter *twoPUCCH-PC-AdjustmentStates*, in which case UE assumes each block in the DCI format 2_2 is of 2 bits; 1 bit otherwise, in which case UE assumes each block in the DCI format 2_2 is of 3 bits;
- TPC command – 2 bits

The number of information bits in format 2_2 shall be equal to or less than the payload size of format 1_0 monitored in common search space in the same serving cell. If the number of information bits in format 2_2 is less than the payload size of format 1_0 monitored in common search space in the same serving cell, zeros shall be appended to format 2_2 until the payload size equals that of format 1_0 monitored in common search space in the same serving cell.

7.3.1.3.4 Format 2_3

DCI format 2_3 is used for the transmission of a group of TPC commands for SRS transmissions by one or more UEs. Along with a TPC command, a SRS request may also be transmitted.

The following information is transmitted by means of the DCI format 2_3 with CRC scrambled by TPC-SRS-RNTI:

- block number 1, block number 2, ..., block number B

where the starting position of a block is determined by the parameter *startingBitOfFormat2-3* or *startingBitOfFormat2-3SUL-v1530* provided by higher layers for the UE configured with the block.

If the UE is configured with higher layer parameter *srs-TPC-PDCCH-Group* = *typeA* for an UL without PUCCH and PUSCH or an UL on which the SRS power control is not tied with PUSCH power control, one block is configured for the UE by higher layers, with the following fields defined for the block:

- SRS request – 0 or 2 bits. The presence of this field is according to the definition in Subclause 11.4 of [5, TS38.213]. If present, this field is interpreted as defined by Table 7.3.1.1.2-24.
- TPC command number 1, TPC command number 2, ..., TPC command number N , where each TPC command applies to a respective UL carrier provided by higher layer parameter *cc-IndexInOneCC-Set*

If the UE is configured with higher layer parameter *srs-TPC-PDCCH-Group* = *typeB* for an UL without PUCCH and PUSCH or an UL on which the SRS power control is not tied with PUSCH power control, one block or more blocks is configured for the UE by higher layers where each block applies to an UL carrier, with the following fields defined for each block:

- SRS request – 0 or 2 bits. The presence of this field is according to the definition in Subclause 11.4 of [5, TS38.213]. If present, this field is interpreted as defined by Table 7.3.1.1.2-24.
- TPC command – 2 bits

The number of information bits in format 2_3 shall be equal to or less than the payload size of format 1_0 monitored in common search space in the same serving cell. If the number of information bits in format 2_3 is less than the payload size of format 1_0 monitored in common search space in the same serving cell, zeros shall be appended to format 2_3 until the payload size equals that of format 1_0 monitored in common search space in the same serving cell.

7.3.2 CRC attachment

Error detection is provided on DCI transmissions through a Cyclic Redundancy Check (CRC).

The entire payload is used to calculate the CRC parity bits. Denote the bits of the payload by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, and the parity bits by $p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the payload size and L is the number of parity bits. Let $a'_0, a'_1, a'_2, a'_3, \dots, a'_{A+L-1}$ be a bit sequence such that $a'_i = 1$ for $i=0, 1, \dots, L-1$ and $a'_i = a_{i-L}$ for $i=L, L+1, \dots, A+L-1$. The parity bits are computed with input bit sequence $a'_0, a'_1, a'_2, a'_3, \dots, a'_{A+L-1}$ and attached according to Subclause 5.1 by setting L to 24 bits and using the generator polynomial $g_{\text{CRC24C}}(D)$. The output bit $b_0, b_1, b_2, b_3, \dots, b_{K-1}$ is

$$b_k = a_k \quad \text{for } k=0, 1, 2, \dots, A-1$$

$$b_k = p_{k-A} \quad \text{for } k=A, A+1, A+2, \dots, A+L-1,$$

where $K = A + L$.

After attachment, the CRC parity bits are scrambled with the corresponding RNTI $x_{\text{rnti},0}, x_{\text{rnti},1}, \dots, x_{\text{rnti},15}$, where $x_{\text{rnti},0}$ corresponds to the MSB of the RNTI, to form the sequence of bits $c_0, c_1, c_2, c_3, \dots, c_{K-1}$. The relation between c_k and b_k is:

$$c_k = b_k \quad \text{for } k = 0, 1, 2, \dots, A+7$$

$$c_k = (b_k + x_{mti, k-A-8}) \bmod 2 \quad \text{for } k = A+8, A+9, A+10, \dots, A+23.$$

7.3.3 Channel coding

Information bits are delivered to the channel coding block. They are denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits, and they are encoded via Polar coding according to Subclause 5.3.1, by setting $n_{\max} = 9$, $I_{LL} = 1$, $n_{PC} = 0$, and $n_{PC}^{wm} = 0$.

After encoding the bits are denoted by $d_0, d_1, d_2, d_3, \dots, d_{N-1}$, where N is the number of coded bits.

7.3.4 Rate matching

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$.

Rate matching is performed according to Subclause 5.4.1 by setting $I_{BIL} = 0$.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

Annex <A> (informative): Change history

Change history							
Date	Meeting	TDoc	CR	Rev	Cat	Subject/Comment	New version
2017-05	RAN1#89	R1-1707082				Draft skeleton	0.0.0
2017-07	AH_NR2	R1-1712014				Inclusion of LDPC related agreements	0.0.1
2017-08	RAN1#90	R1-1714564				Inclusion of Polar coding related agreements	0.0.2
2017-08	RAN1#90	R1-1714659				Endorsed version by RAN1#90 as basis for further updates	0.1.0
2017-09	RAN1#90	R1-1715322				Capturing additional agreements on LDPC and Polar code from RAN1 #90	0.1.1
2017-09	RAN#77	RP-171991				For information to plenary	1.0.0
2017-09	RAN1#90b	R1-1716928				Capturing additional agreements on LDPC and Polar code from RAN1 NR AH#3	1.0.1
2017-10	RAN1#90b	R1-1719106				Endorsed as v1.1.0	1.1.0
2017-11	RAN1#91	R1-1719225				Capturing additional agreements on channel coding, etc.	1.1.1
2017-11	RAN1#91	R1-1719245				Capturing additional agreements on DCI format, channel coding, etc.	1.1.2
2017-11	RAN1#91	R1-1721049				Endorsed as v1.2.0	1.2.0
2017-12	RAN1#91	R1-1721342				Capturing additional agreements on UCI, DCI, channel coding, etc.	1.2.1
2017-12	RAN#78	RP-172668				Endorsed version for approval by plenary.	2.0.0
2017-12	RAN#78					Approved by plenary – Rel-15 spec under change control	15.0.0
2018-03	RAN#79	RP-180200	0001	-	F	CR capturing the Jan18 ad-hoc and RAN1#92 meeting agreements	15.1.0
2018-04	RAN#79					MCC: correction of typo in DCI format 0_1 (time domain resource assignment) – higher layer parameter should be <i>pusch-AllocationList</i>	15.1.1
2018-06	RAN#80	RP-181172	0002	1	F	CR to 38.212 capturing the RAN1#92bis and RAN1#93 meeting agreements	15.2.0
2018-06	RAN#80	RP-181257	0003	-	B	CR to 38.212 capturing the RAN1#92bis and RAN1#93 meeting agreements related to URLLC	15.2.0
2018-09	RAN#81	RP-181789	0004	-	F	CR to 38.212 capturing the RAN1#94 meeting agreements	15.3.0
2018-12	RAN#82	RP-182523	0005	3	F	Combined CR of all essential corrections to 38.212 from RAN1#94bis and RAN1#95	15.4.0
2019-03	RAN#83	RP-190448	0006	-	F	Correction of wrong implementation on frequency domain resource assignment bitwidth	15.5.0
2019-03	RAN#83	RP-190448	0008	-	F	Correction to UCI multiplexing	15.5.0
2019-03	RAN#83	RP-190448	0009	-	F	Correction on DCI format 2_3 for SUL cell in TS 38.212	15.5.0
2019-03	RAN#83	RP-190448	0010	-	F	Corrections to TS38.212	15.5.0
2019-03	RAN#83	RP-190448	0011	-	F	On bitwidth calculation for DCI fields using RRC parameter indicating maximum number of MIMO layers per serving cell	15.5.0
2019-03	RAN#83	RP-190448	0012	-	F	CR on zero-padding of DCI 1_1 in cross-carrier scheduling case	15.5.0
2019-03	RAN#83	RP-190448	0013	-	F	Clarification on UL_SUL indicator field and SRS request field	15.5.0
2019-06	RAN#84	RP-191282	0014	-	F	CR on correction to bitwidth of NNZC indicator	15.6.0
2019-06	RAN#84	RP-191282	0015	-	F	Correction on DCI size alignment in TS 38.212	15.6.0
2019-06	RAN#84	RP-191282	0016	-	F	Correction on UL/SUL indicator in DCI format 0_0	15.6.0
2019-06	RAN#84	RP-191282	0017	-	F	Corrections to 38.212 including alignment of terminology across specifications	15.6.0
2019-06	RAN#84	RP-191282	0018	-	F	CR on maximum modulation order configured for serving cell	15.6.0
2019-06	RAN#84	RP-191282	0019	1	F	Corrections to 38.212 including alignment of terminology across specifications from RAN1#97	15.6.0
2019-09	RAN#85	RP-191941	0020	-	F	Corrections to 38.212 including alignment of terminology across specifications in RAN1#98	15.7.0