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NR;
Multiplexing and channel coding
(Release 15)**



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Foreword

This Technical Specification has been produced by the 3rd Generation Partnership Project (3GPP).

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1 Scope

The present document specifies the coding, multiplexing and mapping to physical channels for 5G NR.

2 References

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

- References are either specific (identified by date of publication, edition number, version number, etc.) or non-specific.
- For a specific reference, subsequent revisions do not apply.
- For a non-specific reference, the latest version applies. In the case of a reference to a 3GPP document (including a GSM document), a non-specific reference implicitly refers to the latest version of that document *in the same Release as the present document*.

- [1] 3GPP TR 21.905: "Vocabulary for 3GPP Specifications".
 - [2] 3GPP TS 38.201: "NR; Physical Layer – General Description"
 - [3] 3GPP TS 38.202: "NR; Services provided by the physical layer"
 - [4] 3GPP TS 38.211: "NR; Physical channels and modulation"
 - [5] 3GPP TS 38.213: "NR; Physical layer procedures for control"
 - [6] 3GPP TS 38.214: "NR; Physical layer procedures for data"
 - [7] 3GPP TS 38.215: "NR; Physical layer measurements"
 - [8] 3GPP TS 38.321: "NR; Medium Access Control (MAC) protocol specification"
-

3 Definitions, symbols and abbreviations

3.1 Definitions

For the purposes of the present document, the terms and definitions given in 3GPP TR 21.905 [1] and the following apply. A term defined in the present document takes precedence over the definition of the same term, if any, in 3GPP TR 21.905 [1].

3.2 Symbols

For the purposes of the present document, the following symbols apply:

3.3 Abbreviations

For the purposes of the present document, the abbreviations given in 3GPP TR 21.905 [1] and the following apply. An abbreviation defined in the present document takes precedence over the definition of the same abbreviation, if any, in 3GPP TR 21.905 [1].

BCH	Broadcast channel
CBG	Code block group
CBGTI	Code block group transmission information
CQI	Channel quality indicator
CRC	Cyclic redundancy check
CRI	CSI-RS resource indicator

CSI	Channel state information
CSI-RS	CSI reference signal
DAI	Downlink assignment index
DCI	Downlink control information
DL	Downlink
DL-SCH	Downlink shared channel
DMRS	Dedicated demodulation reference signal
HARQ	Hybrid automatic repeat request
HARQ-ACK	Hybrid automatic repeat request acknowledgement
LDPC	Low density parity check
LI	Layer indicator
MCS	Modulation and coding scheme
OFDM	Orthogonal frequency division multiplex
PBCH	Physical broadcast channel
PCH	Paging channel
PDCCH	Physical downlink control channel
PDSCH	Physical downlink shared channel
PMI	Precoding matrix indicator
PRB	Physical resource block
PRACH	Physical random access channel
PTRS	Phase-tracking reference signal
PUCCH	Physical uplink control channel
PUSCH	Physical uplink shared channel
RACH	Random access channel
RI	Rank indicator
RSRP	Reference signal received power
SFN	System frame number
SR	Scheduling request
SRS	Sounding reference signal
SS	Synchronisation signal
SUL	Supplementary uplink
TPC	Transmit power control
UCI	Uplink control information
UE	User equipment
UL	Uplink
UL-SCH	Uplink shared channel
VRB	Virtual resource block
ZP CSI-RS	Zero power CSI-RS

4 Mapping to physical channels

4.1 Uplink

Table 4.1-1 specifies the mapping of the uplink transport channels to their corresponding physical channels. Table 4.1-2 specifies the mapping of the uplink control channel information to its corresponding physical channel.

Table 4.1-1

TrCH	Physical Channel
UL-SCH	PUSCH
RACH	PRACH

Table 4.1-2

Control information	Physical Channel
UCI	PUCCH, PUSCH

4.2 Downlink

Table 4.2-1 specifies the mapping of the downlink transport channels to their corresponding physical channels. Table 4.2-2 specifies the mapping of the downlink control channel information to its corresponding physical channel.

Table 4.2-1

TrCH	Physical Channel
DL-SCH	PDSCH
BCH	PBCH
PCH	PDSCH

Table 4.2-2

Control information	Physical Channel
DCI	PDCCH

5 General procedures

Data and control streams from/to MAC layer are encoded /decoded to offer transport and control services over the radio transmission link. Channel coding scheme is a combination of error detection, error correcting, rate matching, interleaving and transport channel or control information mapping onto/splitting from physical channels.

5.1 CRC calculation

Denote the input bits to the CRC computation by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, and the parity bits by

$p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the size of the input sequence and L is the number of parity bits. The parity bits are generated by one of the following cyclic generator polynomials:

- $g_{\text{CRC24A}}(D) = [D^{24} + D^{23} + D^{18} + D^{17} + D^{14} + D^{11} + D^{10} + D^7 + D^6 + D^5 + D^4 + D^3 + D + 1]$ for a CRC length $L=24$;
- $g_{\text{CRC24B}}(D) = [D^{24} + D^{23} + D^6 + D^5 + D + 1]$ for a CRC length $L=24$;
- $g_{\text{CRC24C}}(D) = [D^{24} + D^{23} + D^{21} + D^{20} + D^{17} + D^{15} + D^{13} + D^{12} + D^8 + D^4 + D^2 + D + 1]$ for a CRC length $L=24$;
- $g_{\text{CRC16}}(D) = [D^{16} + D^{12} + D^5 + 1]$ for a CRC length $L=16$;
- $g_{\text{CRC11}}(D) = [D^{11} + D^{10} + D^9 + D^5 + 1]$ for a CRC length $L=11$;
- $g_{\text{CRC6}}(D) = [D^6 + D^5 + 1]$ for a CRC length $L=6$.

The encoding is performed in a systematic form, which means that in GF(2), the polynomial:

$$a_0 D^{A+L-1} + a_1 D^{A+L-2} + \dots + a_{A-1} D^L + p_0 D^{L-1} + p_1 D^{L-2} + \dots + p_{L-2} D^1 + p_{L-1}$$

yields a remainder equal to 0 when divided by the corresponding CRC generator polynomial.

The bits after CRC attachment are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B=A+L$. The relation between a_k and b_k is:

$$b_k = a_k \quad \text{for } k=0,1,2,\dots,A-1$$

$b_k = p_{k-A}$ for $k=A, A+1, A+2, \dots, A+L-1$.

5.2 Code block segmentation and code block CRC attachment

5.2.1 Polar coding

The input bit sequence to the code block segmentation is denoted by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, where $A > 0$.

if $I_{seg} = 1$

Number of code blocks: $C=2$;

else

Number of code blocks: end if

$A' = \lceil A/C \rceil \cdot C$;

for $i=0$ to $A'-A-1$

$a'_i = 0$;

end for

for $i=A'-A$ to $A'-1$

$a'_i = a_{i-(A'-A)}$;

end for

$s=0$;

for $r=0$ to $C-1$

for $k=0$ to $A'/C-1$

$c_{rk} = a'_s$;

$s=s+1$;

end for

The sequence $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r[A'/C-1]}$ is used to calculate the CRC parity bits $p_{r0}, p_{r1}, p_{r2}, \dots, p_{r[L-1]}$ according to Subclause 5.1 with a generator polynomial of length L .

for $k=A'/C$ to $A'/C+L-1$

$c_{rk} = p_{r[k-A'/C]}$;

end for

end for

The value of A is no larger than 1706.

5.2.2 Low density parity check coding

The input bit sequence to the code block segmentation is denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B > 0$. If B is larger than the maximum code block size K_{cb} , segmentation of the input bit sequence is performed and an additional CRC sequence of $L=24$ bits is attached to each code block.

For LDPC base graph 1, the maximum code block size is:

$$K_{cb} = 8448$$

For LDPC base graph 2, the maximum code block size is:

$$K_{cb} = 3840$$

Total number of code blocks C is determined by:

$$\text{if } B \leq K_{cb}$$

$$L = 0$$

$$\text{Number of code blocks: } C = 1$$

$$B' = B$$

else

$$L = 24$$

$$\text{Number of code blocks: } C = \lceil B / (K_{cb} - L) \rceil$$

$$B' = B + C \cdot L$$

end if

The bits output from code block segmentation are denoted by $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r[K_r-1]}$, where $0 \leq r < C$ is the code block number, and $K_r = K$ is the number of bits for the code block number r .

Number of bits in each code block:

$$K' = B' / C ;$$

For LDPC base graph 1,

$$K_b = 22$$

For LDPC base graph 2,

$$\text{if } B > 640$$

$$K_b = 10 ;$$

$$\text{elseif } B > 560$$

$$K_b = 9 ;$$

$$\text{elseif } B > 192$$

$$K_b = 8 ;$$

else

$K_b = 6$;

end if

find the minimum value of Z_c in all sets of lifting sizes in Table 5.3.2-1, denoted as Z_c , such that $K_b \cdot Z_c \geq K'$, and set $K = 22Z_c$ for LDPC base graph 1 and $K = 10Z_c$ for LDPC base graph 2;

$s = 0$;

for $r = 0$ to $C - 1$

for $k = 0$ to $K' - L - 1$

$c_{rk} = b_s$;

$s = s + 1$;

end for

if $C > 1$

The sequence $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r|K' - L - 1|}$ is used to calculate the CRC parity bits

$p_{r0}, p_{r1}, p_{r2}, \dots, p_{r|L - 1|}$ according to Subclause 5.1 with the generator polynomial $g_{\text{CRC24B}}(D)$.

for $k = K' - L$ to $K' - 1$

$c_{rk} = p_{r|k+L-K'|}$;

end for

end if

for $k = K'$ to $K - 1$ -- Insertion of filler bits

$c_{rk} = <\text{NULL}>$;

end for

end for

5.3 Channel coding

Usage of coding scheme for the different types of TrCH is shown in table 5.3-1. Usage of coding scheme for the different control information types is shown in table 5.3-2.

Table 5.3-1: Usage of channel coding scheme for TrCHs

TrCH	Coding scheme
UL-SCH	LDPC
DL-SCH	
PCH	
BCH	Polar code

Table 5.3-2: Usage of channel coding scheme for control information

Control Information	Coding scheme
DCI	Polar code
UCI	Block code
	Polar code

5.3.1 Polar coding

The bit sequence input for a given code block to channel coding is denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits to encode. After encoding the bits are denoted by $d_0, d_1, d_2, \dots, d_{N-1}$, where $N=2^n$ and the value of n is determined by the following:

Denote by E the rate matching output sequence length as given in Subclause 5.4.1;

If $E \leq (9/8) \cdot 2^{\lceil \log_2 E \rceil - 1}$ and $K/E < 9/16$

$$n_1 = \lceil \log_2 E \rceil - 1 ;$$

else

$$n_1 = \lceil \log_2 E \rceil ;$$

end if

$$R_{\min} = 1/8 ;$$

$$n_2 = \lceil \log_2 (K/R_{\min}) \rceil ;$$

$$n = \max \{ \min \{ n_1, n_2, n_{\max} \}, n_{\min} \}$$

where $n_{\min} = 5$.

5.3.1.1 Interleaving

The bit sequence $c_0, c_1, c_2, c_3, \dots, c_{K-1}$ is interleaved into bit sequence $c'_0, c'_1, c'_2, c'_3, \dots, c'_{K-1}$ as follows:

$$c'_k = c_{\Pi(k)}, \quad k=0,1,\dots,K-1$$

where the interleaving pattern $\Pi(k)$ is given by the following:

if $I_{IL} = 0$

$$\Pi(k) = k, \quad k=0,1,\dots,K-1$$

else

$$k=0 ;$$

for $m=0$ to $K_{IL}^{\max} - 1$

if $\Pi_{IL}^{\max}(m) \geq K_{IL}^{\max} - K$

$$\Pi(k) = \Pi_{IL}^{\max}(m) - (K_{IL}^{\max} - K) ;$$

$$k=k+1 ;$$

end if

end for

end if

where $\Pi_{IL}^{\max}(m)$ is given by Table 5.3.1.1-1 and $K_{IL}^{\max}=164$.

Table 5.3.1.1-1: Interleaving pattern $\Pi_{IL}^{\max}(m)$

m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$	m	$\Pi_{IL}^{\max}(m)$
0	0	28	67	56	122	84	68	11 2	33	14 0	38
1	2	29	69	57	123	85	73	11 3	36	14 1	144
2	4	30	70	58	126	86	78	11 4	44	14 2	39
3	7	31	71	59	127	87	84	11 5	47	14 3	145
4	9	32	72	60	129	88	90	11 6	64	14 4	40
5	14	33	76	61	132	89	92	11 7	74	14 5	146
6	19	34	77	62	134	90	94	11 8	79	14 6	41
7	20	35	81	63	138	91	96	11 9	85	14 7	147
8	24	36	82	64	139	92	99	12 0	97	14 8	148
9	25	37	83	65	140	93	102	12 1	100	14 9	149
10	26	38	87	66	1	94	105	12 2	103	15 0	150
11	28	39	88	67	3	95	107	12 3	117	15 1	151
12	31	40	89	68	5	96	109	12 4	125	15 2	152
13	34	41	91	69	8	97	112	12 5	131	15 3	153
14	42	42	93	70	10	98	114	12 6	136	15 4	154
15	45	43	95	71	15	99	116	12 7	142	15 5	155
16	49	44	98	72	21	10 0	121	12 8	12	15 6	156
17	50	45	101	73	27	10 1	124	12 9	17	15 7	157
18	51	46	104	74	29	10 2	128	13 0	23	15 8	158
19	53	47	106	75	32	10 3	130	13 1	37	15 9	159
20	54	48	108	76	35	10 4	133	13 2	48	16 0	160
21	56	49	110	77	43	10 5	135	13 3	75	16 1	161
22	58	50	111	78	46	10 6	141	13 4	80	16 2	162
23	59	51	113	79	52	10 7	6	13 5	86	16 3	163
24	61	52	115	80	55	10 8	11	13 6	137		
25	62	53	118	81	57	10 9	16	13 7	143		
26	65	54	119	82	60	11 0	22	13 8	13		
27	66	55	120	83	63	11 1	30	13 9	18		

5.3.1.2 Polar encoding

The Polar sequence $Q_0^{N_{\max}-1} = \left[Q_0^{N_{\max}}, Q_1^{N_{\max}}, \dots, Q_{N_{\max}-1}^{N_{\max}} \right]$ is given by Table 5.3.1.2-1, where $0 \leq Q_i^{N_{\max}} \leq N_{\max} - 1$. It denotes a bit index before Polar encoding for $i=0, 1, \dots, N_{\max}-1$ and $N_{\max}=1024$. The Polar sequence $Q_0^{N_{\max}-1}$ is in ascending order of reliability $W(Q_0^{N_{\max}}) < W(Q_1^{N_{\max}}) < \dots < W(Q_{N_{\max}-1}^{N_{\max}})$, where $W(Q_i^{N_{\max}})$ denotes the reliability of bit index $Q_i^{N_{\max}}$.

For any code block encoded to N bits, a same Polar sequence $Q_0^{N-1} = [Q_0^N, Q_1^N, Q_2^N, \dots, Q_{N-1}^N]$ is used. The Polar sequence Q_0^{N-1} is a subset of Polar sequence $Q_0^{N_{\max}-1}$ with all elements $Q_i^{N_{\max}}$ of values less than N , ordered in ascending order of reliability $W(Q_0^N) < W(Q_1^N) < W(Q_2^N) < \dots < W(Q_{N-1}^N)$.

Denote \bar{Q}_I^N as a set of bit indices in Polar sequence Q_0^{N-1} , and \bar{Q}_F^N as the set of other bit indices in Polar sequence Q_0^{N-1} , where \bar{Q}_I^N and \bar{Q}_F^N are given in Subclause 5.4.1.1, $|\bar{Q}_I^N| = K + n_{PC}$, $|\bar{Q}_F^N| = N - |\bar{Q}_I^N|$, and n_{PC} is the number of parity check bits.

Denote $G_N = (G_2)^{\otimes n}$ as the n -th Kronecker power of matrix G_2 , where $G_2 = \begin{bmatrix} 1 & 0 \\ 1 & 1 \end{bmatrix}$.

For a bit index j with $j=0, 1, \dots, N-1$, denote g_j as the j -th row of G_N and $w(g_j)$ as the row weight of g_j , where $w(g_j)$ is the number of ones in g_j . Denote the set of bit indices for parity check bits as Q_{PC}^N , where $|Q_{PC}^N| = n_{PC}$. A number of $(n_{PC} - n_{PC}^{wm})$ parity check bits are placed in the $(n_{PC} - n_{PC}^{wm})$ least reliable bit indices in \bar{Q}_I^N . A number of n_{PC}^{wm} other parity check bits are placed in the bit indices of minimum row weight in \tilde{Q}_I^N , where \tilde{Q}_I^N denotes the $(|\bar{Q}_I^N| - n_{PC})$ most reliable bit indices in \bar{Q}_I^N ; if there are more than n_{PC}^{wm} bit indices of the same minimum row weight in \tilde{Q}_I^N , the n_{PC}^{wm} other parity check bits are placed in the n_{PC}^{wm} bit indices of the highest reliability and the minimum row weight in \tilde{Q}_I^N .

Generate $u = [u_0 \ u_1 \ u_2 \ \dots \ u_{N-1}]$ according to the following:

$k=0$;

if $n_{PC} > 0$

$y_0=0$; $y_1=0$; $y_2=0$; $y_3=0$; $y_4=0$;

for $n=0$ to $N-1$

$y_t=y_0$; $y_0=y_1$; $y_1=y_2$; $y_2=y_3$; $y_3=y_4$; $y_4=y_t$;

if $n \in \bar{Q}_I^N$

if $n \in Q_{PC}^N$

$u_n=y_0$;

else

$u_n=c_k'$;

$k=k+1$;

$y_0=y_0 \oplus u_n$;

end if

else

$u_n=0$;

```
end if  
end for  
else  
for n=0 to N-1  
if n∈QIN  
    un=ck' ;  
    k=k+1 ;  
else  
    un=0 ;  
end if  
end for  
end if
```

The output after encoding $d = [d_0 d_1 d_2 \dots d_{N-1}]$ is obtained by $d = \mathbf{u} \mathbf{G}_N$. The encoding is performed in GF(2).


```

if    $c_k \neq \text{NULL}$  ;
     $d_{k-2Z_c} = c_k$  ;
else
     $c_k = 0$  ;
     $d_{k-2Z_c} = \text{NULL}$  ;
end if

```

end for

- 3) Generate $N+2Z_c-K$ parity bits $w = [w_0, w_1, w_2, \dots, w_{N+2Z_c-K-1}]^T$ such that $H \times \begin{bmatrix} c \\ w \end{bmatrix} = 0$, where $c = [c_0, c_1, c_2, \dots, c_{K-1}]^T$; $\mathbf{0}$ is a column vector of all elements equal to 0. The encoding is performed in GF(2).

For LDPC base graph 1, a matrix of H_{BG} has 46 rows with row indices $i=0,1,2,\dots,45$ and 68 columns with column indices $j=0,1,2,\dots,67$. For LDPC base graph 2, a matrix of H_{BG} has 42 rows with row indices $i=0,1,2,\dots,41$ and 52 columns with column indices $j=0,1,2,\dots,51$. The elements in H_{BG} with row and column indices given in Table 5.3.2-2 (for LDPC base graph 1) and Table 5.3.2-3 (for LDPC base graph 2) are of value 1, and all other elements in H_{BG} are of value 0.

The matrix H is obtained by replacing each element of H_{BG} with a $Z_c \times Z_c$ matrix, according to the following:

- Each element of value 0 in H_{BG} is replaced by an all zero matrix $\mathbf{0}$ of size $Z_c \times Z_c$;
- Each element of value 1 in H_{BG} is replaced by a circular permutation matrix $I(P_{i,j})$ of size $Z_c \times Z_c$, where i and j are the row and column indices of the element, and $I(P_{i,j})$ is obtained by circularly shifting the identity matrix I of size $Z_c \times Z_c$ to the right $P_{i,j}$ times. The value of $P_{i,j}$ is given by $P_{i,j} = \text{mod}(V_{i,j}, Z_c)$. The value of $V_{i,j}$ is given by Tables 5.3.2-2 and 5.3.2-3 according to the set index i_{LS} and LDPC base graph.

- 4) for $k=K$ to $N+2Z_c-1$

```
 $d_{k-2Z_c} = w_{k-K}$  ;
```

end for

Table 5.3.2-1: Sets of LDPC lifting size Z

<i>Set index (i_{LS})</i>	<i>Set of lifting sizes (Z)</i>
0	{2, 4, 8, 16, 32, 64, 128, 256}
1	{3, 6, 12, 24, 48, 96, 192, 384}
2	{5, 10, 20, 40, 80, 160, 320}
3	{7, 14, 28, 56, 112, 224}
4	{9, 18, 36, 72, 144, 288}
5	{11, 22, 44, 88, 176, 352}
6	{13, 26, 52, 104, 208}
7	{15, 30, 60, 120, 240}

	21	78	18 8	17 7	32	27 3	16 6	10 4	15 2		17	14 9	31 2	19 7	96	2	13 5	12	30
	22	25 2	33 4	43	84	39	33 8	10 9	16 5		62	0	0	0	0	0	0	0	0
	23	22	11 5	28 0	20 1	26	19 2	12 4	10 7		1	16 7	52	15 4	23	0	12 3	2	53
	33	0	0	0	0	0	0	0	0		3	17 3	31 4	47	21 5	0	77	75	18 9
12	0	16 0	77	22 9	14 2	22 5	12 3	6	18 6	41	9	13 9	13 9	12 4	60	0	25	14 2	21 5
	1	42	18 6	23 5	17 5	16 2	21 7	20	21 5		18	15 1	28 8	20 7	16 7	18 3	27 2	12 8	24
	10	21	17 4	16 9	13 6	24 4	14 2	20 3	12 4		63	0	0	0	0	0	0	0	0
	11	32	23 2	48	3	15 1	11 0	15 3	18 0		0	14 9	11 3	22 6	11 4	27	28 8	16 3	22 2
	13	23 4	50	10 5	28	23 8	17 6	10 4	98		4	15 7	14	65	91	0	83	10	17 0
	18	7	74	52	18 2	24 3	76	20 7	80		24	13 7	21 8	12 6	78	35	17	16 2	71
	34	0	0	0	0	0	0	0	0		64	0	0	0	0	0	0	0	0
13	0	17 7	31 3	39	81	23 1	31 1	52	22 0	42	1	15 1	11 3	22 8	20 6	52	21 0	1	22
	3	24 8	17 7	30 2	56	0	25 1	14 7	18 5		16	16 3	13 2	69	22	24 3	3	16 3	12 7
	7	15 1	26 6	30 3	72	21 6	26 5	1	15 4		18	17 3	11 4	17 6	13 4	0	53	99	49
	20	18 5	11 5	16 0	21 7	47	94	16	17 8		25	13 9	16 8	10 2	16 1	27	16 7	98	12 5
	23	62	37 0	37	78	36	81	46	15 0		65	0	0	0	0	0	0	0	0
	35	0	0	0	0	0	0	0	0		0	13 9	80	23 4	84	18	79	4	19 1
14	0	20 6	14 2	78	14	0	22	1	12 4	44	7	15 7	78	22 7	4	0	24 4	6	21 1
	12	55	24 8	29 9	17 5	18 6	32	20 2	14 4		9	16 3	16 3	25 9	9	0	29 3	14 2	18 7
	15	20 6	13 7	54	21	25 3	27	11 8	18 2		22	17 3	27 4	26 0	12	57	27 2	3	14 8
	16	12 7	89	61	19 1	16	15 6	13 0	95		66	0	0	0	0	0	0	0	0
	17	16	34 7	17 9	51	0	66	1	72		1	14 9	13 5	10 1	18 4	16 8	82	18 1	17 7
	21	22 9	12	25 8	43	79	78	2	76		6	15 1	14 9	22 8	12 1	0	67	45	11 4
	36	0	0	0	0	0	0	0	0		10	16 7	15	12 6	29	14 4	23 5	15 3	93
15	0	40	24 1	22 9	90	17 0	17 6	17 3	39	45	67	0	0	0	0	0	0	0	0

	13	23 2	16 3	27	11 6	97	16 6	10 9	16 2		49	0	0	0	0	0	0	0	
	24	0	0	0	0	0	0	0	0		2	0	10 3	0	98	6	16 0	19 3	
15	0	51	68	0	11 6	13 9	13 7	17 4	38	40	10	75	10 7	36	35	73	15 6	16 3	
	10	17 5	63	73	20 0	96	10 3	10 8	21 7		13	12 0	16 3	14 3	36	10 2	82	17 9	18 0
	11	21 3	81	99	11 0	12 8	40	10 2	15 7		50	0	0	0	0	0	0	0	
	25	0	0	0	0	0	0	0	0		1	12 9	14 7	0	12 0	48	13 2	19 1	53
16	1	20 3	87	0	75	48	78	12 5	17 0	41	5	22 9	7	2	10 1	47	6	19 7	21 5
	9	14 2	17 7	79	15 8	9	15 8	31	23		11	11 8	60	55	81	19	8	16 7	23 0
	11	8	13 5	11 1	13 4	28	17	54	17 5		51	0	0	0	0	0	0	0	0
	12	24 2	64	14 3	97	8	16 5	17 6	20 2										

5.3.3 Channel coding of small block lengths

The bit sequence input for a given code block to channel coding is denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits to encode. After encoding the bits are denoted by $d_0, d_1, d_2, \dots, d_{N-1}$.

5.3.3.1 Encoding of 1-bit information

For $K=1$, the code block is encoded according to Table 5.3.3.1-1, where $N=Q_m$ and Q_m is the modulation order for the code block.

Table 5.3.3.1-1: Encoding of 1-bit information

Q_m	Encoded bits	$d_0, d_1, d_2, \dots, d_{N-1}$
1		$[c_0]$
2		$[c_0 \ y]$
4		$[c_0 \ y \ x \ x]$
6		$[c_0 \ y \ x \ x \ x \ x]$
8		$[c_0 \ y \ x \ x \ x \ x \ x \ x]$

The "x" and "y" in Table 5.3.3.1-1 are placeholders for Subclause 6.3.1.1 of [4, TS 38.211] to scramble the information bits in a way that maximizes the Euclidean distance of the modulation symbols carrying the information bits.

5.3.3.2 Encoding of 2-bit information

For $K=2$, the code block is encoded according to Table 5.3.3.2-1, where $c_2 = (c_0 + c_1) \bmod 2$, $N=3Q_m$, and Q_m is the modulation order for the code block.

Table 5.3.3.2-1: Encoding of 2-bit information

Q_m	Encoded bits	$d_0, d_1, d_2, \dots, d_{N-1}$
1		$[c_0 \ c_1 \ c_2]$
2		$[c_0 \ c_1 \ c_2 \ c_0 \ c_1 \ c_2]$
4		$[c_0 \ c_1 \ x \ x \ c_2 \ c_0 \ x \ x \ c_1 \ c_2 \ x \ x]$
6		$[c_0 \ c_1 \ x \ x \ x \ x \ c_2 \ c_0 \ x \ x \ x \ x \ c_1 \ c_2 \ x \ x \ x \ x]$
8		$[c_0 \ c_1 \ x \ x \ x \ x \ x \ x \ c_2 \ c_0 \ x \ x \ x \ x \ x \ x \ c_1 \ c_2 \ x \ x \ x \ x \ x]$

The "x" in Table 5.3.3.2-1 are placeholders for Subclause 6.3.1.1 of [4, TS 38.211] to scramble the information bits in a way that maximizes the Euclidean distance of the modulation symbols carrying the information bits.

5.3.3.3 Encoding of other small block lengths

For $3 \leq K \leq 11$, the code block is encoded by $d_i = \left(\sum_{k=0}^{K-1} c_k \cdot M_{i,k} \right) \bmod 2$, where $i=0,1,\dots,N-1$, $N=32$, and $M_{i,k}$ represents the basis sequences as defined in Table 5.3.3.3-1.

Table 5.3.3.3-1: Basis sequences for (32, K) code

i	$M_{i,0}$	$M_{i,1}$	$M_{i,2}$	$M_{i,3}$	$M_{i,4}$	$M_{i,5}$	$M_{i,6}$	$M_{i,7}$	$M_{i,8}$	$M_{i,9}$	$M_{i,10}$
0	1	1	0	0	0	0	0	0	0	0	1
1	1	1	1	0	0	0	0	0	0	1	1
2	1	0	0	1	0	0	1	0	1	1	1
3	1	0	1	1	0	0	0	0	1	0	1
4	1	1	1	1	0	0	0	1	0	0	1
5	1	1	0	0	1	0	1	1	1	0	1
6	1	0	1	0	1	0	1	0	1	1	1
7	1	0	0	1	1	0	0	1	1	0	1
8	1	1	0	1	1	0	0	1	0	1	1
9	1	0	1	1	1	0	1	0	0	1	1
10	1	0	1	0	0	1	1	1	0	1	1
11	1	1	1	0	0	1	1	0	1	0	1
12	1	0	0	1	0	1	0	1	1	1	1
13	1	1	0	1	0	1	0	1	0	1	1
14	1	0	0	0	1	1	0	1	0	0	1
15	1	1	0	0	1	1	1	1	0	1	1
16	1	1	1	0	1	1	1	0	0	1	0
17	1	1	0	1	1	1	0	0	1	0	0
18	1	1	0	1	1	1	1	1	0	0	0
19	1	0	0	0	0	1	1	0	0	0	0
20	1	0	1	0	0	0	1	0	0	0	1
21	1	1	0	1	0	0	0	0	0	1	1
22	1	0	0	0	1	0	0	1	1	0	1
23	1	1	1	0	1	0	0	0	1	1	1
24	1	1	1	1	1	0	1	1	1	1	0
25	1	1	0	0	0	1	1	1	0	0	1
26	1	0	1	1	0	1	0	0	1	1	0
27	1	1	1	1	0	1	0	1	1	1	0
28	1	0	1	0	1	1	1	0	1	0	0
29	1	0	1	1	1	1	1	1	1	0	0
30	1	1	1	1	1	1	1	1	1	1	1
31	1	0	0	0	0	0	0	0	0	0	0

5.4 Rate matching

5.4.1 Rate matching for Polar code

The rate matching for Polar code is defined per coded block and consists of sub-block interleaving, bit collection, and bit interleaving. The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$. The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

5.4.1.1 Sub-block interleaving

The bits input to the sub-block interleaver are the coded bits $d_0, d_1, d_2, \dots, d_{N-1}$. The coded bits $d_0, d_1, d_2, \dots, d_{N-1}$ are divided into 32 sub-blocks. The bits output from the sub-block interleaver are denoted as $y_0, y_1, y_2, \dots, y_{N-1}$, generated as follows:

for $n=0$ to $N-1$

$i = \lfloor 32n/N \rfloor$;

$J(n) = P(i) \times (N/32) + \text{mod}(n, N/32)$;

$y_n = d_{J(n)}$;

end for

where the sub-block interleaver pattern $P[i]$ is given by Table 5.4.1.1-1.

Table 5.4.1.1-1: Sub-block interleaver pattern $P[i]$

i	$P[i]$														
0	0	4	3	8	8	12	10	16	12	20	14	24	24	28	27
1	1	5	5	9	16	13	18	17	20	21	22	25	25	29	29
2	2	6	6	10	9	14	11	18	13	22	15	26	26	30	30
3	4	7	7	11	17	15	19	19	21	23	23	27	28	31	31

The sets of bit indices \bar{Q}_I^N and \bar{Q}_F^N are determined as follows, where K , n_{PC} , and Q_0^{N-1} are defined in Subclause 5.3.1

$\bar{Q}_{F,\text{tmp}}^N = \emptyset$

if $E < N$

if $K/E \leq 7/16$ -- puncturing

for $n=0$ to $N-E-1$

$\bar{Q}_{F,\text{tmp}}^N = \bar{Q}_{F,\text{tmp}}^N \cup [J(n)]$;

end for

if $E \geq 3N/4$

$\bar{Q}_{F,\text{tmp}}^N = \bar{Q}_{F,\text{tmp}}^N \cup [0, 1, \dots, \lceil 3N/4 - E/2 \rceil - 1]$;

else

$\bar{Q}_{F,\text{tmp}}^N = \bar{Q}_{F,\text{tmp}}^N \cup [0, 1, \dots, \lceil 9N/16 - E/4 \rceil - 1]$;

end if

```

else -- shortening
for n=E to N-1
     $\bar{Q}_{F,tmp}^N = \bar{Q}_{F,tmp}^N \cup [J(n)]$  ;
end for
end if
end if

 $\bar{Q}_{I,tmp}^N = Q_0^{N-1}[\bar{Q}_{F,tmp}^N]$  ;
 $\bar{Q}_I^N$  comprises  $(K+n_{PC})$  most reliable bit indices in  $\bar{Q}_{I,tmp}^N$  ;
 $\bar{Q}_F^N = Q_0^{N-1}[\bar{Q}_I^N]$  ;

```

5.4.1.2 Bit selection

The bit sequence after the sub-block interleaver $y_0, y_1, y_2, \dots, y_{N-1}$ from Subclause 5.4.1.1 is written into a circular buffer of length N .

Denoting by E the rate matching output sequence length, the bit selection output bit sequence e_k , $k=0,1,2,\dots,E-1$, is generated as follows:

```

if E ≥ N -- repetition
    for k=0 to E-1
         $e_k = y_{\text{mod}(k,N)}$  ;
    end for
else
    if K/E ≤ 7/16 -- puncturing
        for k=0 to E-1
             $e_k = y_{k+N-E}$  ;
        end for
    else -- shortening
        for k=0 to E-1
             $e_k = y_k$  ;
        end for
    end if
end if

```

5.4.1.3 Interleaving of coded bits

The bit sequence $e_0, e_1, e_2, \dots, e_{E-1}$ is interleaved into bit sequence $f_0, f_1, f_2, \dots, f_{E-1}$, as follows:

If $I_{BL}=1$

Denote T as the smallest integer such that $T(T+1)/2 \geq E$;

$k=0$;

for $i=0$ to $T-1$

for $j=0$ to $T-1-i$

if $k < E$

$v_{i,j}=e_k$;

else

$v_{i,j}=<\text{NULL}>$;

end if

$k=k+1$;

end for

end for

$k=0$;

for $j=0$ to $T-1$

for $i=0$ to $T-1-j$

if $v_{i,j} \neq <\text{NULL}>$

$f_k=v_{i,j}$;

$k=k+1$

end if

end for

end for

else

for $i=0$ to $E-1$

$f_i=e_i$;

end for

end if

The value of E is no larger than 8192.

5.4.2 Rate matching for LDPC code

The rate matching for LDPC code is defined per coded block and consists of bit selection and bit interleaving. The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$. The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

5.4.2.1 Bit selection

The bit sequence after encoding $d_0, d_1, d_2, \dots, d_{N-1}$ from Subclause 5.3.2 is written into a circular buffer of length N_{cb} for the r -th coded block, where N is defined in Subclause 5.3.2.

For the r -th code block, let $N_{cb} = N$ if $I_{LBRM} = 0$ and $N_{cb} = \min(N, N_{ref})$ otherwise, where

$N_{ref} = \left\lceil \frac{TBS_{LBRM}}{C \cdot R_{LBRM}} \right\rceil$, $R_{LBRM} = 2/3$, TBS_{LBRM} is determined according to Subclause 6.1.4.2 in [6, TS 38.214] for UL-SCH and Subclause 5.1.3.2 in [6, TS 38.214] for DL-SCH/PCH, assuming the following:

- maximum number of layers for one TB supported by the UE for the serving cell, if the UE has reported its corresponding capability; otherwise a maximum of 2 layers is assumed for DL-SCH;
- maximum modulation order configured for the serving cell, if configured by higher layers; otherwise a maximum modulation order $Q_m = 6$ is assumed for DL-SCH;
- maximum coding rate of 948/1024;
- $n_{PRB} = n_{PRB,LBRM}$ is given by Table 5.4.2.1-1, where the value of $n_{PRB,LBRM}$ for DL-SCH is determined according to the initial bandwidth part if there is no other bandwidth part configured to the UE;
- $N_{RE} = 156 \diamond_{PRB}$;
- C is the number of code blocks of the transport block determined according to Subclause 5.2.2.

Table 5.4.2.1-1: Value of $n_{PRB,LBRM}$

Maximum number of PRBs across all configured BWPs of a carrier	$n_{PRB,LBRM}$
Less than 33	32
33 to 66	66
67 to 107	107
108 to 135	135
136 to 162	162
163 to 217	217
Larger than 217	273

Denoting by E_r the rate matching output sequence length for the r -th coded block, where the value of E_r is determined as follows:

Set $j=0$

for $r=0$ to $C-1$

if the r -th coded block is not scheduled for transmission as indicated by CBGTI according to Subclause 5.1.7.2 for DL-SCH and 6.1.5.2 for UL-SCH in [6, TS 38.214]

$E_r=0$;

```

else
  if    $j \leq C' - \text{mod}(G/(N_L \cdot Q_m), C') - 1$ 
     $E_r = N_L \cdot Q_m \cdot \left\lfloor \frac{G}{N_L \cdot Q_m \cdot C'} \right\rfloor ;$ 
  else
     $E_r = N_L \cdot Q_m \cdot \lceil \frac{G}{N_L \cdot Q_m \cdot C'} \rceil ;$ 
  end if
   $j = j + 1 ;$ 
end if
end for

```

where

- N_L is the number of transmission layers that the transport block is mapped onto;
- Q_m is the modulation order;
- G is the total number of coded bits available for transmission of the transport block;
- $C' = C$ if CBGTI is not present in the DCI scheduling the transport block and C' is the number of scheduled code blocks of the transport block if CBGTI is present in the DCI scheduling the transport block.

Denote by rv_{id} the redundancy version number for this transmission ($rv_{id} = 0, 1, 2$ or 3), the rate matching output bit sequence e_k , $k=0,1,2,\dots,E-1$, is generated as follows, where k_0 is given by Table 5.4.2.1-2 according to the value of rv_{id} and LDPC base graph:

```

 $k = 0 ;$ 
 $j = 0 ;$ 
while  $k < E$ 
  if  $d_{(k_0+j) \bmod N_{cb}} \neq \text{NULL} > \text{NULL}$ 
     $e_k = d_{(k_0+j) \bmod N_{cb}} ;$ 
     $k = k + 1 ;$ 
  end if
   $j = j + 1 ;$ 
end while

```

Table 5.4.2.1-2: Starting position of different redundancy versions, k_0

rv_{id}	k_0	
	LDPC base graph 1	LDPC base graph 2
0	0	0
1	$\left\lfloor \frac{17 N_{cb}}{66 Z_c} \right\rfloor Z_c$	$\left\lfloor \frac{13 N_{cb}}{50 Z_c} \right\rfloor Z_c$
2	$\left\lfloor \frac{33 N_{cb}}{66 Z_c} \right\rfloor Z_c$	$\left\lfloor \frac{25 N_{cb}}{50 Z_c} \right\rfloor Z_c$
3	$\left\lfloor \frac{56 N_{cb}}{66 Z_c} \right\rfloor Z_c$	$\left\lfloor \frac{43 N_{cb}}{50 Z_c} \right\rfloor Z_c$

5.4.2.2 Bit interleaving

The bit sequence $e_0, e_1, e_2, \dots, e_{E-1}$ is interleaved to bit sequence $f_0, f_1, f_2, \dots, f_{E-1}$, according to the following, where the value of Q_m is the modulation order.

for $j=0$ to E/Q_m-1

for $i=0$ to Q_m-1

$$f_{i+j \cdot Q_m} = e_{i \cdot E/Q_m + j} ;$$

end for

end for

5.4.3 Rate matching for channel coding of small block lengths

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$. The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$, where E is the rate matching output sequence length. The bit sequence $f_0, f_1, f_2, \dots, f_{E-1}$ is obtained by the following:

for $k=0$ to $E-1$

$$f_k = d_{k \bmod N} ;$$

end for

5.5 Code block concatenation

The input bit sequence for the code block concatenation block are the sequences f_{rk} , for $r=0, \dots, C-1$ and $k=0, \dots, E_r-1$, where E_r is the number of rate matched bits for the r -th code block. The output bit sequence from the code block concatenation block is the sequence g_k for $k=0, \dots, G-1$.

The code block concatenation consists of sequentially concatenating the rate matching outputs for the different code blocks. Therefore,

Set $k=0$ and $r=0$

```

while r < C
  Set j = 0
  while j < E_r
    g_k = f_{rj}
    k = k + 1
    j = j + 1
  end while
  r = r + 1
end while

```

6 Uplink transport channels and control information

6.1 Random access channel

The sequence index for the random access channel is received from higher layers and is processed according to [4, TS 38.211].

6.2 Uplink shared channel

6.2.1 Transport block CRC attachment

Error detection is provided on each UL-SCH transport block through a Cyclic Redundancy Check (CRC).

The entire transport block is used to calculate the CRC parity bits. Denote the bits in a transport block delivered to layer 1 by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, and the parity bits by $p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the payload size and L is the number of parity bits. The lowest order information bit a_0 is mapped to the most significant bit of the transport block as defined in Subclause x.x of [TS38.321].

The parity bits are computed and attached to the UL-SCH transport block according to Subclause 5.1, by setting L to 24 bits and using the generator polynomial $g_{\text{CRC24A}}(D)$ if $A > 3824$; and by setting L to 16 bits and using the generator polynomial $g_{\text{CRC16}}(D)$ otherwise.

The bits after CRC attachment are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B = A + L$.

6.2.2 LDPC base graph selection

For initial transmission of a transport block with coding rate R indicated by the MCS index according to Subclause 6.1.4.1 in [6, TS 38.214] and subsequent re-transmission of the same transport block, each code block of the transport block is encoded with either LDPC base graph 1 or 2 according to the following:

- if $A \leq 292$, or if $A \leq 3824$ and $R \leq 0.67$, or if $R \leq 0.25$, LDPC base graph 2 is used;
- otherwise, LDPC base graph 1 is used,

where A is the payload size as described in Subclause 6.2.1.

6.2.3 Code block segmentation and code block CRC attachment

The bits input to the code block segmentation are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$ where B is the number of bits in the transport block (including CRC).

Code block segmentation and code block CRC attachment are performed according to Subclause 5.2.2.

The bits after code block segmentation are denoted by $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r|K_r-1|}$, where r is the code block number and K_r is the number of bits for code block number r according to Subclause 5.2.2.

6.2.4 Channel coding of UL-SCH

Code blocks are delivered to the channel coding block. The bits in a code block are denoted by

$c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r|K_r-1|}$, where r is the code block number, and K_r is the number of bits in code block number r . The total number of code blocks is denoted by C and each code block is individually LDPC encoded according to Subclause 5.3.2.

After encoding the bits are denoted by $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r|N_r-1|}$, where the values of N_r is given in Subclause 5.3.2.

6.2.5 Rate matching

Coded bits for each code block, denoted as $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r|N_r-1|}$, are delivered to the rate match block, where r is the code block number, and N_r is the number of encoded bits in code block number r . The total number of code blocks is denoted by C and each code block is individually rate matched according to Subclause 5.4.2 by setting $I_{LBRM}=1$ if higher layer parameter $LBRM-FBRM-selection = LBRM$ and by setting $I_{LBRM}=0$ if higher layer parameter $LBRM-FBRM-selection = FBRM$ or if higher layer parameter $LBRM-FBRM-selection$ is not configured.

After rate matching, the bits are denoted by $f_{r0}, f_{r1}, f_{r2}, f_{r3}, \dots, f_{r|E_r-1|}$, where E_r is the number of rate matched bits for code block number r .

6.2.6 Code block concatenation

The input bit sequence for the code block concatenation block are the sequences $f_{r0}, f_{r1}, f_{r2}, f_{r3}, \dots, f_{r|E_r-1|}$, for $r=0, \dots, C-1$ and where E_r is the number of rate matched bits for the r -th code block.

Code block concatenation is performed according to Subclause 5.5.

The bits after code block concatenation are denoted by $g_0, g_1, g_2, g_3, \dots, g_{G-1}$, where G is the total number of coded bits for transmission.

6.2.7 Data and control multiplexing

Denote the coded bits for UL-SCH as $g_0^{\text{UL-SCH}}, g_1^{\text{UL-SCH}}, g_2^{\text{UL-SCH}}, g_3^{\text{UL-SCH}}, \dots, g_{G^{\text{UL-SCH}}-1}^{\text{UL-SCH}}$.

Denote the coded bits for HARQ-ACK, if any, as $g_0^{\text{ACK}}, g_1^{\text{ACK}}, g_2^{\text{ACK}}, g_3^{\text{ACK}}, \dots, g_{G^{\text{ACK}}-1}^{\text{ACK}}$.

Denote the coded bits for CSI part 1, if any, as $g_0^{\text{CSI-part1}}, g_1^{\text{CSI-part1}}, g_2^{\text{CSI-part1}}, g_3^{\text{CSI-part1}}, \dots, g_{G^{\text{CSI-part1}}-1}^{\text{CSI-part1}}$.

Denote the coded bits for CSI part 2, if any, as $g_0^{\text{CSI-part2}}, g_1^{\text{CSI-part2}}, g_2^{\text{CSI-part2}}, g_3^{\text{CSI-part2}}, \dots, g_{G^{\text{CSI-part2}}-1}^{\text{CSI-part2}}$.

Denote the multiplexed data and control coded bit sequence as $g_0, g_1, g_2, g_3, \dots, g_{G-1}$.

Denote l as the OFDM symbol index of the scheduled PUSCH, starting from 0 to $N_{\text{symb,all}}^{\text{PUSCH}} - 1$, where $N_{\text{symb,all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS.

Denote k as the subcarrier index of the scheduled PUSCH, starting from 0 to $M_{\text{sc}}^{\text{PUSCH}} - 1$, where $M_{\text{sc}}^{\text{PUSCH}}$ is expressed as a number of subcarriers.

Denote $\Phi_l^{\text{UL-SCH}}$ as the set of resource elements, in ascending order of indices k , available for transmission of data in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}} - 1$.

Denote $M_{\text{sc}}^{\text{UL-SCH}}(l) = |\Phi_l^{\text{UL-SCH}}|$ as the number of elements in set $\Phi_l^{\text{UL-SCH}}$. Denote $\Phi_l^{\text{UL-SCH}}(j)$ as the j -th element in $\Phi_l^{\text{UL-SCH}}$.

Denote Φ_l^{UCI} as the set of resource elements, in ascending order of indices k , available for transmission of UCI in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}} - 1$. Denote $M_{\text{sc}}^{\text{UCI}}(l) = |\Phi_l^{\text{UCI}}|$ as the number of elements in set Φ_l^{UCI} . Denote $\Phi_l^{\text{UCI}}(j)$ as the j -th element in Φ_l^{UCI} . For any OFDM symbol that carries DMRS of the PUSCH, $\Phi_l^{\text{UCI}} = \emptyset$. For any OFDM symbol that does not carry DMRS of the PUSCH, $\Phi_l^{\text{UCI}} = \Phi_l^{\text{UL-SCH}}$.

If frequency hopping is configured for the PUSCH,

- denote $l^{(1)}$ as the OFDM symbol index of the first OFDM symbol after the first set of consecutive OFDM symbol(s) carrying DMRS in the first hop;
- denote $l^{(2)}$ as the OFDM symbol index of the first OFDM symbol after the first set of consecutive OFDM symbol(s) carrying DMRS in the second hop;
- denote $l_{\text{CSI}}^{(1)}$ as the OFDM symbol index of the first OFDM symbol that does not carry DMRS in the first hop;
- denote $l_{\text{CSI}}^{(2)}$ as the OFDM symbol index of the first OFDM symbol that does not carry DMRS in the second hop;
- if HARQ-ACK is present for transmission on the PUSCH with UL-SCH, let

$$G^{\text{ACK}}(1) = N_L \cdot Q_m \cdot \left\lfloor G^{\text{ACK}} / (2 \cdot N_L \cdot Q_m) \right\rfloor \quad \text{and} \quad G^{\text{ACK}}(2) = N_L \cdot Q_m \cdot \left\lceil G^{\text{ACK}} / (2 \cdot N_L \cdot Q_m) \right\rceil ;$$

- if CSI is present for transmission on the PUSCH with UL-SCH, let

$$G^{\text{CSI-part1}}(1) = N_L \cdot Q_m \cdot \left\lfloor G^{\text{CSI-part1}} / (2 \cdot N_L \cdot Q_m) \right\rfloor ;$$

$$G^{\text{CSI-part1}}(2) = N_L \cdot Q_m \cdot \left\lceil G^{\text{CSI-part1}} / (2 \cdot N_L \cdot Q_m) \right\rceil ;$$

$$G^{\text{CSI-part2}}(1) = N_L \cdot Q_m \cdot \left\lfloor G^{\text{CSI-part2}} / (2 \cdot N_L \cdot Q_m) \right\rfloor ; \text{ and}$$

$$G^{\text{CSI-part2}}(2) = N_L \cdot Q_m \cdot \left\lceil G^{\text{CSI-part2}} / (2 \cdot N_L \cdot Q_m) \right\rceil ;$$

- if only HARQ-ACK and CSI part 1 are present for transmission on the PUSCH without UL-SCH, let

$$G^{\text{ACK}}(1) = \min \left(N_L \cdot Q_m \cdot \left\lfloor G^{\text{ACK}} / (2 \cdot N_L \cdot Q_m) \right\rfloor, M_1 \cdot N_L \cdot Q_m \right) ;$$

$$G^{\text{ACK}}(2) = G^{\text{ACK}} - G^{\text{ACK}}(1) ;$$

- $G^{\text{CSI-part1}}(1) = M_1 \cdot N_L \cdot Q_m - G^{\text{ACK}}(1)$; and
- $G^{\text{CSI-part1}}(2) = G^{\text{CSI-part1}} - G^{\text{CSI-part1}}(1)$;
- if HARQ-ACK, CSI part 1 and CSI part 2 are present for transmission on the PUSCH without UL-SCH, let
 - $G^{\text{ACK}}(1) = \min\left(N_L \cdot Q_m \cdot \lfloor G^{\text{ACK}} / (2 \cdot N_L \cdot Q_m) \rfloor, M_1 \cdot N_L \cdot Q_m\right)$;
 - $G^{\text{ACK}}(2) = G^{\text{ACK}} - G^{\text{ACK}}(1)$;
 - $G^{\text{CSI-part1}}(1) = \min\left(N_L \cdot Q_m \cdot \lfloor G^{\text{CSI-part1}} / (2 \cdot N_L \cdot Q_m) \rfloor, M_1 \cdot N_L \cdot Q_m - G^{\text{ACK}}(1)\right)$;
 - $G^{\text{CSI-part1}}(2) = G^{\text{CSI-part1}} - G^{\text{CSI-part1}}(1)$;
 - $G^{\text{CSI-part2}}(1) = M_1 \cdot N_L \cdot Q_m - G^{\text{CSI-part1}}(1)$ if the number of HARQ-ACK information bits is no more than 2, and
 $G^{\text{CSI-part2}}(1) = M_1 \cdot N_L \cdot Q_m - G^{\text{ACK}}(1) - G^{\text{CSI-part1}}(1)$ otherwise; and
 - $G^{\text{CSI-part2}}(2) = M_2 \cdot N_L \cdot Q_m - G^{\text{CSI-part1}}(2)$ if the number of HARQ-ACK information bits is no more than 2, and
 $G^{\text{CSI-part2}}(2) = M_2 \cdot N_L \cdot Q_m - G^{\text{ACK}}(2) - G^{\text{CSI-part1}}(2)$ otherwise;
- let $N_{\text{hop}}^{\text{PUSCH}} = 2$, and denote $N_{\text{symb,hop}}^{\text{PUSCH}}(1)$, $N_{\text{symb,hop}}^{\text{PUSCH}}(2)$ as the number of OFDM symbols of the PUSCH in the first and second hop, respectively;
- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH;

- $M_1 = \sum_{l=0}^{N_{\text{symb,hop}}^{\text{PUSCH}}(1)-1} M_{\text{SC}}^{\text{UCI}}[l]$;
- $M_2 = \sum_{l=N_{\text{symb,hop}}^{\text{PUSCH}}(1)}^{N_{\text{symb,hop}}^{\text{PUSCH}}(1)+N_{\text{symb,hop}}^{\text{PUSCH}}(2)-1} M_{\text{SC}}^{\text{UCI}}[l]$.

If frequency hopping is not configured for the PUSCH,

- denote $\lceil l^{(1)} \rceil$ as the OFDM symbol index of the first OFDM symbol after the first set of consecutive OFDM symbol(s) carrying DMRS;
- denote $l_{\text{CSI}}^{(1)}$ as the OFDM symbol index of the first OFDM symbol that does not carry DMRS;
- if HARQ-ACK is present for transmission on the PUSCH, let $G^{\text{ACK}}(1) = G^{\text{ACK}}$;
- if CSI is present for transmission on the PUSCH, let $G^{\text{CSI-part1}}(1) = G^{\text{CSI-part1}}$ and $G^{\text{CSI-part2}}(1) = G^{\text{CSI-part2}}$;
- let $N_{\text{hop}}^{\text{PUSCH}} = 1$ and $N_{\text{symb,hop}}^{\text{PUSCH}}(1) = N_{\text{symb,all}}$.

The multiplexed data and control coded bit sequence $g_0, g_1, g_2, g_3, \dots, g_{G-1}$ is obtained according to the following:

Step 1:

Set $\Phi_l^{\text{UL-SCH}} = \Phi_l^{\text{UL-SCH}}$ for $l=0, 1, 2, \dots, N_{\text{symb,all}}^{\text{PUSCH}}-1$;

Set $\bar{M}_{sc}^{UL-SCH}(l) = |\Phi_l^{UL-SCH}|$ for $l=0,1,2,\dots,N_{symb,all}^{PUSCH}-1$;

Set $\bar{\Phi}_l^{UCI} = \Phi_l^{UCI}$ for $l=0,1,2,\dots,N_{symb,all}^{PUSCH}-1$;

Set $\bar{M}_{sc}^{UCI}(l) = |\bar{\Phi}_l^{UCI}|$ for $l=0,1,2,\dots,N_{symb,all}^{PUSCH}-1$;

if the number of HARQ-ACK information bits to be transmitted on PUSCH is 0, 1 or 2 bits

the number of reserved resource elements for potential HARQ-ACK transmission is calculated according to

Subclause 6.3.2.4.1.1, by setting $O_{ACK} = 2$;

denote G_{rvd}^{ACK} as the number of coded bits for potential HARQ-ACK transmission using the reserved resource elements;

if frequency hopping is configured for the PUSCH, let $G_{rvd}^{ACK}(1) = N_L \otimes_m \otimes_{rvd}^{ACK} / (2 \otimes_L \otimes_m)$ and

$G_{rvd}^{ACK}(2) = N_L \otimes_m \otimes_{rvd}^{ACK} / (2 \otimes_L \otimes_m)$;

if frequency hopping is not configured for the PUSCH, let $G_{rvd}^{ACK}(1) = G_{rvd}^{ACK}$;

denote $\bar{\Phi}_l^{rvd}$ as the set of reserved resource elements for potential HARQ-ACK transmission, in OFDM symbol l , for $l=0,1,2,\dots,N_{symb,all}^{PUSCH}-1$;

Set $m_{count}^{ACK}(1)=0$;

Set $m_{count}^{ACK}(2)=0$;

$\bar{\Phi}_l^{rvd} = \emptyset$ for $l=0,1,2,\dots,N_{symb,all}^{PUSCH}-1$;

for $i=1$ to N_{hop}^{PUSCH}

$l=l^{(i)}$;

while $m_{count}^{ACK}(i) < G_{rvd}^{ACK}(i)$

if $\bar{M}_{sc}^{UCI}(l) > 0$

if $G_{rvd}^{ACK}(i) - m_{count}^{ACK}(i) < \bar{M}_{sc}^{UCI}(l) \otimes_L \otimes_m$

$d=1$;

$m_{count}^{RE} = \bar{M}_{sc}^{UL-SCH}(l)$;

end if

if $G_{rvd}^{ACK}(i) - m_{count}^{ACK}(i) < \bar{M}_{sc}^{UCI}(l) \otimes_L \otimes_m$

$d = \bar{M}_{sc}^{UCI}(l) \otimes_L \otimes_m / (G_{rvd}^{ACK}(i) - m_{count}^{ACK}(i))$;

$m_{count}^{RE} = G_{rvd}^{ACK}(i) - m_{count}^{ACK}(i) / (N_L \otimes_m)$;

end if

```

for   j=0   to   mcountRE-1
       $\bar{\Phi}_l^{\text{rvd}} = \bar{\Phi}_l^{\text{rvd}} \cup \{ \bar{\Phi}_l^{\text{UL-SCH}}(j) \}$ 
      mcountACK(i) = mcountACK(i) + NL · Qm ;
end for
end if
l=l+1 ;
end while
end for
else
 $\bar{\Phi}_l^{\text{rvd}} = \emptyset$    for   l=0,1,2,...,Nsymb,allPUSCH-1 ;
end if
Denote  $\bar{M}_{\text{sc, rvd}}^{\bar{\Phi}}(l) = |\bar{\Phi}_l^{\text{rvd}}|$    as the number of elements in  $\bar{\Phi}_l^{\text{rvd}}$  .

```

Step 2:

if HARQ-ACK is present for transmission on the PUSCH and the number of HARQ-ACK information bits is more than 2,

```

Set   mcountACK(1)=0 ;
Set   mcountACK(2)=0 ;
Set   mcount,allACK=0 ;
for   i=1   to   NhopPUSCH
      l=l(i) ;
      while   mcountACK(i) < GACK(i)
              if    $\bar{M}_{\text{sc}}^{\text{UCI}}(l) > 0$ 
                  if   GACK(i) - mcountACK(i) <  $\bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m$ 
                      d=1 ;
                      mcountRE =  $\bar{M}_{\text{sc}}^{\text{UCI}}(l)$  ;
                  end if
                  if   GACK(i) - mcountACK(i) <  $\bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot N_L \cdot Q_m$ 

```

$$d = \bar{M}_{sc}^{UCI}(l) \cdot N_L \cdot Q_m / (G^{ACK}(i) - m_{count}^{ACK}(i)) ;$$

$$m_{count}^{RE} = \lceil (G^{ACK}(i) - m_{count}^{ACK}(i)) / (N_L \cdot Q_m) \rceil ;$$

end if

for $j=0$ to $m_{count}^{RE}-1$

$$k = \bar{\Phi}_l^{UCI}(j) ;$$

for $v=0$ to $N_L \cdot Q_m - 1$

$$\bar{g}_{l,k,v} = g_{m_{count,all}}^{ACK} ;$$

$$m_{count,all}^{ACK} = m_{count,all}^{ACK} + 1 ;$$

$$m_{count}^{ACK}(i) = m_{count}^{ACK}(i) + 1 ;$$

end for

end for

for $j=0$ to $m_{count}^{RE}-1$

$$\bar{\Phi}_l^{UCI} = \bar{\Phi}_l^{UCI} \setminus \{ \bar{\Phi}_l^{UCI}(j) \} ;$$

$$\bar{\Phi}_l^{UL-SCH} = \bar{\Phi}_l^{UL-SCH} \setminus \{ \bar{\Phi}_l^{UCI}(j) \} ;$$

end for

$$\bar{M}_{sc}^{UCI}(l) = |\bar{\Phi}_l^{UCI}| ;$$

$$\bar{M}_{sc}^{UL-SCH}(l) = |\bar{\Phi}_l^{UL-SCH}| ;$$

end if

$l=l+1$;

end while

end for

end if

Step 3:

if CSI is present for transmission on the PUSCH,

Set $m_{count}^{CSI-part1}(1)=0$;

Set $m_{count}^{CSI-part1}(2)=0$;

Set $m_{count,all}^{CSI-part1}=0$;

for $i=1$ to $N_{\text{hop}}^{\text{PUSCH}}$

$l=l_{\text{CSI}}^{(i)}$;

while $\bar{M}_{\text{sc}}^{\text{UCI}}(l) - \bar{M}_{\text{sc, rvd}}^{\Phi}(l) < 0$

$l=l+1$;

end while

while $m_{\text{count}}^{\text{CSI-part1}}(i) < G^{\text{CSI-part1}}(i)$

if $\bar{M}_{\text{sc}}^{\text{UCI}}(l) - \bar{M}_{\text{sc, rvd}}^{\Phi}(l) > 0$

if $G^{\text{CSI-part1}}(i) - m_{\text{count}}^{\text{CSI-part1}}(i) < (\bar{M}_{\text{sc}}^{\text{UCI}}(l) - \bar{M}_{\text{sc, rvd}}^{\Phi}(l)) \nabla_L \nabla_m$

$d=1$;

$m_{\text{count}}^{\text{RE}} = \bar{M}_{\text{sc}}^{\text{UCI}}(l) - \bar{M}_{\text{sc, rvd}}^{\Phi}(l)$;

end if

if $G^{\text{CSI-part1}}(i) - m_{\text{count}}^{\text{CSI-part1}}(i) < (\bar{M}_{\text{sc}}^{\text{UCI}}(l) - \bar{M}_{\text{sc, rvd}}^{\Phi}(l)) \nabla_L \nabla_m$

$d = (\bar{M}_{\text{sc}}^{\text{UCI}}(l) - \bar{M}_{\text{sc, rvd}}^{\Phi}(l)) \nabla_L \nabla_m / (G^{\text{CSI-part1}}(i) - m_{\text{count}}^{\text{CSI-part1}}(i))$;

$m_{\text{count}}^{\text{RE}} = \lceil (G^{\text{CSI-part1}}(i) - m_{\text{count}}^{\text{CSI-part1}}(i)) / (N_L \cdot Q_m) \rceil$;

end if

$\bar{\Phi}_l^{\text{temp}} = \bar{\Phi}_l^{\text{UCI}} \setminus \bar{\Phi}_l^{\text{rvd}}$;

for $j=0$ to $m_{\text{count}}^{\text{RE}} - 1$

$k = \bar{\Phi}_l^{\text{temp}}(j)$;

for $v=0$ to $N_L \cdot Q_m - 1$

$\bar{g}_{l,k,v} = g_{m_{\text{count, all}}^{\text{CSI-part1}}}^{\text{CSI-part1}}$;

$m_{\text{count, all}}^{\text{CSI-part1}} = m_{\text{count, all}}^{\text{CSI-part1}} + 1$;

$m_{\text{count}}^{\text{CSI-part1}}(i) = m_{\text{count}}^{\text{CSI-part1}}(i) + 1$;

end for

end for

for $j=0$ to $m_{\text{count}}^{\text{RE}} - 1$

$\bar{\Phi}_l^{\text{UCI}} = \bar{\Phi}_l^{\text{UCI}} \setminus \{ \bar{\Phi}_l^{\text{temp}}(j) \}$;

$$\bar{\Phi}_l^{\text{UL-SCH}} = \bar{\Phi}_l^{\text{UL-SCH}} \setminus \{ \bar{\Phi}_l^{\text{temp}}(j) \}$$

end for

$$\bar{M}_{\text{sc}}^{\text{UCI}}(l) = |\bar{\Phi}_l^{\text{UCI}}| ;$$

$$\bar{M}_{\text{sc}}^{\text{UL-SCH}}(l) = |\bar{\Phi}_l^{\text{UL-SCH}}| ;$$

end if $l=l+1$;

end while

end for

$$\text{Set } m_{\text{count}}^{\text{CSI-part2}}(1)=0 ;$$

$$\text{Set } m_{\text{count}}^{\text{CSI-part2}}(2)=0 ;$$

$$\text{Set } m_{\text{count,all}}^{\text{CSI-part2}}=0 ;$$

$$\text{for } i=1 \text{ to } N_{\text{hop}}^{\text{PUSCH}}$$

$$l=l_{\text{CSI}}^{(i)} ;$$

while $\bar{M}_{\text{sc}}^{\text{UCI}}(l) > 0$

$l=l+1$;

end while

while $m_{\text{count}}^{\text{CSI-part2}}(i) < G^{\text{CSI-part2}}(i)$

if $\bar{M}_{\text{sc}}^{\text{UCI}}(l) > 0$

if $G^{\text{CSI-part2}}(i) - m_{\text{count}}^{\text{CSI-part2}}(i) > \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot Q_L \cdot Q_m$

$d=1$;

$$m_{\text{count}}^{\text{RE}} = \bar{M}_{\text{sc}}^{\text{UCI}}(l) ;$$

end if

if $G^{\text{CSI-part2}}(i) - m_{\text{count}}^{\text{CSI-part2}}(i) < \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot Q_L \cdot Q_m$

$$d = \bar{M}_{\text{sc}}^{\text{UCI}}(l) \cdot Q_L \cdot Q_m / (G^{\text{CSI-part2}}(i) - m_{\text{count}}^{\text{CSI-part2}}(i)) ;$$

$$m_{\text{count}}^{\text{RE}} = [(G^{\text{CSI-part2}}(i) - m_{\text{count}}^{\text{CSI-part2}}(i)) / (N_L \cdot Q_m)] ;$$

end if

for $j=0$ to $m_{\text{count}}^{\text{RE}} - 1$

```

 $k = \bar{\Phi}_l^{\text{UCI}}(j) ;$ 
for  $v=0$  to  $N_L \cdot Q_m - 1$ 
 $\bar{g}_{l,k,v} = g_{m_{\text{count, all}}}^{\text{CSI-part2}} ;$ 
 $m_{\text{count, all}}^{\text{CSI-part2}} = m_{\text{count, all}}^{\text{CSI-part2}} + 1 ;$ 
 $m_{\text{count}}^{\text{CSI-part2}}(i) = m_{\text{count}}^{\text{CSI-part2}}(i) + 1 ;$ 
end for
end for
for  $j=0$  to  $m_{\text{count}}^{\text{RE}} - 1$ 
 $\bar{\Phi}_l^{\text{UCI}} = \bar{\Phi}_l^{\text{UCI}} \setminus \{\bar{\Phi}_l^{\text{UCI}}(j)\} ;$ 
 $\bar{\Phi}_l^{\text{UL-SCH}} = \bar{\Phi}_l^{\text{UL-SCH}} \setminus \{\bar{\Phi}_l^{\text{UCI}}(j)\} ;$ 
end for
 $\bar{M}_{\text{sc}}^{\text{UCI}}(l) = |\bar{\Phi}_l^{\text{UCI}}| ;$ 
 $\bar{M}_{\text{sc}}^{\text{UL-SCH}}(l) = |\bar{\Phi}_l^{\text{UL-SCH}}| ;$ 
end if
 $l = l + 1 ;$ 
end while
end for
end if

```

Step 4:

if UL-SCH is present for transmission on the PUSCH,

```

Set  $m_{\text{count}}^{\text{UL-SCH}} = 0 ;$ 
for  $l=0$  to  $N_{\text{symb,all}}^{\text{PUSCH}} - 1$ 
if  $\bar{M}_{\text{sc}}^{\text{UL-SCH}}(l) > 0$ 
for  $j=0$  to  $\bar{M}_{\text{sc}}^{\text{UL-SCH}}(l) - 1$ 
 $k = \bar{\Phi}_l^{\text{UL-SCH}}(j) ;$ 
for  $v=0$  to  $N_L \cdot Q_m - 1$ 

```

```

 $\bar{g}_{l,k,v} = g_{m_{\text{count}}^{\text{UL-SCH}}}^{\text{UL-SCH}} ;$ 
 $m_{\text{count}}^{\text{UL-SCH}} = m_{\text{count}}^{\text{UL-SCH}} + 1 ;$ 
end for
end for
end if
end for
end if

```

Step 5:

if HARQ-ACK is present for transmission on the PUSCH and the number of HARQ-ACK information bits is no more than 2,

Set $m_{\text{count}}^{\text{ACK}}(1) = 0 ;$

Set $m_{\text{count}}^{\text{ACK}}(2) = 0 ;$

Set $m_{\text{count,all}}^{\text{ACK}} = 0 ;$

for $i=1$ to $N_{\text{hop}}^{\text{PUSCH}}$

$l = l^{(i)} ;$

while $m_{\text{count}}^{\text{ACK}}(i) < G^{\text{ACK}}(i)$

if $\bar{M}_{\text{sc, rvd}}^{\Phi}(l) > 0$

if $G^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i) \leq \bar{M}_{\text{sc, rvd}}^{\Phi}(l) \cdot N_L \cdot Q_m$

$d = 1 ;$

$m_{\text{count}}^{\text{RE}} = \bar{M}_{\text{sc, rvd}}^{\Phi}(l) ;$

end if

if $G^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i) < \bar{M}_{\text{sc, rvd}}^{\Phi}(l) \cdot N_L \cdot Q_m$

$d = \bar{M}_{\text{sc, rvd}}^{\Phi}(l) \cdot N_L \cdot Q_m / (G^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i)) ;$

$m_{\text{count}}^{\text{RE}} = \lceil (G^{\text{ACK}}(i) - m_{\text{count}}^{\text{ACK}}(i)) / (N_L \cdot Q_m) \rceil ;$

end if

for $j=0$ to $m_{\text{count}}^{\text{RE}} - 1$

$k = \bar{\Phi}_l^{\text{rvd}}(j) ;$

```

for  $v=0$  to  $N_L \cdot Q_m - 1$ 
 $\bar{g}_{l,k,v} = g_{m_{\text{count, all}}^{\text{ACK}}}^{\text{ACK}}$  ;
 $m_{\text{count, all}}^{\text{ACK}} = m_{\text{count, all}}^{\text{ACK}} + 1$  ;
 $m_{\text{count}}^{\text{ACK}}(i) = m_{\text{count}}^{\text{ACK}}(i) + 1$  ;
end for
end for
end if
 $l = l + 1$  ;
end while
end for
end if

```

Step 6:

```

Set  $t = 0$  ;
for  $l = 0$  to  $N_{\text{symb, all}}^{\text{PUSCH}} - 1$ 
  for  $j = 0$  to  $M_{\text{sc}}^{\text{UL-SCH}}(l) - 1$ 
     $k = \Phi_l^{\text{UL-SCH}}(j)$  ;
    for  $v = 0$  to  $N_L \cdot Q_m - 1$ 
       $g_t = \bar{g}_{l,k,v}$  ;
       $t = t + 1$  ;
    end for
  end for
end for

```

6.3 Uplink control information

6.3.1 Uplink control information on PUCCH

The procedure in this subclause applies to PUCCH formats 2/3/4.

6.3.1.1 UCI bit sequence generation

6.3.1.1.1 HARQ-ACK/SR only

If only HARQ-ACK bits are transmitted on a PUCCH, the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is determined by setting $a_i = \tilde{o}_i^{\text{ACK}}$ for $i=0, 1, \dots, O^{\text{ACK}}-1$ and $A = O^{\text{ACK}}$, where the HARQ-ACK bit sequence $\tilde{o}_0^{\text{ACK}}, \{\tilde{o}_1^{\text{ACK}}, \dots, \tilde{o}_{O^{\text{ACK}}-1}^{\text{ACK}}\}$ is given by Subclause 9.1 of [5, TS38.213].

If only HARQ-ACK and SR bits are transmitted on a PUCCH, the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is determined by setting $a_i = \tilde{o}_i^{\text{ACK}}$ for $i=0, 1, \dots, O^{\text{ACK}}-1$, $a_i = \tilde{o}_i^{\text{SR}}$ for $i=O^{\text{ACK}}, O^{\text{ACK}}+1, \dots, O^{\text{ACK}}+O^{\text{SR}}-1$, and $A = O^{\text{ACK}}+O^{\text{SR}}$, where the HARQ-ACK bit sequence $\tilde{o}_0^{\text{ACK}}, \{\tilde{o}_1^{\text{ACK}}, \dots, \tilde{o}_{O^{\text{ACK}}-1}^{\text{ACK}}\}$ is given by Subclause 9.1 of [5, TS 38.213], and the SR bit sequence $\tilde{o}_0^{\text{SR}}, \{\tilde{o}_1^{\text{SR}}, \dots, \tilde{o}_{O^{\text{SR}}-1}^{\text{SR}}\}$ is given by Subclause x.x of [5, TS 38.213].

6.3.1.1.2 CSI only

The bitwidth for PMI of *CodebookType=TypeI-SinglePanel* with 2 CSI-RS ports is 2 for Rank=1 and 1 for Rank=2, according to Subclause 5.2.2.2 in [6, TS 38.214].

The bitwidth for PMI of *CodebookType=TypeI-SinglePanel* with more than 2 CSI-RS ports is provided in Tables 6.3.1.1.2-1, where the values of (N_1, N_2) and (O_1, O_2) are given by Subclause 5.2.2.2.1 in [6, TS 38.214].

Table 6.3.1.1.2-1: PMI of *CodebookType=TypeI-SinglePanel*

	Information field X_1 for wideband PMI		Information field X_2 for wideband PMI or per subband PMI		
	$(i_{1,1}, i_{1,2})$		$i_{1,3}$	i_2	
	CodebookMode=1	CodebookMode=2		CodebookMode=1	CodebookMode=2
Rank = 1 with >2 CSI-RS ports, $N_2 > 1$	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	$\lceil \log_2\left(\frac{N_1 O_1}{2} \cdot \frac{N_2 O_2}{2}\right) \rceil$	N/A	2	4
Rank = 1 with >2 CSI-RS ports, $N_2 = 1$	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	$\lceil \log_2\left(\frac{N_1 O_1}{2}\right) \rceil$	N/A	2	4
Rank=2 with 4 CSI-RS ports, $N_2 = 1$	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	$\lceil \log_2\left(\frac{N_1 O_1}{2}\right) \rceil$	1	1	3
Rank=2 with >4 CSI-RS ports, $N_2 > 1$	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	$\lceil \log_2\left(\frac{N_1 O_1}{2} \cdot \frac{N_2 O_2}{2}\right) \rceil$	2	1	3
Rank=2 with >4 CSI-RS ports, $N_2 = 1$	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	$\lceil \log_2\left(\frac{N_1 O_1}{2}\right) \rceil$	2	1	3

Rank=3 or 4, with 4 CSI-RS ports	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	0	1
Rank=3 or 4, with 8 or 12 CSI-RS ports	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	2	1
Rank=3 or 4, with $>=16$ CSI- RS ports	$\lceil \log_2\left(\frac{N_1 O_1}{2} \cdot N_2 O_2\right) \rceil$	2	1
Rank=5 or 6	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	N/A	1
Rank=7 or 8, $N_1 = 4, N_2 = 1$	$\lceil \log_2\left(\frac{N_1 O_1}{2} \cdot N_2 O_2\right) \rceil$	N/A	1
Rank=7 or 8, $N_1 > 2, N_2 = 2$	$\lceil \log_2\left(N_1 O_1 \cdot \frac{N_2 O_2}{2}\right) \rceil$	N/A	1
Rank=7 or 8, with $N_1 > 4, N_2 = 1$ or $N_1 = 2, N_2 = 2$ or $N_1 > 2, N_2 > 2$	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	N/A	1

The bitwidth for PMI of *CodebookType= TypeI-MultiPanel* is provided in Tables 6.3.1.1.2-2, where the values of (N_g, N_1, N_2) and (O_1, O_2) are given by Subclause 5.2.2.2.2 in [6, TS 38.214].

Table 6.3.1.1.2-2: PMI of *CodebookType= TypeI-MultiPanel*

	Information fields X_1 for wideband					Information fields X_2 for wideband or per subband			
	$(i_{1,1}, i_{1,2})$	$i_{1,3}$	$i_{1,4,1}$	$i_{1,4,2}$	$i_{1,4,3}$	i_2	$i_{2,0}$	$i_{2,1}$	$i_{2,2}$
Rank=1 with $N_g=2$ <i>CodebookMode=1</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	N/A	2	N/A	N/A	2	N/A	N/A	N/A
Rank=1 with $N_g=4$ <i>CodebookMode=1</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	N/A	2	2	2	2	N/A	N/A	N/A
Rank=2 with $N_g=2$, $N_1 N_2 = 2$ <i>CodebookMode=1</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	1	2	N/A	N/A	1	N/A	N/A	N/A
Rank=3 or 4 with $N_g=2$, $N_1 N_2 = 2$	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	0	2	N/A	N/A	1	N/A	N/A	N/A

<i>CodebookMode=1</i>									
Rank=2 or 3 or 4 with $N_g = 2$, $N_1 N_2 > 2$ <i>CodebookMode=1</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	2	2	N/A	N/A	1	N/A	N/A	N/A
Rank=2 with $N_g = 4$, $N_1 N_2 = 2$ <i>CodebookMode=1</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	1	2	2	2	1	N/A	N/A	N/A
Rank=3 or 4 with $N_g = 4$, $N_1 N_2 = 2$ <i>CodebookMode=1</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	0	2	2	2	1	N/A	N/A	N/A
Rank=2 or 3 or 4 with $N_g = 4$, $N_1 N_2 > 2$ <i>CodebookMode=1</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	2	2	2	2	1	N/A	N/A	N/A
Rank=1 with $N_g = 2$ <i>CodebookMode=2</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	N/A	2	2	N/A	N/A	2	1	1
Rank=2 with $N_g = 2$, $N_1 N_2 = 2$ <i>CodebookMode=2</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	1	2	2	N/A	N/A	1	1	1
Rank=3 or 4 with $N_g = 2$, $N_1 N_2 = 2$ <i>CodebookMode=2</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	0	2	2	N/A	N/A	1	1	1
Rank=2 or 3 or 4 with $N_g = 2$, $N_1 N_2 > 2$ <i>CodebookMode=2</i>	$\lceil \log_2(N_1 O_1 \cdot N_2 O_2) \rceil$	2	2	2	N/A	N/A	1	1	1

The bitwidth for RI/LI/CQI of *CodebookType=TypeI-SinglePanel* is provided in Tables 6.3.1.1.2-3.

Table 6.3.1.1.2-3: RI, LI, and CQI of CodebookType=TypeI-SinglePanel

Field	Bitwidth			
	2 antenna ports		>4 antenna ports	
	4 antenna ports		Rank1~4	Rank5~8
Rank Indicator	$\min(1, \lceil \log_2 n_{\text{RI}} \rceil)$	$\min(2, \lceil \log_2 n_{\text{RI}} \rceil)$	$\lceil \log_2 n_{\text{RI}} \rceil$	$\lceil \log_2 n_{\text{RI}} \rceil$
Layer Indicator	$\min(2, \lceil \log_2 \text{RI} \rceil)$	$\min(2, \lceil \log_2 \text{RI} \rceil)$	$\min(2, \lceil \log_2 \text{RI} \rceil)$	$\min(2, \lceil \log_2 \text{RI} \rceil)$
Wide-band CQI	4	4	4	8
Subband differential CQI	2	2	2	4
CRI	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$

If the higher layer parameter Number_CQI is not configured or $\text{Number_CQI}=1$, n_{RI} in Table 6.3.1.1.2-3 is the number of allowed rank indicator values in the 4 LSBs of the higher layer parameter $\text{TypeI-SinglePanel-RI-Restriction}$ according to Subclause X.X [6, TS 38.214]; otherwise n_{RI} in Table 6.3.1.1.2-3 is the number of allowed rank indicator values according to Subclause X.X [6, TS 38.214]. The value of $K_s^{\text{CSI-RS}}$ is the number of CSI-RS resources in the corresponding resource set.

The bitwidth for RI/LI/CQI of $\text{CodebookType} = \text{TypeI-MultiPanel}$ is provided in Table 6.3.1.1.2-4.

Table 6.3.1.1.2-4: RI, LI, and CQI of CodebookType=TypeI-MultiPanel

Field	Bitwidth
Rank Indicator	$\min(2, \lceil \log_2 n_{\text{RI}} \rceil)$
Layer Indicator	$\min(2, \lceil \log_2 \text{RI} \rceil)$
Wide-band CQI	4
Subband differential CQI	2
CRI	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$

Where n_{RI} is the number of allowed rank indicator values according to Subclause X.X [6, TS 38.214], and $K_s^{\text{CSI-RS}}$ is the number of CSI-RS resources in the corresponding resource set.

The bitwidth for RI/LI/CQI of $\text{CodebookType} = \text{TypeII}$ is provided in Table 6.3.1.1.2-5.

Table 6.3.1.1.2-5: RI, LI, and CQI of CodebookType=TypeII or TypeII-PortSelection

Field	Bitwidth
Rank Indicator	$\min(1, \lceil \log_2 n_{\text{RI}} \rceil)$
Layer Indicator	$\min(2, \lceil \log_2 \text{RI} \rceil)$
Wide-band CQI	4
Subband differential CQI	2
Indicator of the number of non-zero wideband amplitude coefficients M_l for layer l	$\lceil \log_2(2L-1) \rceil$
CRI	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$

Where n_{RI} is the number of allowed rank indicator values according to Subclause X.X [6, TS 38.214], and $K_s^{\text{CSI-RS}}$ is the number of CSI-RS resources in the corresponding resource set.

The bitwidth for CRI, SSB index, RSRP, and differential RSRP are provided in Table 6.3.1.1.2-6.

Table 6.3.1.1.2-6: CRI, SSB index, and RSRP

Field	Bitwidth
CRI	$\lceil \log_2(K_s^{\text{CSI-RS}}) \rceil$
SSB index	$\lceil \log_2(K_s^{\text{SSB}}) \rceil$
RSRP	7
Differential RSRP	4

where $K_s^{\text{CSI-RS}}$ is the number of CSI-RS resources in the corresponding resource set, and K_s^{SSB} is the configured number of SS/PBCH blocks in the corresponding resource set for reporting 'SSBRI/RSRP'.

Table 6.3.1.1.2-7: Mapping order of CSI fields of one CSI report, PMI-FormatIndicator=widebandPMI and CQI-FormatIndicator=widebandCQI

CSI report number	CSI fields
CSI report #n	CRI as in Tables 6.3.1.1.2-3/4/5, if reported
	Rank Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	Layer Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	Zero padding bits O_p , if needed
	PMI wideband information fields X_1 , from left to right as in Tables 6.3.1.1.2-1/2, if reported
	PMI wideband information fields X_2 , from left to right as in Tables 6.3.1.1.2-1/2, if reported
	Wideband CQI as in Tables 6.3.1.1.2-3/4/5, if reported
	Indicator of the number of non-zero wideband amplitude coefficients M_l for layer l as in Table 6.3.1.1.2-5, if reported

The number of zero padding bits O_p in Table 6.3.1.1.2-7 is $O_p = N_{\max} - N_{\text{reported}}$, where

- $N_{\max} = \max_{r \in S_{\text{Rank}}} B(r)$ and S_{Rank} is the set of rank values r that are allowed to be reported;
- $N_{\text{reported}} = B(R)$, where R is the reported rank;
- For 2 CSI-RS ports, $B(r) = N_{\text{PMI}}(r) + N_{\text{CQI}}(r) + N_{\text{LI}}(r)$;
- For more than 2 CSI-RS ports, $B(r) = N_{\text{PMI},i1}(r) + N_{\text{PMI},i2}(r) + N_{\text{CQI}}(r) + N_{\text{LI}}(r)$;
- if PMI is reported, $N_{\text{PMI}}(1)=2$ and $N_{\text{PMI}}(2)=1$; otherwise, $N_{\text{PMI}}(r)=0$;
- if PMI $i1$ is reported, $N_{\text{PMI},i1}(r)$ is obtained according to Tables 6.3.1.1.2-1/2; otherwise, $N_{\text{PMI},i1}(r)=0$;
- if PMI $i2$ is reported, $N_{\text{PMI},i2}(r)$ is obtained according to Tables 6.3.1.1.2-1/2; otherwise, $N_{\text{PMI},i2}(r)=0$;
- if CQI is reported, $N_{\text{CQI}}(r)$ is obtained according to Tables 6.3.1.1.2-3/4; otherwise, $N_{\text{CQI}}(r)=0$;
- if LI is reported, $N_{\text{LI}}(r)$ is obtained according to Tables 6.3.1.1.2-3/4; otherwise, $N_{\text{LI}}(r)=0$.

Table 6.3.1.1.2-8: Mapping order of CSI fields of one report for CRI/RSRP or SSB/RSRP reporting

CSI report number	CSI fields
CSI report #n	CRI or SSB index #1 as in Table 6.3.1.1.2-6, if reported
	CRI or SSB index #2 as in Table 6.3.1.1.2-6, if reported
	CRI or SSB index #3 as in Table 6.3.1.1.2-6, if reported
	CRI or SSB index #4 as in Table 6.3.1.1.2-6, if reported
	RSRP #1 as in Table 6.3.1.1.2-6, if reported
	Differential RSRP #2 as in Table 6.3.1.1.2-6, if reported
	Differential RSRP #3 as in Table 6.3.1.1.2-6, if reported
	Differential RSRP #4 as in Table 6.3.1.1.2-6, if reported

Table 6.3.1.1.2-9: Mapping order of CSI fields of one CSI report, CSI part 1, PMI-FormatIndicator= subbandPMI or CQI-FormatIndicator= subbandCQI

CSI report number	CSI fields
CSI report #n CSI part 1	CRI as in Tables 6.3.1.1.2-3/4/5, if reported
	Rank Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	Wideband CQI for the first TB as in Tables 6.3.1.1.2-3/4/5, if reported
	Subband differential CQI for the first TB as in Tables 6.3.1.1.2-3/4/5, if reported
	Indicator of the number of non-zero wideband amplitude coefficients M_l for layer l as in Table 6.3.1.1.2-5, if reported

Table 6.3.1.1.2-10: Mapping order of CSI fields of one CSI report, CSI part 2 wideband, PMI-FormatIndicator= subbandPMI or CQI-FormatIndicator= subbandCQI

CSI report number	CSI fields
CSI report #n CSI part 2 wideband	Wideband CQI for the second TB as in Tables 6.3.1.1.2-3/4/5, if present and reported
	Layer Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	PMI wideband information fields X_1 , from left to right as in Tables 6.3.1.1.2-1/2, if reported
	PMI wideband information fields X_2 , from left to right as in Tables 6.3.1.1.2-1/2, if PMI-FormatIndicator= widebandPMI and if reported

Table 6.3.1.1.2-11: Mapping order of CSI fields of one CSI report, CSI part 2 subband, PMI-FormatIndicator= subbandPMI or CQI-FormatIndicator= subbandCQI

CSI report #n Part 2 subband	Subband differential CQI for the second TB of all even subbands with increasing order of subband number, as in Tables 6.3.1.1.2-3/4/5, if CQI-FormatIndicator= subbandCQI and if reported
	PMI subband information fields X_2 of all even subbands with increasing order of subband number, from left to right as in Tables 6.3.1.1.2-1/2, if PMI-FormatIndicator= subbandPMI and if reported
	Subband differential CQI for the second TB of all odd subbands with increasing order of subband number, as in Tables 6.3.1.1.2-3/4/5, if CQI-FormatIndicator= subbandCQI and if reported
	PMI subband information fields X_2 of all odd subbands with increasing order of subband number, from left to right as in Tables 6.3.1.1.2-1/2, if PMI-FormatIndicator= subbandPMI and if reported

If none of the CSI reports for transmission on a PUCCH is of two parts, the CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-12, are mapped to the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ starting with a_0 .

Table 6.3.1.1.2-12: Mapping order of CSI reports to UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, without two-part CSI report(s)

UCI bit sequence	CSI report number
a_0	CSI report #1 as in Table 6.3.1.1.2-7/8
a_1	CSI report #2 as in Table 6.3.1.1.2-7/8
a_2	
a_3	
\vdots	...
a_{A-1}	CSI report #n as in Table 6.3.1.1.2-7/8

If at least one of the CSI reports for transmission on a PUCCH is of two parts, two UCI bit sequences are generated, $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ and $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$. The CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-13, are mapped to the UCI bit sequence $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ starting with $a_0^{(1)}$. The CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-14, are mapped to the UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ starting with $a_0^{(2)}$.

Table 6.3.1.1.2-13: Mapping order of CSI reports to UCI bit sequence $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$, with two-part CSI report(s)

UCI bit sequence	CSI report number
$a_0^{(1)}$	CSI report #1 if CSI report #1 is not of two parts, or CSI report #1, CSI part 1, if CSI report #1 is of two parts, as in Table 6.3.1.1.2-7/8/9
$a_1^{(1)}$	CSI report #2 if CSI report #2 is not of two parts, or CSI report #2, CSI part 1, if CSI report #2 is of two parts, as in Table 6.3.1.1.2-7/8/9
$a_2^{(1)}$	
$a_3^{(1)}$...
$a_{A^{(1)}-1}^{(1)}$	CSI report #n if CSI report #n is not of two parts, or CSI report #n, CSI part 1, if CSI report #n is of two parts, as in Table 6.3.1.1.2-7/8/9

Table 6.3.1.1.2-14: Mapping order of CSI reports to UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$, with two-part CSI report(s)

UCI bit sequence	CSI report number
$a_0^{(2)}$ $a_1^{(2)}$ $a_2^{(2)}$ $a_3^{(2)}$ \vdots $a_{A^{(2)}-1}^{(2)}$	CSI report #1, CSI part 2 wideband, as in Table 6.3.1.1.2-10 if CSI part 2 exists for CSI report #1
	CSI report #2, CSI part 2 wideband, as in Table 6.3.1.1.2-10 if CSI part 2 exists for CSI report #2
	...
	CSI report #n, CSI part 2 wideband, as in Table 6.3.1.1.2-10 if CSI part 2 exists for CSI report #n
	CSI report #1, CSI part 2 subband, as in Table 6.3.1.1.2-11 if CSI part 2 exists for CSI report #1
	CSI report #2, CSI part 2 subband, as in Table 6.3.1.1.2-11 if CSI part 2 exists for CSI report #2
	...
	CSI report #n, CSI part 2 subband, as in Table 6.3.1.1.2-11 if CSI part 2 exists for CSI report #n

6.3.1.1.3 HARQ-ACK/SR and CSI

If none of the CSI reports for transmission on a PUCCH is of two parts, the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is generated according to the following, where $A = O^{\text{ACK}} + O^{\text{SR}} + O^{\text{CSI}}$:

- the HARQ-ACK bits are mapped to the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{O^{\text{ACK}}-1}$, where $a_i = \tilde{o}_i^{\text{ACK}}$ for $i=0, 1, \dots, O^{\text{ACK}}-1$, the HARQ-ACK bit sequence $\tilde{o}_0^{\text{ACK}}, \{\tilde{o}_1^{\text{ACK}}, \dots, \tilde{o}_{O^{\text{ACK}}-1}^{\text{ACK}}\}$ is given by Subclause 9.1 of [5, TS38.213], and O^{ACK} is number of HARQ-ACK bits;
- if SR is transmitted on the PUCCH, set $a_i = \tilde{o}_i^{\text{SR}}$ for $i=O^{\text{ACK}}, O^{\text{ACK}}+1, \dots, O^{\text{ACK}}+O^{\text{SR}}-1$, where the SR bit sequence $\tilde{o}_0^{\text{SR}}, \{\tilde{o}_1^{\text{SR}}, \dots, \tilde{o}_{O^{\text{SR}}-1}^{\text{SR}}\}$ is given by Subclause x.x of [5, TS 38.213];
- the CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.1.2-12, are mapped to the UCI bit sequence $a_{O^{\text{ACK}}+O^{\text{SR}}}, a_{O^{\text{ACK}}+O^{\text{SR}}+1}, \dots, a_{O^{\text{ACK}}+O^{\text{SR}}+O^{\text{CSI}}-1}$ starting with $a_{O^{\text{ACK}}+O^{\text{SR}}}$, where O^{CSI} is the number of CSI bits.

If at least one of the CSI reports for transmission on a PUCCH is of two parts, two UCI bit sequences are generated, $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ and $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$, according to the following, where $A^{(1)} = O^{\text{ACK}} + O^{\text{SR}} + O^{\text{CSI-part1}}$ and $A^{(2)} = O^{\text{CSI-part2}}$:

- the HARQ-ACK bits are mapped to the UCI bit sequence $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{O^{\text{ACK}}-1}^{(1)}$, where $a_i^{(1)} = \tilde{o}_i^{\text{ACK}}$ for $i=0, 1, \dots, O^{\text{ACK}}-1$, the HARQ-ACK bit sequence $\tilde{o}_0^{\text{ACK}}, \{\tilde{o}_1^{\text{ACK}}, \dots, \tilde{o}_{O^{\text{ACK}}-1}^{\text{ACK}}\}$ is given by Subclause 9.1 of [5, TS38.213], and O^{ACK} is number of HARQ-ACK bits;
- if SR is transmitted on the PUCCH, set $a_i = \tilde{o}_i^{\text{SR}}$ for $i=O^{\text{ACK}}, O^{\text{ACK}}+1, \dots, O^{\text{ACK}}+O^{\text{SR}}-1$, where the SR bit sequence $\tilde{o}_0^{\text{SR}}, \{\tilde{o}_1^{\text{SR}}, \dots, \tilde{o}_{O^{\text{SR}}-1}^{\text{SR}}\}$ is given by Subclause x.x of [5, TS 38.213];

- the CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.2-13, are mapped to the UCI bit sequence $a_{O^{\text{ACK}}+O^{\text{SR}}}^{(1)}, a_{O^{\text{ACK}}+O^{\text{SR}}+1}^{(1)}, \dots, a_{O^{\text{ACK}}+O^{\text{SR}}+O^{\text{CSI-part1}-1}}^{(1)}$ starting with $a_{O^{\text{ACK}}+O^{\text{SR}}}^{(1)}$, where $O^{\text{CSI-part1}}$ is the number of CSI bits in CSI part 1 of all CSI reports;
- the CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.1.2-14, are mapped to the UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ starting with $a_0^{(2)}$, where $O^{\text{CSI-part2}}$ is the number of CSI bits in CSI part 2 of all CSI reports.

6.3.1.2 Code block segmentation and CRC attachment

The UCI bit sequence from subclause 6.3.1.1 is denoted by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, where A is the payload size. The procedure in 6.3.1.2.1 applies for $A \geq 12$ and the procedure in Subclause 6.3.1.2.2 applies for $A \leq 11$.

6.3.1.2.1 UCI encoded by Polar code

If the payload size $A \geq 12$, code block segmentation and CRC attachment is performed according to Subclause 5.2.1. If $A \geq 360$ and $E \geq 1088$, $I_{\text{seg}} = 1$; otherwise $I_{\text{seg}} = 0$, where E is the rate matching output sequence length as given in Subclause 6.3.1.4.

If $12 \leq A \leq 19$, the parity bits $p_{r0}, p_{r1}, p_{r2}, \dots, p_{r|L-1|}$ in Subclause 5.2.1 are computed by setting L to 6 bits and using the generator polynomial $g_{\text{CRC6}}(D)$ in Subclause 5.1, resulting in the sequence $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r|K_r-1|}$ where r is the code block number and K_r is the number of bits for code block number r .

If $A \geq 20$, the parity bits $p_{r0}, p_{r1}, p_{r2}, \dots, p_{r|L-1|}$ in Subclause 5.2.1 are computed by setting L to 11 bits and using the generator polynomial $g_{\text{CRC11}}(D)$ in Subclause 5.1, resulting in the sequence $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r|K_r-1|}$ where r is the code block number and K_r is the number of bits for code block number r .

6.3.1.2.2 UCI encoded by channel coding of small block lengths

If the payload size $A \leq 11$, CRC bits are not attached.

The output bit sequence is denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where $c_i = a_i$ for $i = 0, 1, \dots, A-1$ and $K = A$.

6.3.1.3 Channel coding of UCI

6.3.1.3.1 UCI encoded by Polar code

Information bits are delivered to the channel coding block. They are denoted by $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r|K_r-1|}$, where r is the code block number, and K_r is the number of bits in code block number r . The total number of code blocks is denoted by C and each code block is individually encoded by the following:

If $18 \leq K_r \leq 25$, the information bits are encoded via Polar coding according to Subclause 5.3.1, by setting $n_{\max} = 10$, $I_{IL} = 0$, $n_{PC} = 3$, $n_{PC}^{wm} = 1$ if $E_r - K_r + 3 > 192$ and $n_{PC}^{wm} = 0$ if $E_r - K_r + 3 \leq 192$, where E_r is the rate matching output sequence length as given in Subclause 6.3.1.4.1.

If $K_r > 30$, the information bits are encoded via Polar coding according to Subclause 5.3.1, by setting $n_{\max} = 10$, $I_{IL} = 0$, $n_{PC} = 0$, and $n_{PC}^{wm} = 0$.

After encoding the bits are denoted by $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r|N_r-1|}$, where N_r is the number of coded bits in code block number r .

6.3.1.3.2 UCI encoded by channel coding of small block lengths

Information bits are delivered to the channel coding block. They are denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits.

The information bits are encoded according to Subclause 5.3.3.

After encoding the bits are denoted by $d_0, d_1, d_2, d_3, \dots, d_{N-1}$, where N is the number of coded bits.

6.3.1.4 Rate matching

For PUCCH formats 2/3/4, the total rate matching output sequence length E_{tot} is given by Table 6.3.1.4-1, where $N_{\text{symb, UCI}}^{\text{PUCCH, 2}}$, $N_{\text{symb, UCI}}^{\text{PUCCH, 3}}$, and $N_{\text{symb, UCI}}^{\text{PUCCH, 4}}$ are the number of symbols carrying UCI for PUCCH formats 2/3/4 respectively; $N_{\text{PRB}}^{\text{PUCCH, 2}}$ and $N_{\text{PRB}}^{\text{PUCCH, 3}}$ are the number of PRBs that are determined by the UE for PUCCH formats 2/3 transmission respectively according to Subclause x.x of [5, TS38.213]; and $N_{\text{SF}}^{\text{PUCCH, 4}}$ is the spreading factor for PUCCH format 4.

Table 6.3.1.4-1: Total rate matching output sequence length E_{tot}

PUCCH format	Modulation order	
	QPSK	$\pi/2\text{-BPSK}$
PUCCH format 2	$16 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 2}} \cdot N_{\text{PRB}}^{\text{PUCCH, 2}}$	N/A
PUCCH format 3	$24 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 3}} \cdot N_{\text{PRB}}^{\text{PUCCH, 3}}$	$12 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 3}} \cdot N_{\text{PRB}}^{\text{PUCCH, 3}}$
PUCCH format 4	$24 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 4}} / N_{\text{SF}}^{\text{PUCCH, 4}}$	$12 \cdot N_{\text{symb, UCI}}^{\text{PUCCH, 4}} / N_{\text{SF}}^{\text{PUCCH, 4}}$

6.3.1.4.1 UCI encoded by Polar code

The input bit sequence to rate matching is $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$ where r is the code block number, and N_r is the number of coded bits in code block number r .

Table 6.3.1.4.1-1: Rate matching output sequence length E_{UCI}

UCI(s) for transmission on a PUCCH	UCI for encoding	Value of E_{UCI}
HARQ-ACK	HARQ-ACK	$E_{\text{UCI}} = E_{\text{tot}}$
HARQ-ACK, SR	HARQ-ACK, SR	$E_{\text{UCI}} = E_{\text{tot}}$
CSI (CSI not of two parts)	CSI	$E_{\text{UCI}} = E_{\text{tot}}$
HARQ-ACK, CSI (CSI not of two parts)	HARQ-ACK, CSI	$E_{\text{UCI}} = E_{\text{tot}}$
HARQ-ACK, SR, CSI (CSI not of two parts)	HARQ-ACK, SR, CSI	$E_{\text{UCI}} = E_{\text{tot}}$
CSI (CSI of two parts)	CSI part 1	$E_{\text{UCI}} = \min(E_{\text{tot}}, \lceil (O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\max} / Q_m \rceil \cdot Q_m)$
	CSI part 2	$E_{\text{UCI}} = E_{\text{tot}} - \min(E_{\text{tot}}, \lceil (O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\max} / Q_m \rceil \cdot Q_m)$
HARQ-ACK, CSI part 1 (CSI of two parts)	HARQ-ACK, CSI part 1	$E_{\text{UCI}} = \min(E_{\text{tot}}, \lceil (O^{\text{ACK}} + O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\max} / Q_m \rceil \cdot Q_m)$
	CSI part 2	$E_{\text{UCI}} = E_{\text{tot}} - \min(E_{\text{tot}}, \lceil (O^{\text{ACK}} + O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\max} / Q_m \rceil \cdot Q_m)$
HARQ-ACK, SR, CSI part 1 (CSI of two parts)	HARQ-ACK, SR, CSI part 1	$E_{\text{UCI}} = \min(E_{\text{tot}}, \lceil (O^{\text{ACK}} + O^{\text{SR}} + O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\max} / Q_m \rceil \cdot Q_m)$
	CSI part 2	$E_{\text{UCI}} = E_{\text{tot}} - \min(E_{\text{tot}}, \lceil (O^{\text{ACK}} + O^{\text{SR}} + O^{\text{CSI-part1}} + L) / R_{\text{UCI}}^{\max} / Q_m \rceil \cdot Q_m)$

Rate matching is performed according to Subclause 5.4.1 by setting $I_{BL}=1$ and the rate matching output sequence length to $E_r = \lfloor E_{UCI} / C_{UCI} \rfloor$, where C_{UCI} is the number of code blocks for UCI determined according to Subclause 6.3.1.2.1 and the value of E_{UCI} is given by Table 6.3.1.4.1-1:

- O^{ACK} is the number of bits for HARQ-ACK for transmission on the current PUCCH;
- O^{SR} is the number of bits for SR for transmission on the current PUCCH;
- $O^{CSI-part1}$ is the number of bits for CSI part 1 for transmission on the current PUCCH;
- $O^{CSI-part2}$ is the number of bits for CSI part 2 for transmission on the current PUCCH;
- L is the number of CRC bits;
- R_{UCI}^{\max} is the configured maximum PUCCH coding rate;
- E_{tot} is given by Table 6.3.1.4-1.

The output bit sequence after rate matching is denoted as $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$ where E_r is the length of rate matching output sequence in code block number r .

6.3.1.4.2 UCI encoded by channel coding of small block lengths

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$.

The value of E_{UCI} is determined according to Table 6.3.1.4.1-1 by setting $L=0$.

Rate matching is performed according to Subclause 5.4.3 by setting the rate matching output sequence length $E=E_{UCI}$.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

6.3.1.5 Code block concatenation

The input bit sequence for the code block concatenation block are the sequences $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$, for $r=0, \dots, C-1$ and where E_r is the number of rate matched bits for the r -th code block.

Code block concatenation is performed according to Subclause 5.5.

The bits after code block concatenation are denoted by $g_0, g_1, g_2, g_3, \dots, g_{G'-1}$, where $G' = \lfloor E_{UCI} / C_{UCI} \rfloor \cdot C_{UCI}$ with the values of E_{UCI} and C_{UCI} given in Subclause 6.3.1.4.1. Let G be the total number of coded bits for transmission and $G=G'+\text{mod}(E_{UCI}, C_{UCI})$. Set $g_i=0$ for $i=G', G'+1, \dots, G-1$.

6.3.1.6 Multiplexing of coded UCI bits to PUCCH

If CSI of two parts are transmitted on a PUCCH, the coded bits corresponding to UCI bit sequence

$a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ is denoted by $g_0^{(1)}, g_1^{(1)}, g_2^{(1)}, g_3^{(1)}, \dots, g_{G^{(1)}-1}^{(1)}$ and the coded bits corresponding to UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ is denoted by $g_0^{(2)}, g_1^{(2)}, g_2^{(2)}, g_3^{(2)}, \dots, g_{G^{(2)}-1}^{(2)}$. The coded bit sequence

$g_0, g_1, g_2, g_3, \dots, g_{G-1}$, where $G=G^{(1)}+G^{(2)}$, is generated according to the following.

Table 6.3.1.6-1: PUCCH DMRS and UCI symbols

PUCCH duration (symbols)	PUCCH DMRS symbol indices	Number of UCI symbol indices sets $N_{\text{UCI}}^{\text{set}}$	1 st UCI symbol indices set $S_{\text{UCI}}^{(1)}$	2 nd UCI symbol indices set $S_{\text{UCI}}^{(2)}$	3 rd UCI symbol indices set $S_{\text{UCI}}^{(3)}$
4	{1}	2	{0,2}	{3}	-
4	{0,2}	1	{1,3}	-	-
5	{0, 3}	1	{1, 2, 4}	-	-
6	{1, 4}	1	{0, 2, 3, 5}	-	-
7	{1, 4}	2	{0, 2, 3, 5}	{6}	-
8	{1, 5}	2	{0, 2, 4, 6}	{3, 7}	-
9	{1, 6}	2	{0, 2, 5, 7}	{3, 4, 8}	-
10	{2, 7}	2	{1, 3, 6, 8}	{0, 4, 5, 9}	-
10	{1, 3, 6, 8}	1	{0,2,4,5,7,9}	-	-
11	{2, 7}	3	{1,3,6,8}	{0,4,5,9}	{10}
11	{1,3,6,9}	1	{0,2,4,5,7,8,10}	-	-
12	{2, 8}	3	{1,3,7,9}	{0,4,6,10}	{5, 11}
12	{1,4,7,10}	1	{0,2,3,5,6,8,9,11}	-	-
13	{2, 9}	3	{1,3,8,10}	{0,4,7,11}	{5,6,12}
13	{1,4,7,11}	2	{0,2,3,5,6,8,10,12}	{9}	-
14	{3, 10}	3	{2,4,9,11}	{1,5,8,12}	{0,6,7,13}
14	{1,5,8,12}	2	{0,2,4,6,7,9,11,13}	{3, 10}	-

Denote s_i as UCI OFDM symbol index. Denote $N_{\text{UCI}}^{(i)}$ as the number of elements in UCI symbol indices set $S_{\text{UCI}}^{(i)}$ for $i=1, \dots, N_{\text{UCI}}^{\text{set}}$, where $S_{\text{UCI}}^{(i)}$ and $N_{\text{UCI}}^{\text{set}}$ are given by Table 6.3.1.6-1 according to the PUCCH duration and the PUCCH DMRS configuration. Denote $N_{\text{symb, UCI}}^{\text{PUCCH}} = \sum_{i=1}^{N_{\text{UCI}}^{\text{set}}} N_{\text{UCI}}^{(i)}$ as the number of OFDM symbols carrying UCI in the PUCCH. Denote Q_m as the modulation order of the PUCCH.

For PUCCH format 3, set $N_{\text{UCI}}^{\text{symbol}} = 12 \cdot N_{\text{PRB}}^{\text{PUCCH,3}}$, where $N_{\text{PRB}}^{\text{PUCCH,3}}$ is the number of PRBs that is determined by the UE for PUCCH format 3 transmission according to Subclause x.x of [5, TS 38.213].

For PUCCH format 4, set $N_{\text{UCI}}^{\text{symbol}} = 12 / N_{\text{SF}}^{\text{PUCCH,4}}$, where $N_{\text{SF}}^{\text{PUCCH,4}}$ is the spreading factor for PUCCH format 4.

Find the smallest $j > 0$ such that $\left(\sum_{i=1}^j N_{\text{UCI}}^{(i)} \right) \cdot N_{\text{UCI}}^{\text{symbol}} \cdot Q_m \geq G^{(1)}$.

Set $n_1 = 0$;

Set $n_2 = 0$;

Set $\bar{N}_{\text{UCI}}^{\text{symbol}} = \left\lceil \left(G^{(1)} - \left(\sum_{i=1}^{j-1} N_{\text{UCI}}^{(i)} \right) \cdot N_{\text{UCI}}^{\text{symbol}} \cdot Q_m \right) / (N_{\text{UCI}}^{(j)} \cdot Q_m) \right\rceil$;

Set $M = \text{mod} \left(\left(G^{(1)} - \left(\sum_{i=1}^{j-1} N_{\text{UCI}}^{(i)} \right) \cdot N_{\text{UCI}}^{\text{symbol}} \cdot Q_m \right) / Q_m, N_{\text{UCI}}^{(j)} \right)$;

for $l=0$ to $N_{\text{symb, UCI}}^{\text{PUCCH}} - 1$

if $s_l \in \bigcup_{i=1}^{j-1} S_{\text{UCI}}^{(i)}$

for $k=0$ to $N_{\text{UCI}}^{\text{symbol}}-1$

for $v=0$ to Q_m-1

$\bar{g}_{l,k,v}=g_{n_1}^{(1)}$;

$n_1=n_1+1$;

end for

end for

elseif $s_l \in S_{\text{UCI}}^{(j)}$

if $M > 0$

$\gamma=1$;

else

$\gamma=0$;

end if

$M=M-1$;

for $k=0$ to $\bar{N}_{\text{UCI}}^{\text{symbol}}+\gamma-1$

for $v=0$ to Q_m-1

$\bar{g}_{l,k,v}=g_{n_1}^{(1)}$;

$n_1=n_1+1$;

end for

end for

for $k=\bar{N}_{\text{UCI}}^{\text{symbol}}+\gamma$ to $N_{\text{UCI}}^{\text{symbol}}-1$

for $v=0$ to Q_m-1

$\bar{g}_{l,k,v}=g_{n_2}^{(2)}$;

$n_2=n_2+1$;

end for

end for

else

for $k=0$ to $N_{\text{UCI}}^{\text{symbol}}-1$

for $v=0$ to Q_m-1

```

 $\bar{g}_{l,k,v} = g_{n_2}^{(2)} ;$ 
 $n_2 = n_2 + 1 ;$ 
end for
end for
end if
end for
Set  $n = 0$ 
for  $l = 0$  to  $N_{\text{symb, UCI}}^{\text{PUCCH}} - 1$ 
  for  $k = 0$  to  $N_{\text{UCI}}^{\text{symbol}} - 1$ 
    for  $v = 0$  to  $Q_m - 1$ 
       $g_n = \bar{g}_{l,k,v} ;$ 
       $n = n + 1 ;$ 
    end for
  end for
end for

```

6.3.2 Uplink control information on PUSCH

6.3.2.1 UCI bit sequence generation

6.3.2.1.1 HARQ-ACK

If HARQ-ACK bits are transmitted on a PUSCH, the UCI bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is determined as follows:

- If UCI is transmitted on PUSCH without UL-SCH and the UCI includes CSI part 1 without CSI part 2,
- if there is no HARQ-ACK bit given by Subclause 9.1 of [5, TS 38.213], set $a_0 = 0$, $a_1 = 0$, and $A = 2$;
- if there is only one HARQ-ACK bit \tilde{o}_0^{ACK} given by Subclause 9.1 of [5, TS 38.213], set $a_0 = \tilde{o}_0^{\text{ACK}}$, $a_1 = 0$, and $A = 2$;
- otherwise, set $a_i = \tilde{o}_i^{\text{ACK}}$ for $i = 0, 1, \dots, O^{\text{ACK}} - 1$ and $A = O^{\text{ACK}}$, where the HARQ-ACK bit sequence $\tilde{o}_0^{\text{ACK}}, \{\tilde{o}_1^{\text{ACK}}, \dots, \tilde{o}_{O^{\text{ACK}}-1}^{\text{ACK}}\}$ is given by Subclause 9.1 of [5, TS 38.213].

6.3.2.1.2 CSI

The bitwidth for PMI of *CodebookType=TypeII* is provided in Tables 6.3.2.1.2-1, where the values of (N_1, N_2) , (O_1, O_2) , L , N_{PSK} , M_1 , M_2 , and $K^{(2)}$ are given by Subclause 5.2.2.2.3 in [6, TS 38.214].

Table 6.3.2.1.2-1: PMI of CodebookType= TypeII

	Information fields for wideband PMI						Information fields per subband PMI			
	$i_{1,1}$	$i_{1,2}$	$i_{1,3,1}$	$i_{1,4,1}$	$i_{1,3,2}$	$i_{1,4,2}$	$i_{2,1,1}$	$i_{2,1,2}$	$i_{2,2,1}$	$i_{2,2,2}$
Rank=1 SBAmp off	$\lceil \log_2(O_1 O_2) \rceil$	$\lceil \log_2 \binom{N_1 N_2}{L} \rceil$	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	N/A	N/A	$(M_1-1) \cdot \log_2 N_{\text{PSK}}$	N/A	N/A	N/A
Rank=2 SBAmp off	$\lceil \log_2(O_1 O_2) \rceil$	$\lceil \log_2 \binom{N_1 N_2}{L} \rceil$	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	$(M_1-1) \cdot \log_2 N_{\text{PSK}}$	$(M_2-1) \cdot \log_2 N_{\text{PSK}}$	N/A	N/A
Rank=1 SBAmp on	$\lceil \log_2(O_1 O_2) \rceil$	$\lceil \log_2 \binom{N_1 N_2}{L} \rceil$	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	N/A	N/A	$\min(M_1, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_1 - \min(M_1, K^{(2)}))$	N/A	$\min(M_1, K^{(2)}) - 1$	N/A
Rank=2 SBAmp on	$\lceil \log_2(O_1 O_2) \rceil$	$\lceil \log_2 \binom{N_1 N_2}{L} \rceil$	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	$\min(M_1, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_1 - \min(M_1, K^{(2)}))$	$\min(M_2, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_2 - \min(M_2, K^{(2)}))$	$\min(M_1, K^{(2)}) - 1$	$\min(M_2, K^{(2)}) - 1$

The bitwidth for PMI of *CodebookType= TypeII-PortSelection* is provided in Tables 6.3.2.1.2-2, where the values of $P_{\text{CSI-RS}}$, d , L , N_{PSK} , M_1 , M_2 , and $K^{(2)}$ are given by Subclause 5.2.2.2.4 in [6, TS 38.214].

Table 6.3.2.1.2-2: PMI of CodebookType= TypeII-PortSelection

	Information fields for wideband PMI						Information fields per subband PMI			
	$i_{1,1}$	$i_{1,3,1}$	$i_{1,4,1}$	$i_{1,3,2}$	$i_{1,4,2}$	$i_{2,1,1}$	$i_{2,1,2}$	$i_{2,2,1}$	$i_{2,2,2}$	
Rank=1 SBAmp off	$\lceil \log_2 \lceil \frac{P_{\text{CSI-RS}}}{2d} \rceil \rceil$	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	N/A	N/A	$(M_1-1) \cdot \log_2 N_{\text{PSK}}$	N/A	N/A	N/A	
Rank=2 SBAmp off	$\lceil \log_2 \lceil \frac{P_{\text{CSI-RS}}}{2d} \rceil \rceil$	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	$(M_1-1) \cdot \log_2 N_{\text{PSK}}$	$(M_2-1) \cdot \log_2 N_{\text{PSK}}$	N/A	N/A	
Rank=1 SBAmp on	$\lceil \log_2 \lceil \frac{P_{\text{CSI-RS}}}{2d} \rceil \rceil$	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	N/A	N/A	$\min(M_1, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_1 - \min(M_1, K^{(2)}))$	N/A	$\min(M_1, K^{(2)}) - 1$	N/A	
Rank=2 SBAmp on	$\lceil \log_2 \lceil \frac{P_{\text{CSI-RS}}}{2d} \rceil \rceil$	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	$\lceil \log_2(2L) \rceil$	$3 2L-1 $	$\min(M_1, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_1 - \min(M_1, K^{(2)}))$	$\min(M_2, K^{(2)}) \cdot \log_2 N_{\text{PSK}} - \log_2 N_{\text{PSK}} + 2 \cdot (M_2 - \min(M_2, K^{(2)}))$	$\min(M_1, K^{(2)}) - 1$	$\min(M_2, K^{(2)}) - 1$	

For CSI on PUSCH, two UCI bit sequences are generated, $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ and

$a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$. The CSI fields of all CSI reports, in the order from upper part to lower part in Table

6.3.2.1.2-6, are mapped to the UCI bit sequence $a_0^{(1)}, a_1^{(1)}, a_2^{(1)}, a_3^{(1)}, \dots, a_{A^{(1)}-1}^{(1)}$ starting with $a_0^{(1)}$. The CSI fields of all CSI reports, in the order from upper part to lower part in Table 6.3.2.1.2-7, are mapped to the UCI bit sequence $a_0^{(2)}, a_1^{(2)}, a_2^{(2)}, a_3^{(2)}, \dots, a_{A^{(2)}-1}^{(2)}$ starting with $a_0^{(2)}$.

Table 6.3.2.1.2-3: Mapping order of CSI fields of one CSI report, CSI part 1

CSI report number	CSI fields
CSI report #n CSI part 1	CRI or SSB index as in Tables 6.3.1.1.2-6, if reported
	Rank Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	Wideband CQI as in Tables 6.3.1.1.2-3/4/5, if reported
	Subband differential CQI for the first TB as in Tables 6.3.1.1.2-3/4/5, if reported
	Indicator of the number of non-zero wideband amplitude coefficients M_l for layer l as in Table 6.3.1.1.2-5, if reported
	RSRP as in Table 6.3.1.1.2-6, if reported
	Differential RSRP as in Table 6.3.1.1.2-6, if reported

Table 6.3.2.1.2-4: Mapping order of CSI fields of one CSI report, CSI part 2 wideband

CSI report number	CSI fields
CSI report #n CSI part 2 wideband	Wideband CQI for the second TB as in Tables 6.3.1.1.2-3/4/5, if present and reported
	Layer Indicator as in Tables 6.3.1.1.2-3/4/5, if reported
	PMI wideband information fields X_1 , from left to right as in Tables 6.3.1.1.2-1/2 or 6.3.2.1.2-1/2, if reported
	PMI wideband information fields X_2 , from left to right as in Tables 6.3.1.1.2-1/2 or 6.3.2.1.2-1/2, if PMI-FormatIndicator= widebandPMI and if reported

Table 6.3.2.1.2-5: Mapping order of CSI fields of one CSI report, CSI part 2 subband

CSI report #n Part 2 subband	Subband differential CQI for the second TB of all even subbands with increasing order of subband number, as in Tables 6.3.1.1.2-3/4/5, if CQI-FormatIndicator=subbandCQI and if reported
	PMI subband information fields X_2 of all even subbands with increasing order of subband number, from left to right as in Tables 6.3.1.1.2-1/2 or 6.3.2.1.2-1/2, if PMI-FormatIndicator= subbandPMI and if reported
	Subband differential CQI for the second TB of all odd subbands with increasing order of subband number, as in Tables 6.3.1.1.2-3/4/5, if CQI-FormatIndicator=subbandCQI and if reported
	PMI subband information fields X_2 of all odd subbands with increasing order of subband number, from left to right as in Tables 6.3.1.1.2-1/2 or 6.3.2.1.2-1/2, if PMI-FormatIndicator= subbandPMI and if reported

Table 6.3.2.1.2-6: Mapping order of CSI reports to UCI bit sequence with two-part CSI report(s)

UCI bit sequence	CSI report number
$a_0^{(1)}$	CSI part 1 of CSI report #1 as in Table 6.3.2.1.2-3
$a_1^{(1)}$	CSI part 1 of CSI report #2 as in Table 6.3.2.1.2-3
$a_2^{(1)}$...
$a_3^{(1)}$	CSI part 1 of CSI report #n as in Table 6.3.2.1.2-3
\vdots	
$a_{A^{(1)}-1}^{(1)}$	

Table 6.3.2.1.2-7: Mapping order of CSI reports to UCI bit sequence with two-part CSI report(s)

UCI bit sequence	CSI report number
$a_0^{(2)}$ $a_1^{(2)}$ $a_2^{(2)}$ $a_3^{(2)}$ ⋮ $a_{A^{(2)}-1}^{(2)}$	CSI report #1, CSI part 2 wideband, as in Table 6.3.2.1.2-4 if CSI part 2 exists for CSI report #1
	CSI report #2, CSI part 2 wideband, as in Table 6.3.2.1.2-4 if CSI part 2 exists for CSI report #2
	...
	CSI report #n, CSI part 2 wideband, as in Table 6.3.2.1.2-4 if CSI part 2 exists for CSI report #n
	CSI report #1, CSI part 2 subband, as in Table 6.3.2.1.2-5 if CSI part 2 exists for CSI report #1
	CSI report #2, CSI part 2 subband, as in Table 6.3.2.1.2-5 if CSI part 2 exists for CSI report #2
	...
	CSI report #n, CSI part 2 subband, as in Table 6.3.2.1.2-5 if CSI part 2 exists for CSI report #n

6.3.2.2 Code block segmentation and CRC attachment

Denote the bits of the payload by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, where A is the payload size. The procedure in 6.3.2.2.1 applies for $A \geq 12$ and the procedure in Subclause 6.3.2.2.2 applies for $A \leq 11$.

6.3.2.2.1 UCI encoded by Polar code

Code block segmentation and CRC attachment is performed according to Subclause 6.3.1.2.1.

6.3.2.2.2 UCI encoded by channel coding of small block lengths

The procedure in Subclause 6.3.1.2.2 applies.

6.3.2.3 Channel coding of UCI

6.3.2.3.1 UCI encoded by Polar code

Channel coding is performed according to Subclause 6.3.1.3.1.

6.3.2.3.2 UCI encoded by channel coding of small block lengths

Information bits are delivered to the channel coding block. They are denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits.

The information bits are encoded according to Subclause 5.3.3.

After encoding the bits are denoted by $d_0, d_1, d_2, d_3, \dots, d_{N-1}$, where N is the number of coded bits.

6.3.2.4 Rate matching

6.3.2.4.1 UCI encoded by Polar code

6.3.2.4.1.1 HARQ-ACK

For HARQ-ACK transmission on PUSCH with UL-SCH, the number of coded modulation symbols per layer for HARQ-ACK transmission, denoted as Q_{ACK} , is determined as follows:

$$Q_{\text{ACK}} = \min \left\{ \left[\frac{\left(O_{\text{ACK}} + L_{\text{ACK}} \right) \cdot \beta_{\text{offset}}^{\text{PUSCH}} \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{\text{sc}}^{\text{UCI}}(l)}{\sum_{r=0}^{C_{\text{UL-SCH}} - 1} K_r} \right], \left[\alpha \cdot \sum_{l=l_0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{\text{sc}}^{\text{UCI}}(l) \right] \right\}$$

where

- O_{ACK} is the number of HARQ-ACK bits;
- L_{ACK} is the number of CRC bits for HARQ-ACK;
- $\beta_{\text{offset}}^{\text{PUSCH}} = \beta_{\text{offset}}^{\text{HARQ-ACK}}$;
- $C_{\text{UL-SCH}}$ is the number of code blocks for UL-SCH of the PUSCH transmission;
- if the DCI format scheduling the PUSCH transmission includes a CBGT field indicating that the UE shall not transmit the r -th code block, $K_r = 0$; otherwise, K_r is the r -th code block size for UL-SCH of the PUSCH transmission;
- $M_{\text{sc}}^{\text{PUSCH}}$ is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{\text{sc}}^{\text{PT-RS}}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- $M_{\text{sc}}^{\text{UCI}}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}} - 1$, in the PUSCH transmission and $N_{\text{symb,all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;
 - for any OFDM symbol that carries DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = 0$;
 - for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = M_{\text{sc}}^{\text{PUSCH}} - M_{\text{sc}}^{\text{PT-RS}}(l)$;
- $\alpha \in \{0.5, 0.65, 0.8, 1\}$ is configured by higher layer parameter *uci-on-pusch-scaling*;
- l_0 is the symbol index of the first OFDM symbol that does not carry DMRS of the PUSCH, after the first DMRS symbol(s), in the PUSCH transmission.

For HARQ-ACK transmission on PUSCH without UL-SCH, the number of coded modulation symbols per layer for HARQ-ACK transmission, denoted as Q_{ACK} , is determined as follows:

$$Q_{\text{ACK}} = \min \left\{ \left[\frac{\left(O_{\text{ACK}} + L_{\text{ACK}} \right) \cdot \beta_{\text{offset}}^{\text{PUSCH}} \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{\text{sc}}^{\text{UCI}}(l)}{O_{\text{CSI}}} \right], \sum_{l=l_0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{\text{sc}}^{\text{UCI}}(l) \right\}$$

where

- O_{ACK} is the number of HARQ-ACK bits;
- L_{ACK} is the number of CRC bits for HARQ-ACK bits;
- O_{CSI} is the number of bits for CSI part 1;
- $\beta_{\text{offset}}^{\text{PUSCH}} = \beta_{\text{offset}}^{\text{HARQ-ACK}} / \beta_{\text{offset}}^{\text{CSI-part1}}$;
- $M_{\text{sc}}^{\text{PUSCH}}$ is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{\text{sc}}^{\text{PT-RS}}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- $M_{\text{sc}}^{\text{UCI}}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb},\text{all}}^{\text{PUSCH}}-1$, in the PUSCH transmission and $N_{\text{symb},\text{all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;
 - for any OFDM symbol that carries DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = 0$;
 - for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = M_{\text{sc}}^{\text{PUSCH}} - M_{\text{sc}}^{\text{PT-RS}}(l)$;
- l_0 is the symbol index of the first OFDM symbol that does not carry DMRS of the PUSCH, after the first DMRS symbol(s), in the PUSCH transmission.

The input bit sequence to rate matching is $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$ where r is the code block number, and N_r is the number of coded bits in code block number r .

Rate matching is performed according to Subclause 5.4.1 by setting $I_{\text{BIL}}=1$ and the rate matching output sequence length to $E_r = \lfloor E_{\text{UCI}} / C_{\text{UCI}} \rfloor$, where

- C_{UCI} is the number of code blocks for UCI determined according to Subclause 5.2.1;
- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH;
- $E_{\text{UCI}} = N_L \cdot Q_{\text{ACK}} \cdot Q_m$.

The output bit sequence after rate matching is denoted as $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$ where E_r is the length of rate matching output sequence in code block number r .

6.3.2.4.1.2CSI part 1

For CSI part 1 transmission on PUSCH with UL-SCH, the number of coded modulation symbols per layer for CSI part 1 transmission, denoted as $Q'_{\text{CSI-part1}}$, is determined as follows:

$$Q'_{\text{CSI-1}} = \min \left(\left[\frac{\left(O_{\text{CSI-1}} + L_{\text{CSI-1}} \right) \cdot \beta_{\text{offset}}^{\text{PUSCH}} \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{\text{sc}}^{\text{UCI}}(l)}{C_{\text{UL-SCH}} - 1 \sum_{r=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} K_r} \right], \lceil \alpha \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{\text{sc}}^{\text{UCI}}(l) \rceil - Q'_{\text{ACK}} \right)$$

where

- $O_{\text{CSI-1}}$ is the number of bits for CSI part 1;
- $L_{\text{CSI-1}}$ is the number of CRC bits for CSI part 1;
- $\beta_{\text{offset}}^{\text{PUSCH}} = \beta_{\text{offset}}^{\text{CSI-part1}}$;
- $C_{\text{UL-SCH}}$ is the number of code blocks for UL-SCH of the PUSCH transmission;
- if the DCI format scheduling the PUSCH transmission includes a CBGTI field indicating that the UE shall not transmit the r -th code block, $K_r = 0$; otherwise, K_r is the r -th code block size for UL-SCH of the PUSCH transmission;
- $M_{\text{sc}}^{\text{PUSCH}}$ is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{\text{sc}}^{\text{PT-RS}}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- Q'_{ACK} is the number of coded modulation symbols per layer for HARQ-ACK transmitted on the PUSCH if number of HARQ-ACK information bits is more than 2, and $Q'_{\text{ACK}} = \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} \bar{M}_{\text{sc, rvd}}^{\text{ACK}}(l)$ if the number of HARQ-ACK information bits is no more than 2 bits, where $\bar{M}_{\text{sc, rvd}}^{\text{ACK}}(l)$ is the number of reserved resource elements for potential HARQ-ACK transmission in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}} - 1$, in the PUSCH transmission, defined in Subclause 6.2.7;
- $M_{\text{sc}}^{\text{UCI}}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l=0,1,2,\dots,N_{\text{symb,all}}^{\text{PUSCH}} - 1$, in the PUSCH transmission and $N_{\text{symb,all}}^{\text{PUSCH}}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;
 - for any OFDM symbol that carries DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = 0$;
 - for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{\text{sc}}^{\text{UCI}}(l) = M_{\text{sc}}^{\text{PUSCH}} - M_{\text{sc}}^{\text{PT-RS}}(l)$;
- $\alpha \in \{0.5, 0.65, 0.8, 1\}$ is configured by higher layer parameter *uci-on-pusch-scaling*.

The input bit sequence to rate matching is $d_{r_0}, d_{r_1}, d_{r_2}, d_{r_3}, \dots, d_{r(N_r - 1)}$ where r is the code block number, and N_r is the number of coded bits in code block number r .

Rate matching is performed according to Subclause 5.4.1 by setting $I_{BL}=1$ and the rate matching output sequence length to $E_r = \lceil E_{UCI} / C_{UCI} \rceil$, where

- C_{UCI} is the number of code blocks for UCI determined according to Subclause 5.2.1;
- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH;
- $E_{UCI} = N_L \cdot Q'_{CSI,1} \cdot Q_m$.

The output bit sequence after rate matching is denoted as $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$ where E_r is the length of rate matching output sequence in code block number r .

6.3.2.4.1.3 CSI part 2

For CSI part 2 transmission on PUSCH with UL-SCH, the number of coded modulation symbols per layer for CSI part 2 transmission, denoted as $Q'_{CSI\text{-part}2}$, is determined as follows:

$$Q'_{CSI\text{-}2} = \min \left(\left\lceil \frac{\left(O_{CSI\text{-}2} + L_{CSI\text{-}2} \right) \cdot \beta_{\text{offset}}^{\text{PUSCH}} \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{sc}^{\text{UCI}}(l) }{ \sum_{r=0}^{C_{UL\text{-}SCH} - 1} K_r } \right\rceil, \left\lceil \alpha \cdot \sum_{l=0}^{N_{\text{symb,all}}^{\text{PUSCH}} - 1} M_{sc}^{\text{UCI}}(l) \right\rceil - Q'_{ACK} - Q'_{CSI\text{-}1} \right)$$

where

- $O_{CSI\text{-}2}$ is the number of bits for CSI part 2;
- $L_{CSI\text{-}2}$ is the number of CRC bits for CSI part 2;
- $\beta_{\text{offset}}^{\text{PUSCH}} = \beta_{\text{offset}}^{\text{CSI-part}2}$;
- $C_{UL\text{-}SCH}$ is the number of code blocks for UL-SCH of the PUSCH transmission;
- if the DCI format scheduling the PUSCH transmission includes a CBGTI field indicating that the UE shall not transmit the r -th code block, $K_r = 0$; otherwise, K_r is the r -th code block size for UL-SCH of the PUSCH transmission;
- M_{sc}^{PUSCH} is the scheduled bandwidth of the PUSCH transmission, expressed as a number of subcarriers;
- $M_{sc}^{\text{PT-RS}}(l)$ is the number of subcarriers in OFDM symbol l that carries PTRS, in the PUSCH transmission;
- Q'_{ACK} is the number of coded modulation symbols per layer for HARQ-ACK transmitted on the PUSCH if number of HARQ-ACK information bits is more than 2, and $Q'_{ACK} = 0$ if the number of HARQ-ACK information bits is 1 or 2 bits;
- $Q'_{CSI\text{-}1}$ is the number of coded modulation symbols per layer for CSI part 1 transmitted on the PUSCH;

- $M_{sc}^{UCI}(l)$ is the number of resource elements that can be used for transmission of UCI in OFDM symbol l , for $l=0,1,2,\dots,N_{symb,all}^{PUSCH}-1$, in the PUSCH transmission and $N_{symb,all}^{PUSCH}$ is the total number of OFDM symbols of the PUSCH, including all OFDM symbols used for DMRS;
- for any OFDM symbol that carries DMRS of the PUSCH, $M_{sc}^{UCI}(l)=0$;
- for any OFDM symbol that does not carry DMRS of the PUSCH, $M_{sc}^{UCI}(l)=M_{sc}^{PUSCH}-M_{sc}^{PT-RS}(l)$.
- $\alpha \in \{0.5, 0.65, 0.8, 1\}$ is configured by higher layer parameter *uci-on-pusch-scaling*.

The input bit sequence to rate matching is $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r(N_r-1)}$ where r is the code block number, and N_r is the number of coded bits in code block number r .

Rate matching is performed according to Subclause 5.4.1 by setting $I_{BL}=1$ and the rate matching output sequence length to $E_r = \lfloor E_{UCI}/C_{UCI} \rfloor$, where

- C_{UCI} is the number of code blocks for UCI determined according to Subclause 5.2.1;
- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH;
- $E_{UCI} = N_L \cdot Q'_{CSI,2} \cdot Q_m$.

The output bit sequence after rate matching is denoted as $f_{r0}, f_{r1}, f_{r2}, \dots, f_{r(E_r-1)}$ where E_r is the length of rate matching output sequence in code block number r .

6.3.2.4.2 UCI encoded by channel coding of small block lengths

6.3.2.4.2.1 HARQ-ACK

For HARQ-ACK transmission on PUSCH with UL-SCH, the number of coded modulation symbols per layer for HARQ-ACK transmission, denoted as Q'_{ACK} , is determined according to Subclause 6.3.2.4.1.1, by setting the number of CRC bits $L=0$.

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$.

Rate matching is performed according to Subclause 5.4.3, by setting the rate matching output sequence length $E = N_L \cdot Q'_{ACK} \cdot Q_m$, where

- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

6.3.2.4.2.2 CSI part 1

For CSI part 1 transmission on PUSCH with UL-SCH, the number of coded modulation symbols per layer for CSI part 1 transmission, denoted as $Q'_{CSI,1}$, is determined according to Subclause 6.3.2.4.1.2, by setting the number of CRC bits $L=0$.

Rate matching is performed according to Subclause 5.4.3, by setting the rate matching output sequence length $E=N_L \cdot Q_{\text{CSI},1} \cdot Q_m$, where

- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

6.3.2.4.2.3 CSI part 2

For CSI part 2 transmission on PUSCH with UL-SCH, the number of coded modulation symbols per layer for CSI part 2 transmission, denoted as $Q_{\text{CSI},2}$, is determined according to Subclause 6.3.2.4.1.3, by setting the number of CRC bits $L=0$.

Rate matching is performed according to Subclause 5.4.3, by setting the rate matching output sequence length

$$E=N_L \cdot Q_{\text{CSI},2} \cdot Q_m, \text{ where}$$

- N_L is the number of transmission layers of the PUSCH;
- Q_m is the modulation order of the PUSCH.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

6.3.2.5 Code block concatenation

Code block concatenation is performed according to Subclause 6.3.1.5, except that the values of E_{UCI} and C_{UCI} given in Subclause 6.3.2.4.1.

6.3.2.6 Multiplexing of coded UCI bits to PUSCH

The coded UCI bits are multiplexed onto PUSCH according to the procedures in Subclause 6.2.7.

7 Downlink transport channels and control information

7.1 Broadcast channel

Data arrives to the coding unit in the form of a maximum of one transport block every 80ms. The following coding steps can be identified:

- Payload generation
- Scrambling
- Transport block CRC attachment
- Channel coding
- Rate matching

7.1.1 PBCH payload generation

Denote the bits in a transport block delivered to layer 1 by $\bar{a}_0, \bar{a}_1, \bar{a}_2, \bar{a}_3, \dots, \bar{a}_{\bar{A}-1}$, where \bar{A} is the payload size generated by higher layers. The lowest order information bit \bar{a}_0 is mapped to the most significant bit of the transport block as defined in Subclause [6.1.4] of [8, TS 38.321].

Generate the following additional timing related PBCH payload bits $\bar{a}_{\bar{A}}, \bar{a}_{\bar{A}+1}, \bar{a}_{\bar{A}+2}, \bar{a}_{\bar{A}+3}, \dots, \bar{a}_{\bar{A}+7}$, where:

- $\bar{a}_{\bar{A}}, \bar{a}_{\bar{A}+1}, \bar{a}_{\bar{A}+2}, \bar{a}_{\bar{A}+3}$ are the 4th, 3rd, 2nd, and 1st LSB of SFN, respectively;

- $\bar{a}_{\bar{A}+4}$ is the half radio frame bit \bar{a}_{HRF} ;

- if $L_{\text{SSB}} = 64$

- $\bar{a}_{\bar{A}+5}, \bar{a}_{\bar{A}+6}, \bar{a}_{\bar{A}+7}$ are the 6th, 5th, and 4th bits of SS/PBCH block index, respectively.

else

- $\bar{a}_{\bar{A}+5}$ is the MSB of k_0 as defined in Subclause 7.4.3.1 of [4, TS 38.211].

- $\bar{a}_{\bar{A}+6}, \bar{a}_{\bar{A}+7}$ are reserved.

end if

Let $A = \bar{A} + 8$; $j_{\text{SFN}} = 0$; $j_{\text{HRF}} = 10$; $j_{\text{SSB}} = 11$; $j_{\text{other}} = 14$;

for $i = 0$ to $A - 1$

- if \bar{a}_i is an SFN bit

- $a_{G(j_{\text{SFN}})} = \bar{a}_i$;

- $j_{\text{SFN}} = j_{\text{SFN}} + 1$;

- elseif \bar{a}_i is the half radio frame bit

- $a_{G(j_{\text{HRF}})} = \bar{a}_i$

- elseif $\bar{A} + 5 \leq i \leq \bar{A} + 7$

- $a_{G(j_{\text{SSB}})} = \bar{a}_i$;

- $j_{\text{SSB}} = j_{\text{SSB}} + 1$;

else

- $a_{G(j_{\text{Other}})} = \bar{a}_i$;

- $j_{\text{Other}} = j_{\text{Other}} + 1$;

end if

end for

where L_{SSB} is the number of candidate SS/PBCH blocks in a half frame according to Subclause 4.1 of [5, TS38.213], and the value of $G(j)$ is given by Table 7.1.1-1.

Table 7.1.1-1: Value of PBCH payload interleaver pattern $G(j)$

j	$G[j]$														
0	16	4	8	8	24	12	3	16	9	20	14	24	21	28	27
1	23	5	30	9	7	13	2	17	11	21	15	25	22	29	28
2	18	6	10	10	0	14	1	18	12	22	19	26	25	30	29
3	17	7	6	11	5	15	4	19	13	23	20	27	26	31	31

7.1.2 Scrambling

For PBCH transmission in a frame, the bit sequence $a_0, a_1, a_2, a_3, \dots, a_{A-1}$ is scrambled into a bit sequence $a'_0, a'_1, a'_2, a'_3, \dots, a'_{A-1}$, where $a'_i = (a_i + s_i) \bmod 2$ for $i=0,1,\dots,A-1$ and $s_0, s_1, s_2, s_3, \dots, s_{A-1}$ is generated according to the following:

$i=0$;

$j=0$;

while $i < A$

if a_i corresponds to any one of the bits belonging to the SS/PBCH block index, the half radio frame index, and 2nd and 3rd least significant bits of the system frame number

$s_i=0$;

else

$s_i=c(j+vM)$;

$j=j+1$;

end if

$i=i+1$;

end while

The scrambling sequence $c(i)$ is given by Subclause 5.2.1 of [4, TS38.211] and initialized with $c_{\text{init}}=N_{ID}^{\text{cell}}$ at the start of each SFN satisfying $\text{mod}(SFN, 8)=0$; $M=A-3$ for $L=4$ or $L=8$, and $M=A-6$ for $L=64$, where L is the number of candidate SS/PBCH blocks in a half frame according to Subclause 4.1 of [5, TS38.213]; and v is determined according to Table 7.1.2-1 using the 3rd and 2nd LSB of the SFN in which the PBCH is transmitted.

Table 7.1.2-1: Value of v for PBCH scrambling

(3 rd LSB of SFN, 2 nd LSB of SFN)	Value of v
(0, 0)	0
(0, 1)	1
(1, 0)	2
(1, 1)	3

7.1.3 Transport block CRC attachment

Error detection is provided on BCH transport blocks through a Cyclic Redundancy Check (CRC).

The entire transport block is used to calculate the CRC parity bits. The input bit sequence is denoted by $a'_0, a'_1, a'_2, a'_3, \dots, a'_{A-1}$, and the parity bits by $p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the payload size and L is the number of parity bits.

The parity bits are computed and attached to the BCH transport block according to Subclause 5.1 by setting L to 24 bits and using the generator polynomial $g_{\text{CRC24C}}[D]$, resulting in the sequence $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B = A + L$.

The bit sequence $b_0, b_1, b_2, b_3, \dots, b_{B-1}$ is the input bit sequence $c_0, c_1, c_2, c_3, \dots, c_{K-1}$ to the channel encoder, where $c_i = b_i$ for $i = 0, 1, \dots, B-1$ and $K = B$.

7.1.4 Channel coding

Information bits are delivered to the channel coding block. They are denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits, and they are encoded via Polar coding according to Subclause 5.3.1, by setting $n_{\max} = 9$, $I_{IL} = 1$, $n_{PC} = 0$, and $n_{PC}^{wm} = 0$.

After encoding the bits are denoted by $d_0, d_1, d_2, d_3, \dots, d_{N-1}$, where N is the number of coded bits.

7.1.5 Rate matching

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$.

The rate matching output sequence length $E = 864$.

Rate matching is performed according to Subclause 5.4.1 by setting $I_{BL} = 0$.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

7.2 Downlink shared channel and paging channel

7.2.1 Transport block CRC attachment

Error detection is provided on each transport block through a Cyclic Redundancy Check (CRC).

The entire transport block is used to calculate the CRC parity bits. Denote the bits in a transport block delivered to layer 1 by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, and the parity bits by $p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the payload size and L is the number of parity bits. The lowest order information bit a_0 is mapped to the most significant bit of the transport block as defined in Subclause x.x of [TS38.321].

The parity bits are computed and attached to the DL-SCH transport block according to Subclause 5.1, by setting L to 24 bits and using the generator polynomial $g_{\text{CRC24A}}[D]$ if $A > 3824$; and by setting L to 16 bits and using the generator polynomial $g_{\text{CRC16}}[D]$ otherwise.

The bits after CRC attachment are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$, where $B = A + L$.

7.2.2 LDPC base graph selection

For initial transmission of a transport block with coding rate R indicated by the MCS index according to Subclause 5.1.3.1 in [6, TS 38.214] and subsequent re-transmission of the same transport block, each code block of the transport block is encoded with either LDPC base graph 1 or 2 according to the following:

- if $A \leq 292$, or if $A \leq 3824$ and $R \leq 0.67$, or if $R \leq 0.25$, LDPC base graph 2 is used;

- otherwise, LDPC base graph 1 is used,

where A is the payload size in Subclause 7.2.1.

7.2.3 Code block segmentation and code block CRC attachment

The bits input to the code block segmentation are denoted by $b_0, b_1, b_2, b_3, \dots, b_{B-1}$ where B is the number of bits in the transport block (including CRC).

Code block segmentation and code block CRC attachment are performed according to Subclause 5.2.2.

The bits after code block segmentation are denoted by $c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r|K_r-1|}$, where r is the code block number and K_r is the number of bits for code block number r according to Subclause 5.2.2.

7.2.4 Channel coding

Code blocks are delivered to the channel coding block. The bits in a code block are denoted by

$c_{r0}, c_{r1}, c_{r2}, c_{r3}, \dots, c_{r|K_r-1|}$, where r is the code block number, and K_r is the number of bits in code block number r . The total number of code blocks is denoted by C and each code block is individually LDPC encoded according to Subclause 5.3.2.

After encoding the bits are denoted by $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r|N_r-1|}$, where the values of N_r is given in Subclause 5.3.2.

7.2.5 Rate matching

Coded bits for each code block, denoted as $d_{r0}, d_{r1}, d_{r2}, d_{r3}, \dots, d_{r|N_r-1|}$, are delivered to the rate match block, where r is the code block number, and N_r is the number of encoded bits in code block number r . The total number of code blocks is denoted by C and each code block is individually rate matched according to Subclause 5.4.2 by setting $I_{LBRM}=1$.

After rate matching, the bits are denoted by $f_{r0}, f_{r1}, f_{r2}, f_{r3}, \dots, f_{r|E_r-1|}$, where E_r is the number of rate matched bits for code block number r .

7.2.6 Code block concatenation

The input bit sequence for the code block concatenation block are the sequences $f_{r0}, f_{r1}, f_{r2}, f_{r3}, \dots, f_{r|E_r-1|}$, for $r=0, \dots, C-1$ and where E_r is the number of rate matched bits for the r -th code block.

Code block concatenation is performed according to Subclause 5.5.

The bits after code block concatenation are denoted by $g_0, g_1, g_2, g_3, \dots, g_{G-1}$, where G is the total number of coded bits for transmission.

7.3 Downlink control information

A DCI transports downlink and uplink scheduling information, requests for aperiodic CQI reports, or uplink power control commands for one cell and one RNTI.

The following coding steps can be identified:

- Information element multiplexing
- CRC attachment
- Channel coding

- Rate matching

7.3.1 DCI formats

The DCI formats defined in table 7.3.1-1 are supported.

Table 7.3.1-1: DCI formats

DCI format	Usage
0_0	Scheduling of PUSCH in one cell
0_1	Scheduling of PUSCH in one cell
1_0	Scheduling of PDSCH in one cell
1_1	Scheduling of PDSCH in one cell
2_0	Notifying a group of UEs of the slot format
2_1	Notifying a group of UEs of the PRB(s) and OFDM symbol(s) where UE may assume no transmission is intended for the UE
2_2	Transmission of TPC commands for PUCCH and PUSCH
2_3	Transmission of a group of TPC commands for SRS transmissions by one or more UEs

The fields defined in the DCI formats below are mapped to the information bits a_0 to a_{A-1} as follows.

Each field is mapped in the order in which it appears in the description, including the zero-padding bit(s), if any, with the first field mapped to the lowest order information bit a_0 and each successive field mapped to higher order information bits. The most significant bit of each field is mapped to the lowest order information bit for that field, e.g. the most significant bit of the first field is mapped to a_0 .

If the number of information bits in a DCI format is less than 12 bits, zeros shall be appended to the DCI format until the payload size equals 12.

7.3.1.1 DCI formats for scheduling of PUSCH

7.3.1.1.1 Format 0_0

DCI format 0_0 is used for the scheduling of PUSCH in one cell.

The following information is transmitted by means of the DCI format 0_0 with CRC scrambled by C-RNTI:

- Identifier for DCI formats – 1 bit
- The value of this bit field is always set to 0, indicating an UL DCI format
- Frequency domain resource assignment – $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil$ bits where
 - $N_{\text{RB}}^{\text{UL,BWP}}$ is the size of the initial bandwidth part in case DCI format 0_0 is monitored in the common search space
 - $N_{\text{RB}}^{\text{UL,BWP}}$ is the size of the active bandwidth part in case DCI format 0_0 is monitored in the UE specific search space and satisfying
 - the total number of different DCI sizes monitored per slot is no more than 4, and
 - the total number of different DCI sizes with C-RNTI monitored per slot is no more than 3
 - For PUSCH hopping with resource allocation type 1:
 - $N_{\text{UL,hop}}$ MSB bits are used to indicate the frequency offset according to Subclause 6.3 of [6, TS 38.214], where $N_{\text{UL,hop}}=1$ if the higher layer parameter *Frequency-hopping-offsets-set* contains two

offset values and $N_{\text{UL_hop}} = 2$ if the higher layer parameter *Frequency-hopping-offsets-set* contains four offset values

- $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil - N_{\text{UL_hop}}$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
- For non-PUSCH hopping with resource allocation type 1:
 - $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
- Time domain resource assignment – X bits as defined in Subclause 6.1.2.1 of [6, TS 38.214]
- Frequency hopping flag – 1 bit.
- Modulation and coding scheme – 5 bits as defined in Subclause 6.1.3 of [6, TS 38.214]
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits
- TPC command for scheduled PUSCH – [2] bits as defined in Subclause x.x of [5, TS 38.213]
- UL/SUL indicator – 1 bit for UEs configured with SUL in the cell as defined in Table 7.3.1.1.1-1 and the number of bits for DCI format 1_0 before padding is larger than the number of bits for DCI format 0_0 before padding; 0 bit otherwise.
 - If the UL/SUL indicator is present in DCI format 0_0 and the higher layer parameter *dynamicPUSCHSUL* is set to *Disabled*, the UE ignores the UL/SUL indicator field in DCI format 0_0, and the corresponding PUSCH scheduled by the DCI format 0_0 is for the carrier indicated by the higher layer parameter *pucchCarrierSUL*;
 - If the UL/SUL indicator is not present in DCI format 0_0, the corresponding PUSCH scheduled by the DCI format 0_0 is for the carrier indicated by the higher layer parameter *pucchCarrierSUL*.

The following information is transmitted by means of the DCI format 0_0 with CRC scrambled by TC-RNTI:

- Identifier for DCI formats – 1 bit
 - The value of this bit field is always set to 0, indicating an UL DCI format
- Frequency domain resource assignment – $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil$ bits where
 - $N_{\text{RB}}^{\text{UL,BWP}}$ is the size of the initial bandwidth part in case DCI format 0_0 is monitored in the common search space in CORESET 0
 - $N_{\text{RB}}^{\text{UL,BWP}}$ is the size of the active bandwidth part in case DCI format 0_0 is monitored in the UE specific search space and satisfying
 - the total number of different DCI sizes monitored per slot is no more than 4, and
 - the total number of different DCI sizes with C-RNTI monitored per slot is no more than 3
- For PUSCH hopping with resource allocation type 1:
 - $N_{\text{UL_hop}}$ MSB bits are used to indicate the frequency offset according to Subclause 6.3 of [6, TS 38.214], where $N_{\text{UL_hop}} = 1$ if $N_{\text{RB}}^{\text{UL,BWP}} < 50$ and $N_{\text{UL_hop}} = 2$ otherwise

- $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil - N_{\text{UL_hop}}$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
- For non-PUSCH hopping with resource allocation type 1:
 - $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
- Time domain resource assignment – X bits as defined in Subclause 6.1.2.1 of [6, TS 38.214]
- Frequency hopping flag – 1 bit.
- Modulation and coding scheme – 5 bits as defined in Subclause 6.1.3 of [6, TS 38.214], using Table 5.1.3.1-1
- New data indicator – 1 bit, reserved
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits, reserved
- TPC command for scheduled PUSCH – [2] bits as defined in Subclause x.x of [5, TS 38.213]
- UL/SUL indicator – 1 bit if the cell has two ULs and the number of bits for DCI format 1_0 before padding is larger than the number of bits for DCI format 0_0 before padding; 0 bit otherwise.
 - If 1 bit, reserved, and the corresponding PUSCH is always on the same UL carrier as the previous transmission of the same TB

The following information is transmitted by means of the DCI format 0_0 with CRC scrambled by CS-RNTI:

- XXX – x bit

If DCI format 0_0 is monitored in common search space and if the number of information bits in the DCI format 0_0 prior to padding is less than the payload size of the DCI format 1_0 monitored in common search space for scheduling the same serving cell, zeros shall be appended to the DCI format 0_0 until the payload size equals that of the DCI format 1_0.

If DCI format 0_0 is monitored in common search space and if the number of information bits in the DCI format 0_0 prior to padding is larger than the payload size of the DCI format 1_0 monitored in common search space for scheduling the same serving cell, the bitwidth of the frequency domain resource allocation field in the DCI format 0_0 is reduced such that the size of DCI format 0_0 equals to the size of the DCI format 1_0..

Table 7.3.1.1.1-1: UL/SUL indicator

Value of UL/SUL indicator	Uplink
0	The non-supplementary uplink
1	The supplementary uplink

Table 7.3.1.1.1-2: Redundancy version

Value of the Redundancy version field	Value of rV_{id} to be applied
00	0
01	1
10	2
11	3

7.3.1.1.2 Format 0_1

DCI format 0_1 is used for the scheduling of PUSCH in one cell.

The following information is transmitted by means of the DCI format 0_1 with CRC scrambled by C-RNTI:

- Carrier indicator – 0 or 3 bits, as defined in Subclause x.x of [5, TS38.213].
- UL/SUL indicator – 0 bit for UEs not configured with SUL in the cell or UEs configured with SUL in the cell but only PUCCH carrier in the cell is configured for PUSCH transmission; 1 bit for UEs configured with SUL in the cell as defined in Table 7.3.1.1.1-1.
- Identifier for DCI formats – 1 bit
 - The value of this bit field is always set to 0, indicating an UL DCI format
- Bandwidth part indicator – 0, 1 or 2 bits as defined in Table 7.3.1.1.2-1. The bitwidth for this field is determined as $\lceil \log_2(n_{\text{BWP}}) \rceil$ bits, where
 - $n_{\text{BWP}} = n_{\text{BWP,RRC}} + 1$ if the higher layer parameter *BandwidthPart-Config* configures up to 3 bandwidth parts and the initial bandwidth part is not included in higher layer parameter *BandwidthPart-Config*;
 - otherwise $n_{\text{BWP}} = n_{\text{BWP,RRC}}$;
 - $n_{\text{BWP,RRC}}$ is the number of configured bandwidth parts according to higher layer parameter *BandwidthPart-Config*.
- Frequency domain resource assignment – number of bits determined by the following, where $N_{\text{RB}}^{\text{UL,BWP}}$ is the size of the active bandwidth part:
 - N_{RB} bits if only resource allocation type 0 is configured, where N_{RB} is defined in Subclause 6.1.2.2.1 of [6, TS 38.214],
 - $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil$ bits if only resource allocation type 1 is configured, or $\max(\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil, N_{\text{RB}}) + 1$ bits if both resource allocation type 0 and 1 are configured.
- If both resource allocation type 0 and 1 are configured, the MSB bit is used to indicate resource allocation type 0 or resource allocation type 1, where the bit value of 0 indicates resource allocation type 0 and the bit value of 1 indicates resource allocation type 1.
- For resource allocation type 0, the N_{RB} LSBs provide the resource allocation as defined in Subclause 6.1.2.2.1 of [6, TS 38.214].
- For resource allocation type 1, the $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil - N_{\text{UL,hop}}$ LSBs provide the resource allocation as follows:
 - For PUSCH hopping with resource allocation type 1:
 - $N_{\text{UL,hop}}$ MSB bits are used to indicate the frequency offset according to Subclause 6.3 of [6, TS 38.214], where $N_{\text{UL,hop}} = 1$ if the higher layer parameter *Frequency-hopping-offsets-set* contains two offset values and $N_{\text{UL,hop}} = 2$ if the higher layer parameter *Frequency-hopping-offsets-set* contains four offset values
 - $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil - N_{\text{UL,hop}}$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
 - For non-PUSCH hopping with resource allocation type 1:

- $\lceil \log_2(N_{\text{RB}}^{\text{UL,BWP}}(N_{\text{RB}}^{\text{UL,BWP}}+1)/2) \rceil$ bits provides the frequency domain resource allocation according to Subclause 6.1.2.2.2 of [6, TS 38.214]
- Time domain resource assignment – 0, 1, 2, 3, or 4 bits as defined in Subclause 6.1.2.1 of [6, TS38.214]. The bitwidth for this field is determined as $\lceil \log_2(I) \rceil$ bits, where I the number of entries in the higher layer parameter *pusch-AllocationList*.
- VRB-to-PRB mapping – 0 or 1 bit:
 - 0 bit if only resource allocation type 0 is configured or if *PUSCH-tp=Enabled*;
 - 1 bit according to Table 7.3.1.1.2-33 otherwise, only applicable to resource allocation type 1, as defined in Subclause 6.3.1.7 of [4, TS 38.211].
- Frequency hopping flag – 0 or 1 bit:
 - 0 bit if only resource allocation type 0 is configured;
 - 1 bit otherwise, only applicable to resource allocation type 1, as defined in Subclause 6.3 of [6, TS 38.214].
- Modulation and coding scheme – 5 bits as defined in Subclause x.x of [6, TS 38.214]
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits
- 1st downlink assignment index – 1 or 2 bits:
 - 1 bit for semi-static HARQ-ACK codebook;
 - 2 bits for dynamic HARQ-ACK codebook with single HARQ-ACK codebook.
- 2nd downlink assignment index – 0 or 2 bits:
 - 2 bits for dynamic HARQ-ACK codebook with two HARQ-ACK sub-codebooks;
 - 0 bit otherwise.
- TPC command for scheduled PUSCH – 2 bits as defined in Subclause 7.1.1 of [5, TS38.213]
- SRS resource indicator – $\lceil \log_2 \left(\sum_{k=1}^{\min[L_{\max}, N_{\text{SRS}}]} \binom{N_{\text{SRS}}}{k} \right) \rceil$ or $\lceil \log_2(N_{\text{SRS}}) \rceil$ bits, where N_{SRS} is the number of configured SRS resources in the SRS resource set associated with the higher layer parameter *SRS-SetUse* of value '*CodeBook*' or '*NonCodeBook*', and L_{\max} is the maximum number of supported layers for the PUSCH.
- $\lceil \log_2 \left(\sum_{k=1}^{\min[L_{\max}, N_{\text{SRS}}]} \binom{N_{\text{SRS}}}{k} \right) \rceil$ bits for non-codebook based PUSCH transmission according to Tables 7.3.1.1.2-28/29/30/31, where N_{SRS} is the number of configured SRS resources in the SRS resource set associated with the higher layer parameter *SRS-SetUse* of value '*NonCodeBook*'; $\lceil \log_2(N_{\text{SRS}}) \rceil$ bits for codebook based PUSCH transmission according to Tables 7.3.1.1.2-32, where N_{SRS} is the number of configured SRS resources in the SRS resource set associated with the higher layer parameter *SRS-SetUse* of value '*CodeBook*'.
- Precoding information and number of layers – number of bits determined by the following:
 - 0 bits if the higher layer parameter *ulTxConfig = NonCodeBook*;

- 0 bits for 1 antenna port and if the higher layer parameter *ulTxConfig* = *Codebook*;
- 4, 5, or 6 bits according to Table 7.3.1.1.2-2 for 4 antenna ports, if *ulTxConfig* = *Codebook*, and according to the values of higher layer parameters *PUSCH-tp*, *ULmaxRank*, and *ULCodebookSubset*;
- 2, 4, or 5 bits according to Table 7.3.1.1.2-3 for 4 antenna ports, if *ulTxConfig* = *Codebook*, and according to the values of higher layer parameters *PUSCH-tp*, *ULmaxRank*, and *ULCodebookSubset*;
- 2 or 4 bits according to Table 7.3.1.1.2-4 for 2 antenna ports, if *ulTxConfig* = *Codebook*, and according to the values of higher layer parameters *ULmaxRank* and *ULCodebookSubset*;
- 1 or 3 bits according to Table 7.3.1.1.2-5 for 2 antenna ports, if *ulTxConfig* = *Codebook*, and according to the values of higher layer parameters *ULmaxRank* and *ULCodebookSubset*.
- Antenna ports – number of bits determined by the following
 - 2 bits as defined by Tables 7.3.1.1.2-6, if *PUSCH-tp*=*Enabled*, *UL-DMRS-config-type*=1, and *UL-DMRS-max-len*=1;
 - 4 bits as defined by Tables 7.3.1.1.2-7, if *PUSCH-tp*=*Enabled*, *UL-DMRS-config-type*=1, and *UL-DMRS-max-len*=2;
 - 3 bits as defined by Tables 7.3.1.1.2-8/9/10/11, if *PUSCH-tp*=*Disabled*, *UL-DMRS-config-type*=1, and *UL-DMRS-max-len*=1, and the value of rank is determined according to the SRS resource indicator field if *SRS-SetUse* = *NonCodeBook* and according to the Precoding information and number of layers field if *SRS-SetUse* = *CodeBook*;
 - 4 bits as defined by Tables 7.3.1.1.2-12/13/14/15, if *PUSCH-tp*=*Disabled*, *UL-DMRS-config-type*=1, and *UL-DMRS-max-len*=2, and the value of rank is determined according to the SRS resource indicator field if *SRS-SetUse* = *NonCodeBook* and according to the Precoding information and number of layers field if *SRS-SetUse* = *CodeBook*;
 - 4 bits as defined by Tables 7.3.1.1.2-16/17/18/19, if *PUSCH-tp*=*Disabled*, *UL-DMRS-config-type*=2, and *UL-DMRS-max-len*=1, and the value of rank is determined according to the SRS resource indicator field if *SRS-SetUse* = *NonCodeBook* and according to the Precoding information and number of layers field if *SRS-SetUse* = *CodeBook*;
 - 5 bits as defined by Tables 7.3.1.1.2-20/21/22/23, if *PUSCH-tp*=*Disabled*, *UL-DMRS-config-type*=2, and *UL-DMRS-max-len*=2, and the value of rank is determined according to the SRS resource indicator field if *SRS-SetUse* = *NonCodeBook* and according to the Precoding information and number of layers field if *SRS-SetUse* = *CodeBook*.

where the number of CDM groups without data of values 1, 2, and 3 in Tables 7.3.1.1.2-6 to 7.3.1.1.2-23 refers to CDM groups {0}, {0,1}, and {0, 1,2} respectively.

- SRS request – 2 bits as defined by Table 7.3.1.1.2-24 for UEs not configured with SUL in the cell; 3 bits for UEs configured SUL in the cell where the first bit is the non-SUL/SUL indicator as defined in Table 7.3.1.1.1-1 and the second and third bits are defined by Table 7.3.1.1.2-24. This bit field may also indicate the associated CSI-RS according to Subclause 6.1.1.2 of [6, TS 38.214].
- CSI request – 0, 1, 2, 3, 4, 5, or 6 bits determined by higher layer parameter *ReportTriggerSize*.
- CBG transmission information (CBGTI) – 0, 2, 4, 6, or 8 bits determined by higher layer parameter *maxCodeBlockGroupsPerTransportBlock* for PUSCH.
- PTRS-DMRS association – number of bits determined as follows
 - 0 bit if UL-PTRS-present=OFF and PUSCH-tp=Disabled, or if PUSCH-tp=Enabled;
 - 2 bits otherwise, where Table 7.3.1.1.2-25 and 7.3.1.1.2-26 are used to indicate the association between PTRS port(s) and DMRS port(s) for UL-PTRS-ports = 1 and UL-PTRS-ports = 2 respectively, and the DMRS ports are indicated by the Antenna ports field.
- beta_offset indicator – 0 if the higher layer parameter *dynamic* in *uci-on-PUSCH* is not configured; otherwise 2 bits as defined by Table 9.3-3 in [5, TS 38.213].

- DMRS sequence initialization – 0 if the higher layer parameter $PUSCH\text{-}tp=Enabled$ or 1 bit if the higher layer parameter $PUSCH\text{-}tp=Disabled$ for n_{SCID} selection defined in Subclause 7.4.1.1.1 of [4, TS 38.211].

The following information is transmitted by means of the DCI format 0_1 with CRC scrambled by CS-RNTI:

- XXX – x bit

The following information is transmitted by means of the DCI format 0_1 with CRC scrambled by SP-CSI-RNTI:

- XXX – x bit

For a UE configured with SUL in a cell, if PUSCH is configured to be transmitted on both the SUL and the non-SUL of the cell and if the number of information bits in format 0_1 for the SUL is not equal to the number of information bits in format 0_1 for the non-SUL, zeros shall be appended to smaller format 0_1 until the payload size equals that of the larger format 0_1.

Table 7.3.1.1.2-1: Bandwidth part indicator

Value of BWP indicator field		Bandwidth part	
1 bit	2 bits		
0	00	First bandwidth part configured by higher layers	
1	01	Second bandwidth part configured by higher layers	
	10	Third bandwidth part configured by higher layers	
	11	Fourth bandwidth part configured by higher layers	

Table 7.3.1.1.2-2: Precoding information and number of layers, for 4 antenna ports, if $PUSCH\text{-}tp=Disabled$ and $ULmaxRank = 2$ or 3 or 4

Bit field mapped to index	$ULCodebookSubset = fullAndPartialAndNonCoherent$	Bit field mapped to index	$ULCodebookSubset = partialAndNonCoherent$	Bit field mapped to index	$ULCodebookSubset = nonCoherent$
0	1 layer: TPMI=0	0	1 layer: TPMI=0	0	1 layer: TPMI=0
1	1 layer: TPMI=1	1	1 layer: TPMI=1	1	1 layer: TPMI=1
...
3	1 layer: TPMI=3	3	1 layer: TPMI=3	3	1 layer: TPMI=3
4	2 layers: TPMI=0	4	2 layers: TPMI=0	4	2 layers: TPMI=0
...
9	2 layers: TPMI=5	9	2 layers: TPMI=5	9	2 layers: TPMI=5
10	3 layers: TPMI=0	10	3 layers: TPMI=0	10	3 layers: TPMI=0
11	4 layers: TPMI=0	11	4 layers: TPMI=0	11	4 layers: TPMI=0
12	1 layer: TPMI=4	12	1 layer: TPMI=4	12-15	reserved
...		
19	1 layer: TPMI=11	19	1 layer: TPMI=11		
20	2 layers: TPMI=6	20	2 layers: TPMI=6		
...		
27	2 layers: TPMI=13	27	2 layers: TPMI=13		
28	3 layers: TPMI=1	28	3 layers: TPMI=1		
29	3 layers: TPMI=2	29	3 layers: TPMI=2		
30	4 layers: TPMI=1	30	4 layers: TPMI=1		
31	4 layers: TPMI=2	31	4 layers: TPMI=2		
32	1 layers: TPMI=12				
...	...				
47	1 layers: TPMI=27				
48	2 layers: TPMI=14				
...	...				
55	2 layers: TPMI=21				
56	3 layers: TPMI=3				
...	...				
59	3 layers: TPMI=6				
60	4 layers: TPMI=3				
61	4 layers: TPMI=4				
62-63	reserved				

Table 7.3.1.1.2-3: Precoding information and number of layers for 4 antenna ports, if *PUSCH-tp=Enabled*, or if *PUSCH-tp=Disabled* and *ULmaxRank = 1*

Bit field mapped to index	<i>ULCodebookSubset = fullAndPartialAndNonCoherent</i>	Bit field mapped to index	<i>ULCodebookSubset = partialAndNonCoherent</i>	Bit field mapped to index	<i>ULCodebookSubset = nonCoherent</i>
0	1 layer: TPMI=0	0	1 layer: TPMI=0	0	1 layer: TPMI=0
1	1 layer: TPMI=1	1	1 layer: TPMI=1	1	1 layer: TPMI=1
...
3	1 layer: TPMI=3	3	1 layer: TPMI=3	3	1 layer: TPMI=3
4	1 layer: TPMI=4	4	1 layer: TPMI=4		
...		
11	1 layer: TPMI=11	11	1 layer: TPMI=11		
12	1 layers: TPMI=12	12-15	reserved		
...	...				
27	1 layers: TPMI=27				
28-31	reserved				

Table 7.3.1.1.2-4: Precoding information and number of layers, for 2 antenna ports, if *PUSCH-tp=Disabled* and *ULmaxRank = 2*

Bit field mapped to index	<i>ULCodebookSubset = fullAndPartialAndNonCoherent</i>	Bit field mapped to index	<i>ULCodebookSubset = nonCoherent</i>
0	1 layer: TPMI=0	0	1 layer: TPMI=0
1	1 layer: TPMI=1	1	1 layer: TPMI=1
2	2 layers: TPMI=0	2	2 layers: TPMI=0
3	1 layer: TPMI=2	3	reserved
4	1 layer: TPMI=3		
5	1 layer: TPMI=4		
6	1 layer: TPMI=5		
7	2 layers: TPMI=1		
8	2 layers: TPMI=2		
9-15	reserved		

Table 7.3.1.1.2-5: Precoding information and number of layers, for 2 antenna ports, if *PUSCH-tp=Enabled*, or if *PUSCH-tp=Disabled* and *ULmaxRank = 1*

Bit field mapped to index	<i>ULCodebookSubset = fullAndPartialAndNonCoherent</i>	Bit field mapped to index	<i>ULCodebookSubset = nonCoherent</i>
0	1 layer: TPMI=0	0	1 layer: TPMI=0
1	1 layer: TPMI=1	1	1 layer: TPMI=1
2	1 layer: TPMI=2		
3	1 layer: TPMI=3		
4	1 layer: TPMI=4		
5	1 layer: TPMI=5		
6-7	reserved		

Table 7.3.1.1.2-6: Antenna port(s), *PUSCH-tp=Enabled*, *UL-DMRS-config-type=1*, *UL-DMRS-max-len=1*

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0
1	2	1
2	2	2
3	2	3

Table 7.3.1.1.2-7: Antenna port(s), *PUSCH-tp=Enabled*, *UL-DMRS-config-type=1*, *UL-DMRS-max-len=2*

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0	1
1	2	1	1
2	2	2	1
3	2	3	1
4	2	0	2
5	2	1	2
6	2	2	2
7	2	3	2
8	2	4	2
9	2	5	2
10	2	6	2
11	2	7	2
12-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-8: Antenna port(s), *PUSCH-tp=Disabled*, *UL-DMRS-config-type=1*, *UL-DMRS-max-len=1*, rank = 1

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0
1	1	1
2	2	0
3	2	1
4	2	2
5	2	3
6-7	Reserved	Reserved

Table 7.3.1.1.2-9: Antenna port(s), *PUSCH-tp=Disabled*, *UL-DMRS-config-type=1*, *UL-DMRS-max-len=1*, rank = 2

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0,1
1	2	0,1
2	2	2,3
3	2	0,2
4-7	Reserved	Reserved

Table 7.3.1.1.2-10: Antenna port(s), *PUSCH-tp=Disabled*, *UL-DMRS-config-type=1*, *UL-DMRS-max-len=1*, rank = 3

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0-2
2-7	Reserved	Reserved

Table 7.3.1.1.2-11: Antenna port(s), *PUSCH-tp=Disabled*, *UL-DMRS-config-type=1*, *UL-DMRS-max-len=1*, rank = 4

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0-3
2-7	Reserved	Reserved

Table 7.3.1.1.2-12: Antenna port(s), PUSCH-tp=Disabled, UL-DMRS-config-type=1, UL-DMRS-max-len=2, rank = 1

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0	1
1	1	1	1
2	2	0	1
3	2	1	1
4	2	2	1
5	2	3	1
6	2	0	2
7	2	1	2
8	2	2	2
9	2	3	2
10	2	4	2
11	2	5	2
12	2	6	2
13	2	7	2
14-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-13: Antenna port(s), PUSCH-tp=Disabled, UL-DMRS-config-type=1, UL-DMRS-max-len=2, rank = 2

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0,1	1
1	2	0,1	1
2	2	2,3	1
3	2	0,2	1
4	2	0,1	2
5	2	2,3	2
6	2	4,5	2
7	2	6,7	2
8	2	0,4	2
9	2	2,6	2
10-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-14: Antenna port(s), PUSCH-tp=Disabled, UL-DMRS-config-type=1, UL-DMRS-max-len=2, rank = 3

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0-2	1
1	2	0,1,4	2
2	2	2,3,6	2
3-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-15: Antenna port(s), PUSCH-tp=Disabled, UL-DMRS-config-type=1, UL-DMRS-max-len=2, rank = 4

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0-3	1
1	2	0,1,4,5	2
2	2	2,3,6,7	2
3	2	0,2,4,6	2
4-15	Reserved	Reserved	Reserved

Table 7.3.1.1.2-16: Antenna port(s), $PUSCH\text{-}tp=Disabled$, $UL\text{-}DMRS\text{-}config\text{-}type=2$, $UL\text{-}DMRS\text{-}max\text{-}len=1$, rank=1

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0
1	1	1
2	2	0
3	2	1
4	2	2
5	2	3
6	3	0
7	3	1
8	3	2
9	3	3
10	3	4
11	3	5
12-15	Reserved	Reserved

Table 7.3.1.1.2-17: Antenna port(s), $PUSCH\text{-}tp=Disabled$, $UL\text{-}DMRS\text{-}config\text{-}type=2$, $DL\text{-}DMRS\text{-}max\text{-}len=1$, rank=2

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0,1
1	2	0,1
2	2	2,3
3	3	0,1
4	3	2,3
5	3	4,5
6	2	0,2
7-15	Reserved	Reserved

Table 7.3.1.1.2-18: Antenna port(s), $PUSCH\text{-}tp=Disabled$, $UL\text{-}DMRS\text{-}config\text{-}type=2$, $DL\text{-}DMRS\text{-}max\text{-}len=1$, rank =3

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0-2
1	3	0-2
2	3	3-5
3-15	Reserved	Reserved

Table 7.3.1.1.2-19: Antenna port(s), $PUSCH\text{-}tp=Disabled$, $UL\text{-}DMRS\text{-}config\text{-}type=2$, $UL\text{-}DMRS\text{-}max\text{-}len=1$, rank =4

Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	2	0-3
1	3	0-3
2-15	Reserved	Reserved

Table 7.3.1.1.2-20: Antenna port(s), $PUSCH\text{-}tp=Disabled$, $UL\text{-}DMRS\text{-}config\text{-}type=2$, $UL\text{-}DMRS\text{-}max\text{-}len=2$, rank=1

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0	1
1	1	1	1
2	2	0	1
3	2	1	1
4	2	2	1
5	2	3	1
6	3	0	1
7	3	1	1
8	3	2	1
9	3	3	1
10	3	4	1
11	3	5	1
12	3	0	2
13	3	1	2
14	3	2	2
15	3	3	2
16	3	4	2
17	3	5	2
18	3	6	2
19	3	7	2
20	3	8	2
21	3	9	2
22	3	10	2
23	3	11	2
24	1	0	2
25	1	1	2
26	1	6	2
27	1	7	2
28-31	Reserved	Reserved	Reserved

Table 7.3.1.1.2-21: Antenna port(s), $PUSCH\text{-}tp=Disabled$, $UL\text{-}DMRS\text{-}config\text{-}type=2$, $UL\text{-}DMRS\text{-}max\text{-}len=2$, rank=2

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0,1	1
1	2	0,1	1
2	2	2,3	1
3	3	0,1	1
4	3	2,3	1
5	3	4,5	1
6	2	0,2	1
7	3	0,1	2
8	3	2,3	2
9	3	4,5	2
10	3	6,7	2
11	3	8,9	2
12	3	10,11	2
13	1	0,1	2
14	1	6,7	2
15	2	0,1	2
16	2	2,3	2
17	2	6,7	2
18	2	8,9	2
19-31	Reserved	Reserved	Reserved

Table 7.3.1.1.2-22: Antenna port(s), *PUSCH-tp=Disabled*, *UL-DMRS-config-type=2*, *UL-DMRS-max-len=2*, rank=3

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0-2	1
1	3	0-2	1
2	3	3-5	1
3	3	0,1,6	2
4	3	2,3,8	2
5	3	4,5,10	2
6-31	Reserved	Reserved	Reserved

Table 7.3.1.1.2-23: Antenna port(s), *PUSCH-tp=Disabled*, *UL-DMRS-config-type=2*, *UL-DMRS-max-len=2*, rank=4

Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	2	0-3	1
1	3	0-3	1
2	3	0,1,6,7	2
3	3	2,3,8,9	2
4	3	4,5,10,11	2
5-31	Reserved	Reserved	Reserved

Table 7.3.1.1.2-24: SRS request

Value of SRS request field	SRS resource set
00	
01	
10	
11	

Table 7.3.1.1.2-25: PTRS-DMRS association for UL PTRS port 0, *UL-PTRS-ports = 1*

Value	DMRS port
0	0
1	1
2	2
3	3

Table 7.3.1.1.2-26: PTRS-DMRS association for UL PTRS ports 0 and 1, *UL-PTRS-ports = 2*

Value of MSB	DMRS port	Value of LSB	DMRS port
0	1 st DMRS port transmitting layers corresponding to SRS port 0 and 2	0	1 st DMRS port transmitting layers corresponding to SRS port 1 and 3
1	2 nd DMRS port transmitting layers corresponding to SRS port 0 and 2	1	2 nd DMRS port transmitting layers corresponding to SRS port 1 and 3

Table 7.3.1.1.2-27: void

Table 7.3.1.1.2-28: SRI indication for non-codebook based PUSCH transmission, $L_{\max} = 1$

Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 2$	Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 3$	Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 4$
0	0	0	0	0	0
1	1	1	1	1	1
		2	2	2	2
		3	reserved	3	3

Table 7.3.1.1.2-29: SRI indication for non-codebook based PUSCH transmission, $L_{\max} = 2$

Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 2$	Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 3$	Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 4$
0	0	0	0	0	0
1	1	1	1	1	1
2	0,1	2	2	2	2
3	reserved	3	0,1	3	3
		4	0,2	4	0,1
		5	1,2	5	0,2
		6-7	reserved	6	0,3
				7	1,2
				8	1,3
				9	2,3
				10-15	reserved

Table 7.3.1.1.2-30: SRI indication for non-codebook based PUSCH transmission, $L_{\max} = 3$

Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 2$	Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 3$	Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 4$
0	0	0	0	0	0
1	1	1	1	1	1
2	0,1	2	2	2	2
3	reserved	3	0,1	3	3
		4	0,2	4	0,1
		5	1,2	5	0,2
		6	0,1,2	6	0,3
		7	reserved	7	1,2
				8	1,3
				9	2,3
				10	0,1,2
				11	0,1,3
				12	0,2,3
				13	1,2,3
				14-15	reserved

Table 7.3.1.1.2-31: SRI indication for non-codebook based PUSCH transmission, $L_{\max} = 4$

Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 2$	Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 3$	Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 4$
0	0	0	0	0	0
1	1	1	1	1	1
2	0,1	2	2	2	2
3	reserved	3	0,1	3	3
		4	0,2	4	0,1
		5	1,2	5	0,2
		6	0,1,2	6	0,3
		7	reserved	7	1,2
				8	1,3
				9	2,3
				10	0,1,2
				11	0,1,3
				12	0,2,3
				13	1,2,3
				14	0,1,2,3
				15	reserved

Table 7.3.1.1.2-32: SRI indication for codebook based PUSCH transmission

Bit field mapped to index	SRI(s), $N_{\text{SRS}} = 2$
0	0
1	1

Table 7.3.1.1.2-33: VRB-to-PRB mapping

Bit field mapped to index	VRB-to-PRB mapping
0	Non-interleaved
1	Interleaved

7.3.1.2 DCI formats for scheduling of PDSCH

7.3.1.2.1 Format 1_0

DCI format 1_0 is used for the scheduling of PDSCH in one DL cell.

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by C-RNTI:

- Identifier for DCI formats – 1 bits
- The value of this bit field is always set to 1, indicating a DL DCI format
- Frequency domain resource assignment – $\lceil \log_2(N_{\text{RB}}^{\text{DL,BWP}}(N_{\text{RB}}^{\text{DL,BWP}}+1)/2) \rceil$ bits
 - $N_{\text{RB}}^{\text{DL,BWP}}$ is the size of the initial bandwidth part in case DCI format 1_0 is monitored in the common search space
 - $N_{\text{RB}}^{\text{DL,BWP}}$ is the size of the active bandwidth part in case DCI format 1_0 is monitored in the UE specific search space and satisfying
 - the total number of different DCI sizes monitored per slot is no more than 4, and
 - the total number of different DCI sizes with C-RNTI monitored per slot is no more than 3
- Time domain resource assignment – X bits as defined in Subclause 5.1.2.1 of [6, TS 38.214]

- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.1.2-33
- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3 of [6, TS 38.214]
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.2
- HARQ process number – 4 bits
- Downlink assignment index – 2 bits as defined in Subclause 9.1.3 of [5, TS 38.213], as counter DAI
- TPC command for scheduled PUCCH – [2] bits as defined in Subclause 7.2.1 of [5, TS 38.213]
- PUCCH resource indicator – 3 bits as defined in Subclause 9.2.3 of [5, TS 38.213]
- PDSCH-to-HARQ_feedback timing indicator – [3] bits as defined in Subclause x.x of [5, TS38.213]

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by P-RNTI:

- Short Messages Indicator – 1 bit. This bit is used to indicate whether the short message only or scheduling information only is carried in the Paging DCI.

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by SI-RNTI:

- XXX – x bit

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by RA-RNTI:

- Identifier for DCI formats – 1 bit, reserved
- Frequency domain resource assignment – $\lceil \log_2(N_{\text{RB}}^{\text{DL,BWP}}(N_{\text{RB}}^{\text{DL,BWP}}+1)/2) \rceil$ bits
 - $N_{\text{RB}}^{\text{DL,BWP}}$ is the size of the initial bandwidth part in case DCI format 1_0 is monitored in the common search space in CORESET 0
 - $N_{\text{RB}}^{\text{DL,BWP}}$ is the size of the active bandwidth part in case DCI format 1_0 is monitored in the UE specific search space and satisfying
 - the total number of different DCI sizes monitored per slot is no more than 4, and
 - the total number of different DCI sizes with C-RNTI monitored per slot is no more than 3
- Time domain resource assignment – X bits as defined in Subclause 5.1.2.1 of [6, TS38.214]
- VRB-to-PRB mapping – 1 bit
- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1
- New data indicator – 1 bit, reserved
- Redundancy version – 2 bits, reserved
- HARQ process number – 4 bits, reserved
- Downlink assignment index – 2 bits, reserved
- TPC command for scheduled PUCCH – 2 bits, reserved
- PUCCH resource indicator – 3 bits, reserved
- PDSCH-to-HARQ_feedback timing indicator – 3 bits, reserved

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by TC-RNTI:

- Identifier for DCI formats – 1 bit

- The value of this bit field is always set to 1, indicating a DL DCI format
- Frequency domain resource assignment – $\lceil \log_2(N_{\text{RB}}^{\text{DL,BWP}}(N_{\text{RB}}^{\text{DL,BWP}}+1)/2) \rceil$ bits
 - $N_{\text{RB}}^{\text{DL,BWP}}$ is the size of the initial bandwidth part in case DCI format 1_0 is monitored in the common search space in CORESET 0
 - $N_{\text{RB}}^{\text{DL,BWP}}$ is the size of the active bandwidth part in case DCI format 0_0 is monitored in the UE specific search space and satisfying
 - the total number of different DCI sizes monitored per slot is no more than 4, and
 - the total number of different DCI sizes with C-RNTI monitored per slot is no more than 3
- Time domain resource assignment – X bits as defined in Subclause 5.1.2.1 of [6, TS38.214]
- VRB-to-PRB mapping – 1 bit
- Modulation and coding scheme – 5 bits as defined in Subclause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits
- Downlink assignment index – 2 bits, reserved
- TPC command for scheduled PUCCH – 2 bits as defined in Subclause 7.2.1 of [5, TS38.213]
- PUCCH resource indicator – 3 bits as defined in Subclause 9.2.3 of [5, TS38.213]
- PDSCH-to-HARQ_feedback timing indicator – 3 bits as defined in Subclause x.x of [5, TS38.213]

The following information is transmitted by means of the DCI format 1_0 with CRC scrambled by CS-RNTI:

- XXX – x bit

7.3.1.2.2 Format 1_1

DCI format 1_1 is used for the scheduling of PDSCH in one cell.

The following information is transmitted by means of the DCI format 1_1 with CRC scrambled by C-RNTI:

- Carrier indicator – 0 or 3 bits as defined in Subclause x.x of [5, TS 38.213].
- Identifier for DCI formats – 1 bits
 - The value of this bit field is always set to 1, indicating a DL DCI format
- Bandwidth part indicator – 0, 1 or 2 bits as defined in Table 7.3.1.1.2-1. The bitwidth for this field is determined as $\lceil \log_2(n_{\text{BWP}}) \rceil$ bits, where
 - $n_{\text{BWP}} = n_{\text{BWP,RRC}} + 1$ if the higher layer parameter *BandwidthPart-Config* configures up to 3 bandwidth parts and the initial bandwidth part is not included in higher layer parameter *BandwidthPart-Config*;
 - otherwise $n_{\text{BWP}} = n_{\text{BWP,RRC}}$;
 - $n_{\text{BWP,RRC}}$ is the number of configured bandwidth parts according to higher layer parameter *BandwidthPart-Config*.

- Frequency domain resource assignment – number of bits determined by the following, where $N_{\text{RB}}^{\text{DL,BWP}}$ is the size of the active bandwidth part:
 - N_{RBG} bits if only resource allocation type 0 is configured, where N_{RBG} is defined in Subclause 5.1.2.2.1 of [6, TS38.214],
 - $\lceil \log_2(N_{\text{RB}}^{\text{DL,BWP}}(N_{\text{RB}}^{\text{DL,BWP}}+1)/2) \rceil$ bits if only resource allocation type 1 is configured, or
 - $\max(\lceil \log_2(N_{\text{RB}}^{\text{DL,BWP}}(N_{\text{RB}}^{\text{DL,BWP}}+1)/2) \rceil, N_{\text{RBG}})+1$ bits if both resource allocation type 0 and 1 are configured.
- If both resource allocation type 0 and 1 are configured, the MSB bit is used to indicate resource allocation type 0 or resource allocation type 1, where the bit value of 0 indicates resource allocation type 0 and the bit value of 1 indicates resource allocation type 1.
- For resource allocation type 0, the N_{RBG} LSBs provide the resource allocation as defined in Subclause 5.1.2.2.1 of [6, TS 38.214].
- For resource allocation type 1, the $\lceil \log_2(N_{\text{RB}}^{\text{DL,BWP}}(N_{\text{RB}}^{\text{DL,BWP}}+1)/2) \rceil$ LSBs provide the resource allocation as defined in Subclause 5.1.2.2.2 of [6, TS 38.214]
- Time domain resource assignment – 0, 1, 2, 3, or 4 bits as defined in Subclause 5.1.2.1 of [6, TS 38.214]. The bitwidth for this field is determined as $\lceil \log_2(I) \rceil$ bits, where I is the number of entries in the higher layer parameter *pdsch-AllocationList*.
- VRB-to-PRB mapping – 0 or 1 bit:
 - 0 bit if only resource allocation type 0 is configured;
 - 1 bit according to Table 7.3.1.1.2-33 otherwise, only applicable to resource allocation type 1, as defined in Subclause xxx of [4, TS 38.211].
- PRB bundling size indicator – 0 bit if the higher layer parameter *PRB_bundling* is not configured or is set to 'static', or 1 bit if the higher layer parameter *PRB_bundling* is set to 'dynamic' according to Subclause 5.1.2.3 of [6, TS 38.214].
- Rate matching indicator – 0, 1, or 2 bits according to higher layer parameter *rate-match-PDSCH-resource-set*.
- ZP CSI-RS trigger – 0, 1, or 2 bits as defined in Subclause x.x of [6, TS 38.214]. The bitwidth for this field is determined as $\lceil \log_2(n_{\text{ZP}}+1) \rceil$ bits, where n_{ZP} is the number of ZP CSI-RS resource sets in the higher layer parameter *[ZP-CSI-RS-ResourceConfigList]*.

For transport block 1:

- Modulation and coding scheme – 5 bits as defined in Subclause x.x of [6, TS 38.214]
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2

For transport block 2 (only present if *Number-MCS-HARQ-DL-DCI* equals 2):

- Modulation and coding scheme – 5 bits as defined in Subclause x.x of [6, TS 38.214]
- New data indicator – 1 bit
- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2
- HARQ process number – 4 bits
- Downlink assignment index – number of bits as defined in the following

- 4 bits if more than one serving cell are configured in the DL and the higher layer parameter *HARQ-ACK-codebook=dynamic*, where the 2 MSB bits are the counter DAI and the 2 LSB bits are the total DAI;
 - 2 bits if only one serving cell is configured in the DL and the higher layer parameter *HARQ-ACK-codebook=dynamic*, where the 2 bits are the counter DAI;
 - 0 bits otherwise.
- TPC command for scheduled PUCCH – 2 bits as defined in Subclause x.x of [5, TS 38.213]
 - PUCCH resource indicator – 3 bits as defined in Subclause 9.2.3 of [5, TS 38.213]
 - PDSCH-to-HARQ_feedback timing indicator – 3 bits as defined in Subclause 9.2.3 of [5, TS 38.213]
 - Antenna port(s) – 4, 5, or 6 bits as defined by Tables 7.3.1.2.2-1/2/3/4, where the number of CDM groups without data of values 1, 2, and 3 refers to CDM groups {0}, {0,1}, and {0,1,2} respectively.
 - Transmission configuration indication – 0 bit if higher layer parameter *tci-PresentInDCI* is not enabled; otherwise 3 bits as defined in Subclause x.x of [6, TS38.214].
 - SRS request – 2 bits as defined by Table 7.3.1.1.2-24 for UEs not configured with SUL in the cell; 3 bits for UEs configured SUL in the cell where the first bit is the non-SUL/SUL indicator as defined in Table 7.3.1.1.1-1 and the second and third bits are defined by Table 7.3.1.1.2-24.
 - CBG transmission information (CBGTI) – 0, 2, 4, 6, or 8 bits as defined in Subclause x.x of [6, TS38.214], determined by higher layer parameter *maxCodeBlockGroupsPerTransportBlock* for the PDSCH.
 - CBG flushing out information (CBGFI) – 0 or 1 bit as defined in Subclause x.x of [6, TS38.214], determined by higher layer parameter *codeBlockGroupFlushIndicator*.
 - DMRS sequence initialization – 1 bit for n_{SCID} selection defined in Subclause 7.4.1.1.1 of [4, TS 38.211].

The following information is transmitted by means of the DCI format 1_1 with CRC scrambled by CS-RNTI:

- XXX – x bit

Table 7.3.1.2.2-1: Antenna port(s) (1000 + DMRS port), *DL-DMRS-config-type=1*, *DL-DMRS-max-len=1*

One Codeword: Codeword 0 enabled, Codeword 1 disabled		
Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0
1	1	1
2	1	0,1
3	2	0
4	2	1
5	2	2
6	2	3
7	2	0,1
8	2	2,3
9	2	0-2
10	2	0-3
11	2	0,2
12-15	Reserved	Reserved

Table 7.3.1.2.2-2: Antenna port(s) (1000 + DMRS port), DL-DMRS-config-type=1, DL-DMRS-max-len=2

One Codeword: Codeword 0 enabled, Codeword 1 disabled				Two Codewords: Codeword 0 enabled, Codeword 1 enabled			
Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols	Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0	1	0	2	0-4	2
1	1	1	1	1	2	0,1,2,3,4,6	2
2	1	0,1	1	2	2	0,1,2,3,4,5,6	2
3	2	0	1	3	2	0,1,2,3,4,5,6,7	2
4	2	1	1	4-31	reserved	reserved	reserved
5	2	2	1				
6	2	3	1				
7	2	0,1	1				
8	2	2,3	1				
9	2	0-2	1				
10	2	0-3	1				
11	2	0,2	1				
12	2	0	2				
13	2	1	2				
14	2	2	2				
15	2	3	2				
16	2	4	2				
17	2	5	2				
18	2	6	2				
19	2	7	2				
20	2	0,1	2				
21	2	2,3	2				
22	2	4,5	2				
23	2	6,7	2				
24	2	0,4	2				
25	2	2,6	2				
26	2	0,1,4	2				
27	2	2,3,6	2				
28	2	0,1,4,5	2				
29	2	2,3,6,7	2				
30	2	0,2,4,6	2				
31	Reserved	Reserve d	Reserved				

Table 7.3.1.2.2-3: Antenna port(s) (1000 + DMRS port), $DL\text{-}DMRS\text{-}config\text{-}type=2$, $DL\text{-}DMRS\text{-}max\text{-}len=1$

One codeword: Codeword 0 enabled, Codeword 1 disabled			Two codewords: Codeword 0 enabled, Codeword 1 enabled		
Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Value	Number of DMRS CDM group(s) without data	DMRS port(s)
0	1	0	0	3	0-4
1	1	1	1	3	0-5
2	1	0,1	2-31	reserved	reserved
3	2	0			
4	2	1			
5	2	2			
6	2	3			
7	2	0,1			
8	2	2,3			
9	2	0-2			
10	2	0-3			
11	3	0			
12	3	1			
13	3	2			
14	3	3			
15	3	4			
16	3	5			
17	3	0,1			
18	3	2,3			
19	3	4,5			
20	3	0-2			
21	3	3-5			
22	3	0-3			
23	2	0,2			
24-31	Reserved	Reserved			

Table 7.3.1.2.2-4: Antenna port(s) (1000 + DMRS port), DL-DMRS-config-type=2, DL-DMRS-max-len=2

One codeword: Codeword 0 enabled, Codeword 1 disabled				Two Codewords: Codeword 0 enabled, Codeword 1 enabled			
Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols	Value	Number of DMRS CDM group(s) without data	DMRS port(s)	Number of front-load symbols
0	1	0	1	0	3	0-4	1
1	1	1	1	1	3	0-5	1
2	1	0,1	1	2	2	0,1,2,3,6	2
3	2	0	1	3	2	0,1,2,3,6,8	2
4	2	1	1	4	2	0,1,2,3,6,7,8	2
5	2	2	1	5	2	0,1,2,3,6,7,8,9	2
6	2	3	1	6-63	Reserved	Reserved	Reserved
7	2	0,1	1				
8	2	2,3	1				
9	2	0-2	1				
10	2	0-3	1				
11	3	0	1				
12	3	1	1				
13	3	2	1				
14	3	3	1				
15	3	4	1				
16	3	5	1				
17	3	0,1	1				
18	3	2,3	1				
19	3	4,5	1				
20	3	0-2	1				
21	3	3-5	1				
22	3	0-3	1				
23	2	0,2	1				
24	3	0	2				
25	3	1	2				
26	3	2	2				
27	3	3	2				
28	3	4	2				
29	3	5	2				
30	3	6	2				
31	3	7	2				
32	3	8	2				
33	3	9	2				
34	3	10	2				
35	3	11	2				
36	3	0,1	2				
37	3	2,3	2				
38	3	4,5	2				
39	3	6,7	2				
40	3	8,9	2				
41	3	10,11	2				
42	3	0,1,6	2				
43	3	2,3,8	2				
44	3	4,5,10	2				
45	3	0,1,6,7	2				
46	3	2,3,8,9	2				
47	3	4,5,10,11	2				
48	1	0	2				
49	1	1	2				
50	1	6	2				
51	1	7	2				
52	1	0,1	2				
53	1	6,7	2				
54	2	0,1	2				
55	2	2,3	2				
56	2	6,7	2				

57 58-63	2 Reserved	8,9 Reserved	2 Reserved				
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7.3.1.3 DCI formats for other purposes

7.3.1.3.1 Format 2_0

DCI format 2_0 is used for notifying the slot format.

The following information is transmitted by means of the DCI format 2_0 with CRC scrambled by SFI-RNTI:

- Identifier for DCI formats – [1] bits
- Slot format indicator 1, Slot format indicator 2, ..., Slot format indicator N .

The size of DCI format 2_0 is configurable by higher layers up to 128 bits, according to Subclause 11.1.1 of [5, TS 38.213].

7.3.1.3.2 Format 2_1

DCI format 2_1 is used for notifying the PRB(s) and OFDM symbol(s) where UE may assume no transmission is intended for the UE.

The following information is transmitted by means of the DCI format 2_1 with CRC scrambled by INT-RNTI:

- Identifier for DCI formats – [1] bits
- Pre-emption indication 1, Pre-emption indication 2, ..., Pre-emption indication N .

The size of DCI format 2_1 is configurable by higher layers up to 126 bits, according to Subclause 11.2 of [5, TS 38.213]. Each pre-emption indication is 14 bits.

7.3.1.3.3 Format 2_2

DCI format 2_2 is used for the transmission of TPC commands for PUCCH and PUSCH.

The following information is transmitted by means of the DCI format 2_2 with CRC scrambled by TPC-PUSCH-RNTI or TPC-PUCCH-RNTI:

- Identifier for DCI formats – [1] bits
- TPC command number 1, TPC command number 2,..., TPC command number N

The parameter xxx provided by higher layers determines the index to the TPC command number for an UL of a cell. Each TPC command number is 2 bits.

If the number of information bits in format 2_2 is less than the payload size of format 0_0 as defined in the initial bandwidth part in the same serving cell, zeros shall be appended to format 2_2 until the payload size equals that of format 0_0 as defined in the initial bandwidth part in the same serving cell.

7.3.1.3.4 Format 2_3

DCI format 2_3 is used for the transmission of a group of TPC commands for SRS transmissions by one or more UEs. Along with a TPC command, a SRS request may also be transmitted.

The following information is transmitted by means of the DCI format 2_3 with CRC scrambled by TPC-SRS-RNTI:

- Identifier for DCI formats – [1] bits
- block number 1, block number 2, ..., block number B

where the starting position of a block is determined by the parameter *startingBitOfFormat2_3* provided by higher layers for the UE configured with the block.

For an UL without PUCCH and PUSCH or an UL on which the SRS power control is not tied with PUSCH power control, one block is configured for the UE by higher layers, with the following fields defined for the block:

- SRS request – 0 or 2 bits. The presence of this field is according to the definition in Subclause x.x of [5, TS38.213]. If present, this field is interpreted as defined by Table 7.3.1.1.2-5.
- TPC command number – 2 bits

If the number of information bits in format 2_3 is less than the payload size of format 0_0 as defined in the initial bandwidth part in the same serving cell, zeros shall be appended to format 2_3 until the payload size equals that of format 0_0 as defined in the initial bandwidth part in the same serving cell.

7.3.2 CRC attachment

Error detection is provided on DCI transmissions through a Cyclic Redundancy Check (CRC).

The entire payload is used to calculate the CRC parity bits. Denote the bits of the payload by $a_0, a_1, a_2, a_3, \dots, a_{A-1}$, and the parity bits by $p_0, p_1, p_2, p_3, \dots, p_{L-1}$, where A is the payload size and L is the number of parity bits. Let $a'_0, a'_1, a'_2, a'_3, \dots, a'_{A+L-1}$ be a bit sequence such that $a'_i=1$ for $i=0,1,\dots,L-1$ and $a'_i=a_{i-L}$ for $i=L,L+1,\dots,A+L-1$. The parity bits are computed with input bit sequence $a'_0, a'_1, a'_2, a'_3, \dots, a'_{A+L-1}$ and attached according to Subclause 5.1 by setting L to 24 bits and using the generator polynomial $g_{\text{CRC24C}}(D)$. The output bit $b_0, b_1, b_2, b_3, \dots, b_{B-1}$ is

$$b_k = a_k \quad \text{for } k=0,1,2,\dots,A-1$$

$$b_k = p_{k-A} \quad \text{for } k=A, A+1, A+2, \dots, A+L-1,$$

where $K=A+L$.

After attachment, the CRC parity bits are scrambled with the corresponding RNTI $x_{rnti,0}, x_{rnti,1}, \dots, x_{rnti,15}$, where $x_{rnti,0}$ corresponds to the MSB of the RNTI, to form the sequence of bits $c_0, c_1, c_2, c_3, \dots, c_{K-1}$. The relation between c_k and b_k is:

$$c_k = b_k \quad \text{for } k = 0, 1, 2, \dots, A+7$$

$$c_k = (b_k + x_{rnti,k-A-8}) \bmod 2 \quad \text{for } k = A+8, A+9, A+10, \dots, A+23.$$

7.3.3 Channel coding

Information bits are delivered to the channel coding block. They are denoted by $c_0, c_1, c_2, c_3, \dots, c_{K-1}$, where K is the number of bits, and they are encoded via Polar coding according to Subclause 5.3.1, by setting $n_{\max}=9$, $I_{BL}=1$, $n_{PC}=0$, and $n_{PC}^{wm}=0$.

After encoding the bits are denoted by $d_0, d_1, d_2, d_3, \dots, d_{N-1}$, where N is the number of coded bits.

7.3.4 Rate matching

The input bit sequence to rate matching is $d_0, d_1, d_2, \dots, d_{N-1}$.

Rate matching is performed according to Subclause 5.4.1 by setting $I_{BL}=0$.

The output bit sequence after rate matching is denoted as $f_0, f_1, f_2, \dots, f_{E-1}$.

Annex <A> (informative): Change history

Change history							
Date	Meeting	TDoc	CR	Rev	Cat	Subject/Comment	New version
2017-05	RAN1#89	R1-1707082				Draft skeleton	0.0.0
2017-07	AH_NR2	R1-1712014				Inclusion of LDPC related agreements	0.0.1
2017-08	RAN1#90	R1-1714564				Inclusion of Polar coding related agreements	0.0.2
2017-08	RAN1#90	R1-1714659				Endorsed version by RAN1#90 as basis for further updates	0.1.0
2017-09	RAN1#90	R1-1715322				Capturing additional agreements on LDPC and Polar code from RAN1 #90	0.1.1
2017-09	RAN#77	RP-171991				For information to plenary	1.0.0
2017-09	RAN1#90b	R1-1716928				Capturing additional agreements on LDPC and Polar code from RAN1 NR AH#3	1.0.1
2017-10	RAN1#90b	R1-1719106				Endorsed as v1.1.0	1.1.0
2017-11	RAN1#91	R1-1719225				Capturing additional agreements on channel coding, etc.	1.1.1
2017-11	RAN1#91	R1-1719245				Capturing additional agreements on DCI format, channel coding, etc.	1.1.2
2017-11	RAN1#91	R1-1721049				Endorsed as v1.2.0	1.2.0
2017-12	RAN1#91	R1-1721342				Capturing additional agreements on UCI, DCI, channel coding, etc.	1.2.1
2017-12	RAN#78	RP-172668				Endorsed version for approval by plenary.	2.0.0
2017-12	RAN#78					Approved by plenary – Rel-15 spec under change control	15.0.0
2018-03	RAN#79	RP-180200	0001	-	F	CR capturing the Jan18 ad-hoc and RAN1#92 meeting agreements	15.1.0
2018-04	RAN#79					MCC: correction of typo in DCI format 0_1 (time domain resource assignment) – higher layer parameter should be <i>pusch-AllocationList</i>	15.1.1